

SAW Components

SAW filter

Short range devices

Series/type: B3732

Ordering code: B39431B3732H110

Date: June 07, 2006

Version: 2.1

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SAW Components B3732

SAW filter 433.92 MHz

Data sheet



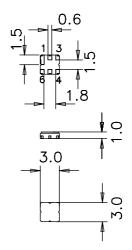
Application

- Low-loss RF filter for remote control receivers
- Balanced and unbalanced operation possible



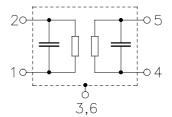
Features

- Package size 3.0 x 3.0 x 1.0 mm³
- Package code DCC6E
- RoHS compatible
- Approximate weight 0.037 g
- Package for Surface Mount Technology (SMT)
- Ni, gold-plated terminals
- Lead free soldering compatible with J STD20C
- Passivation layer Elpas
- AEC-Q200 qualified component family
- Electrostactic Sensitive Device (ESD)



Pin configuration¹⁾

- 1 Input (recommended) or input ground
- 2 Input ground (recommended) or input
- 4 Output (recommended) or output ground
- 5 Output ground (recommended) or output
- 3,6 To be grounded



The recommended pin configuration usually offers best suppression of electrical crosstalk. The filter characteristics refer to this configuration.



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Characteristics

Temperature range for specification: $T = -45 \,^{\circ}\text{C}$ to +95 $^{\circ}\text{C}$

Terminating source impedance: $Z_S = 50 \Omega$ and matching network Terminating load impedance: $Z_L = 50 \Omega$ and matching network

	min.	typ. @ 25 °C	max.	
Center frequency f _C	_	433.92	_	MHz
Minimum insertion attenuation α_{min}				
incl. loss in matching elements (Q ₁ = 47)	_	2.4	3.1	dB
excl. loss in matching elements	_	1.7	2.4	dB
Pass band (relative to α_{\min})				
433.77 434.07 MHz		0.6	2.0	dB
433.74 434.10 MHz		0.8	3.0	dB
433.70 434.14 MHz	_	1.2	6.0	dB
Filter bandwidth				
α _{rel} ≤ 3 dB	0.66	0.72	0.78	MHz
Relative attenuation (relative to α_{min}) α_{rel}				
10.00 414.00 MHz	55	60	_	dB
414.00 426.00 MHz	48	53	_	dB
426.00 430.50 MHz	24	28	_	dB
430.50 431.72 MHz	30	35	_	dB
431.72 431.97 MHz	40	52	<u> </u>	dB
431.97 432.07 MHz	30	52	<u> </u>	dB
432.07 432.12 MHz	25	52	<u> </u>	dB
432.12 433.10 MHz	15	18	<u> </u>	dB
434.92 444.00 MHz	15	20	_	dB
444.00 500.00 MHz	40	45	_	dB
500.00 810.00 MHz	50	55	_	dB
810.00 1500.00 MHz	60	50	_	dB
1500.00 2500.00 MHz	53	58	_	dB
Impedance for pass band matching ¹⁾				
Input: $Z_{IN} = R_{IN} C_{IN}$	_	240 3.0	_	$\Omega \parallel pF$
Output: $Z_{OUT} = R_{OUT} C_{OUT}$	_	240 3.0	_	Ω pF

¹⁾ Impedance for passband matching bases on an ideal, perfect matching of the SAW filter to source- and to load impedance (here 50 Ohm). After removal of the SAW filter the input impedance of the input and output matching network is calculated. The conjugate complex value of these characteristic impedances are the input and output impedances for flat passband. For more details we refer to EPCOS application note #4.



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Maximum ratings

Operable temperature range	Т	-45/+120	°C	
Storage temperature range	T_{stg}	-45/+120	°C	
DC voltage	V_{DC}	6	V	
Source power	P_S	10	dBm	source impedance 50 Ω



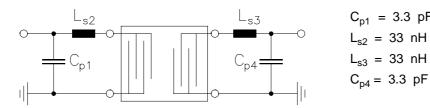
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Matching network to 50 Ω (element values depend on pcb layout and equivalent circuit)



$$C_{p1} = 3.3 pF$$

$$L_{s2} = 33 \text{ nH}$$

$$L_{e3} = 33 \text{ nH}$$

$$C_{n4} = 3.3 \text{ pF}$$

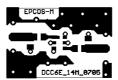
Minimising the crosstalk

For a good ultimate rejection a low crosstalk is necessary. Low crosstalk can be realised with a good RF layout. The major crosstalk mechanism is caused by the "ground-loop" problem.

Grounding loops are created if input-and output transducer GND are connected on the top-side of the PCB and fed to the system grounding plane by a common via hole. To avoid the common ground path, the ground pin of the input- and output transducer are fed to the system ground plane (bottom PCB plane) by their own via hole. The transducers' grounding pins should be isolated from the upper grounding plane.

A common GND inductivity of 0.5nH degrades the ultimate rejection (crosstalk) by 20dB.

The optimised PCB layout, including matching network for transformation to 50 Ohm, is shown here. In this PCB layout the grounding loops are minimised to realise good ultimate rejection



Optimised PCB layout for SAW filters in DCC6E package, pinning 1,4 (top side, scale 1:1)

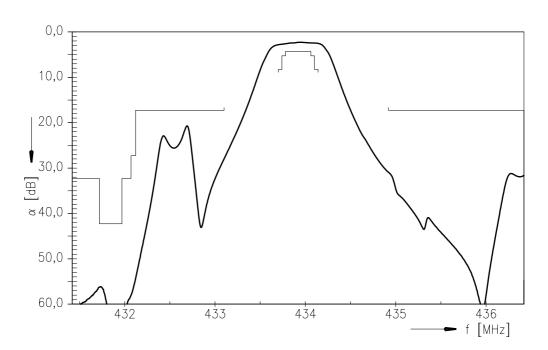
The bottom side is a copper plane (system ground area). The input and output grounding pins are isolated and connected to the common ground by separated via holes.

For good contact of the upper grounding area with the lower side it is necessary to place enough via holes.

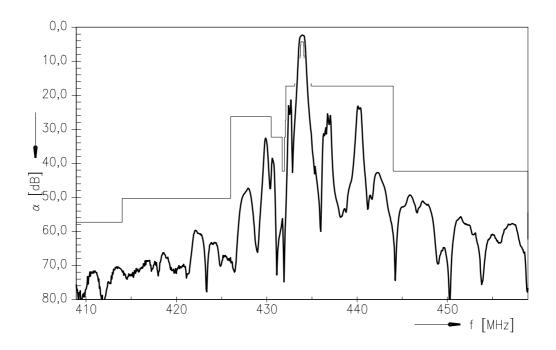


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Transfer function



Transfer function (wideband)





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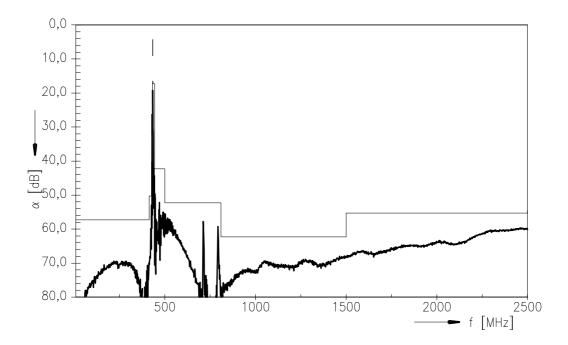
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Transfer function (ultimate rejection)





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References

Туре	B3732
Ordering code	B39431B3732H110
Marking and package	C61157-A7-A143
Packaging	F61074-V8168-Z000
Date codes	L_1126
S-parameters	B3732_SB.s2p B3732_WP.s2p
Soldering profile	S_6001
RoHS compatible	defined as compatible with the following documents: "DIRECTIVE 2002/95/EC OF THE EUROPEAN PARLIAMENT AND OF THE COUNCIL of 27 January 2003 on the restriction of the use of certain hazardous substances in electrical and electronic equipment. 2005/618/EC from April 18th, 2005, amending Directive 2002/95/EC of the European Parliament and of the Council for the purposes of establishing the maximum concentration values for certain hazardous substances in electrical and electronic equipment."

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