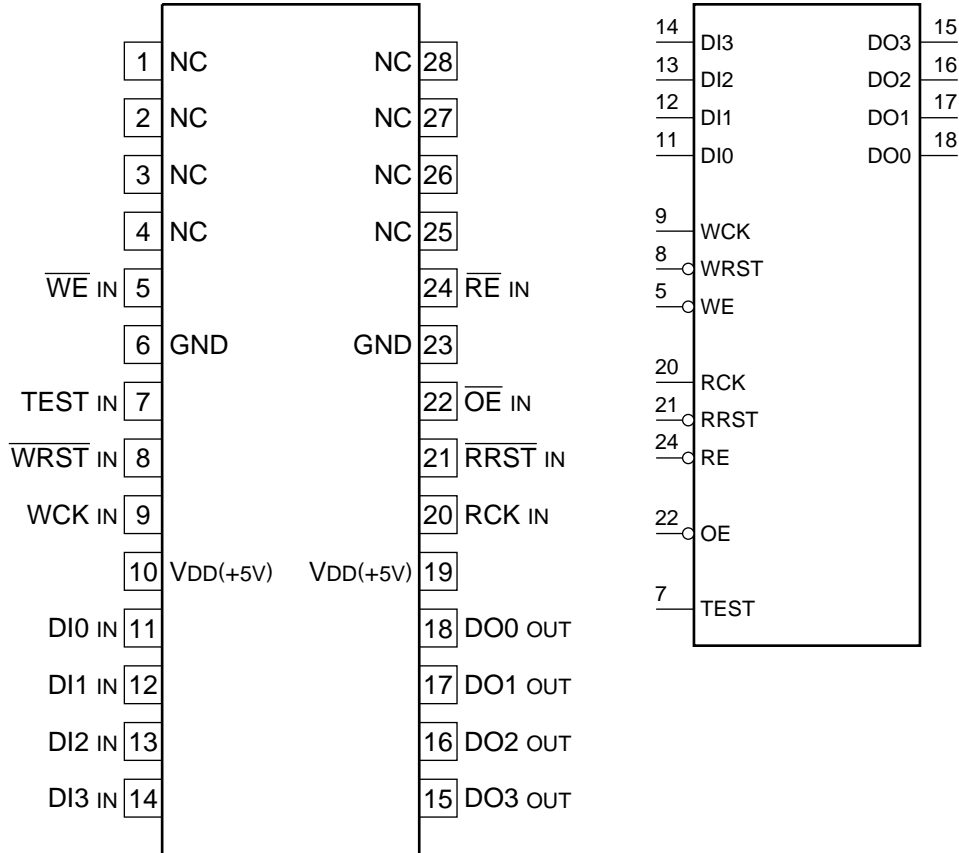

C-MOS 1M (262,144-WORD × 4)-BIT FIFO MEMORY

—TOP VIEW—

**INPUT**

DI0 - DI3 : DATA
 \overline{OE} : OUTPUT ENABLE
 RCK : READ CLOCK
 \overline{RE} : READ ENABLE
 \overline{RRST} : READ RESET
 TEST : TEST
 WCK : WRITE CLOCK
 \overline{WE} : WRITE ENABLE
 \overline{WRST} : WRITE RESET

OUTPUT

DO0 - DO3 : DATA

