



PJSD05MLTM

ESD PROTECTION DIODES

The PJSD05MLTM is designed to protect voltage sensitive components from ESD. Excellent clamping capability, low leakage, and fast response time provide best in class protection on designs that are exposed to ESD.

VOLTAGE 5 Volts **POWER** 90 Watts

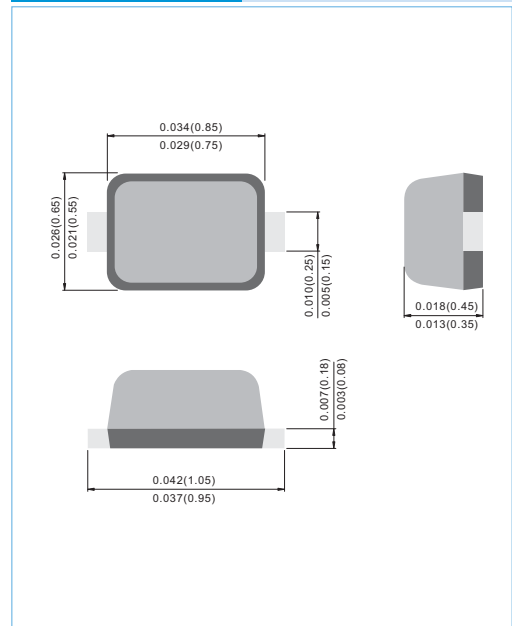
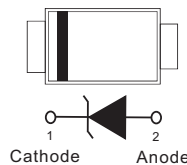
SOD-923 Unit: inch (mm)

FEATURES

- Low leakage
- In compliance with EU RoHS 2002/95/EC directives
- IEC61000-4-2 Level 4 ESD Protection
- IEC61000-4-4 Level 4 EFT Protection 80A ($t_p=5/50ns$)

MECHANICAL DATA

- Case: SOD-923, Molded Plastic
- Terminals: Solderable per MIL-STD-750, Method 2026
- Polarity: Color band cathode



MAXIMUM RATINGS

Rating	Symbol	Value	Units
Peak Power Dissipation 8/20 Pulse	P_{PM}	90	W
Lead Solder Temperature-Maximum (10 Second Duration)	T_L	260	°C
Operating Junction and Storage Temperature Range	T_J, T_{STG}	-55 to +150	°C

ELECTRICAL CHARACTERISTICS

($T_A=25^\circ\text{C}$ UNLESS OTHERWISE NOTED, $V_F=0.9\text{V Max.}@I_F=10\text{mA}$)

Device	$V_{RWM}(V)$	$I_R(\mu A)@V_{RWM}$	$V_{BR}(V) I_T$ (Note 1)	I_T	$V_C(V)@I_{PP}^{MAX}$	$I_{PP}(A)^+$	$P_{PK}(W)^+$	$C_J(pF)$	Marking Code
	Max	Max	Min	mA	Max	Max	Max	Typ	
PJSD05MLTM	5.0	1.0	6.2	1.0	10	9	90	65	R

NOTES:

*Other voltages available upon request

1. V_{BR} is measured with a pulse test current I_T at an ambient temperature of 25°C



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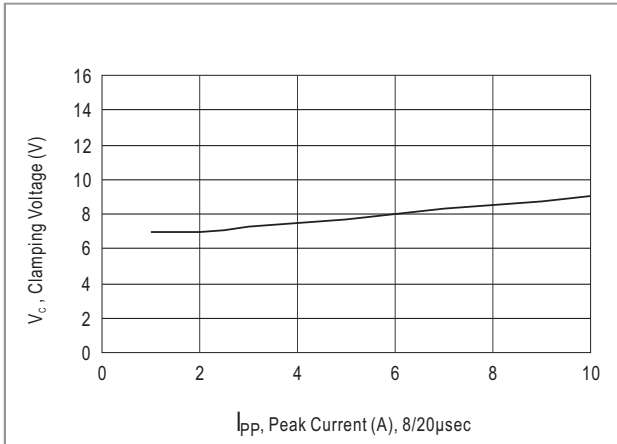


FIG. 1- Clamping Voltage

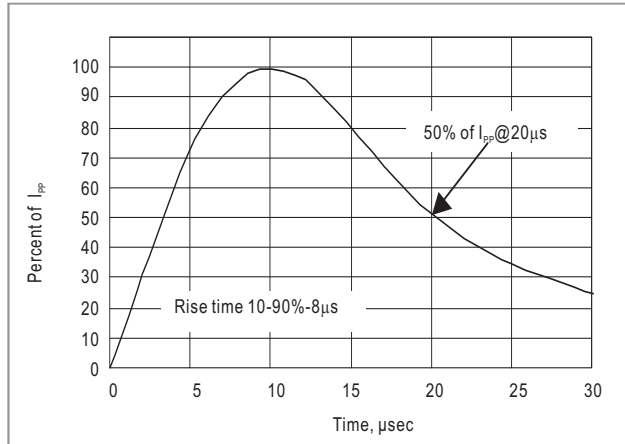


FIG. 2- Surge Pulse Waveform Definition

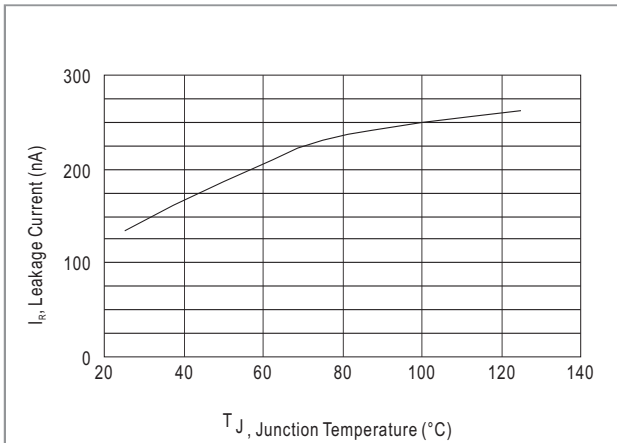


FIG. 3- Typical Reverse Characteristics

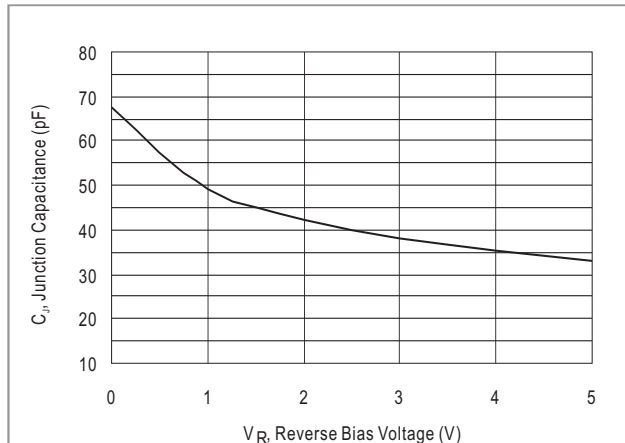
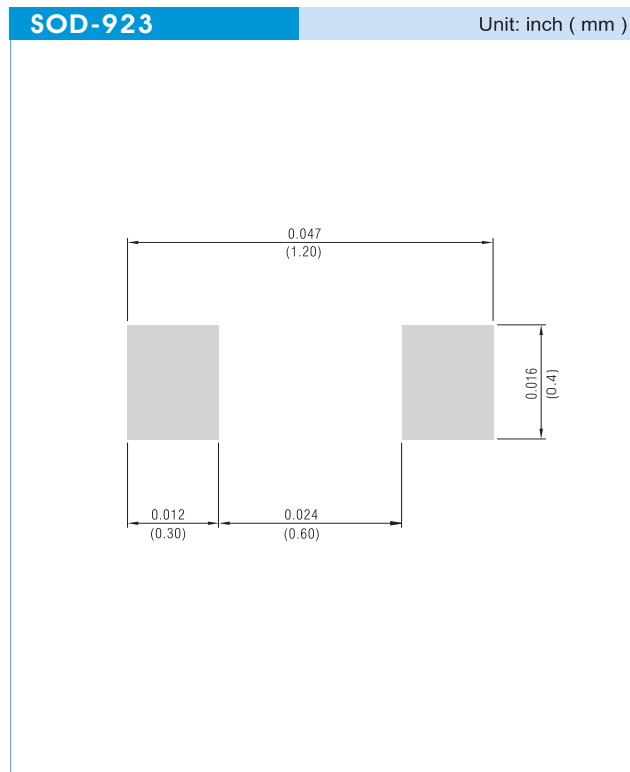


FIG. 4- Typical Junction Capacitance



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MOUNTING PAD LAYOUT



ORDER INFORMATION

- Packing information
T/R - 8K per 7" plastic Reel

LEGAL STATEMENT

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