



# CHENMKO ENTERPRISE CO.,LTD

**CH400DPT**

## SURFACE MOUNT

### SCHOTTKY BARRIER DIODE

VOLTAGE 40 Volts CURRENT 0.5 Ampere

Lead free devices

#### APPLICATION

\* High frequency rectification for switching power supply

#### FEATURE

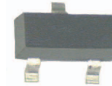
- \* Small surface mounting type. (SOT-23)
- \* Low IR. (IR=1uA Typ.)
- \* High reliability

#### CONSTRUCTION

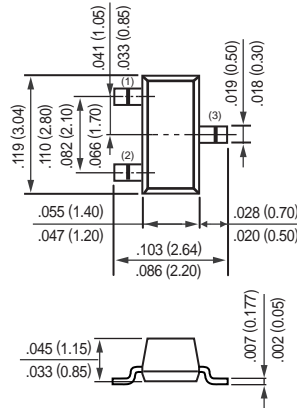
\* Silicon epitaxial planar

#### MARKING

\* 05F



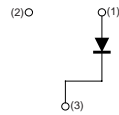
**SOT-23**



Dimensions in inches and (millimeters)

**SOT-23**

#### CIRCUIT



#### MAXIMUM RATINGS ( At TA = 25°C unless otherwise noted )

| RATINGS  | SYMBOL | CH400DPT    | UNITS |
|--|--------|-------------|-------|
| Maximum Recurrent Peak Reverse Voltage                       | VRRM   | 40          | Volts |
| Maximum RMS Voltage  | VRMS   | 28          | Volts |
| Maximum DC Blocking Voltage                                  | VDC    | 40          | Volts |
| Maximum Average Forward Rectified Current                    | Io     | 0.5         | Amps  |
| Peak Forward Surge Current at 8.3 mSec single half sine-wave | IFSM   | 3.0         | Amps  |
| Typical Junction Capacitance between Terminal (Note 1)       | CJ     | 20          | pF    |
| Maximum Operating Temperature Range                          | TJ     | +125        | °C    |
| Storage Temperature Range                                    | TSTG   | -40 to +125 | °C    |

#### ELECTRICAL CHARACTERISTICS ( At TA = 25°C unless otherwise noted )

| CHARACTERISTICS                                   | SYMBOL | CH400DPT | UNITS |
|---|--------|----------|-------|
| Maximum Instantaneous Forward Voltage at IF= 0.5A | VF     | 0.55     | Volts |
| Maximum Average Reverse Current at VR(1)= 30V     | IR(1)  | 50       | uAmps |
| Maximum Average Reverse Current at VR(2)= 10V     | IR(2)  | 30       | uAmps |

NOTES : 1. Measured at 1.0 MHz and applied reverse voltage of 10.0 volts.  
2. ESD sensitive product handling required.

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## RATING CHARACTERISTIC CURVES ( CH400DPT )

FIG. 1 - FORWARD CHARACTERISTICS

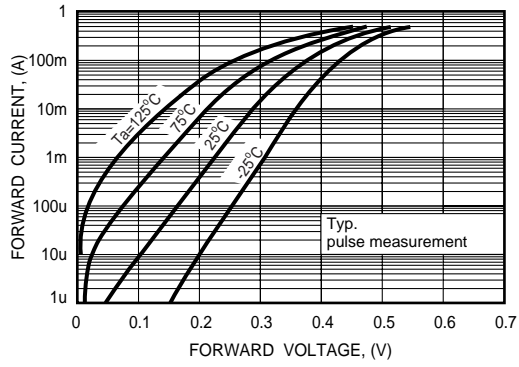


FIG. 2 - REVERSE CHARACTERISTICS

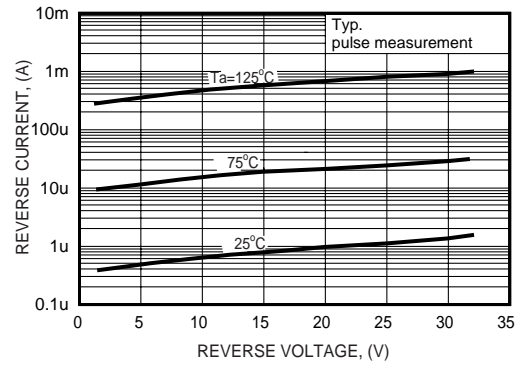


FIG. 3 - TYPICAL JUNCTION CAPACITANCE

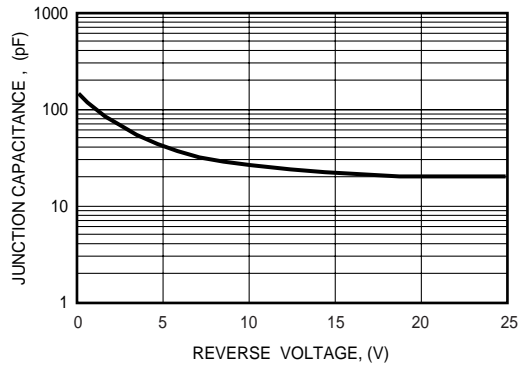


FIG. 4 - FORWARD POWER DISSIPATION CHARACTERISTICS

