SINGLE P-CHANNEL ENHANCEMENT MODE FIELD EFFECT TRANSISTOR

NEW PRODUCT

Features

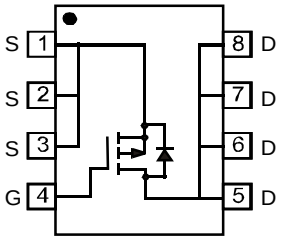
- Low On-Resistance
  - 7.5mΩ @ V_{GS} = -10V
  - 10.2mΩ @ V_{GS} = -4.5V
- Low Gate Threshold Voltage
- Low Input Capacitance
- Fast Switching Speed
- Low Input/Output Leakage
- **Lead Free By Design/RoHS Compliant (Note 2)**
- **"Green" Device (Note 4)**
- **Qualified to AEC-Q101 Standards for High Reliability**

Mechanical Data

- Case: SOP-8L
- Case Material: Molded Plastic, "Green" Molding Compound. UL Flammability Classification Rating 94V-0
- Moisture Sensitivity: Level 1 per J-STD-020D
- Terminals Connections: See Diagram
- Terminals: Finish - Matte Tin annealed over Copper lead frame. Solderable per MIL-STD-202, Method 208
- Marking Information: See Page 2
- Ordering Information: See Page 2
- Weight: 0.072g (approximate)

TOP VIEW

SOP-8L



TOP VIEW
Internal Schematic

Maximum Ratings @T_A = 25°C unless otherwise specified

Characteristic			Symbol	Value	Units
Drain-Source Voltage			V_{DSS}	-30	V
Gate-Source Voltage			V_{GSS}	±20	V
Drain Current (Note 1)	Steady State	T_A = 25°C T_A = 70°C	I_D	-10.5 -8.3	A
Pulsed Drain Current (Note 3)			I_{DM}	-40	A

Thermal Characteristics

Characteristic	Symbol	Value	Unit
Total Power Dissipation (Note 1)	P_D	2.5	W
Thermal Resistance, Junction to Ambient (Note 1)	$R_{\theta JA}$	50	°C/W
Operating and Storage Temperature Range	T_J, T_{STG}	-55 to +150	°C

Notes:
1. Device mounted on 1" x 1" 2 oz. Copper pads on 2" x 2" FR-4 PCB.
2. No purposefully added lead.
3. Pulse width ≤10μS, Duty Cycle ≤1%.
4. Diodes Inc.'s "Green" policy can be found on our website at http://www.diodes.com/products/lead_free/index.php.

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Electrical Characteristics @T_A = 25°C unless otherwise specified

Characteristic	Symbol	Min	Typ	Max	Unit	Test Condition
OFF CHARACTERISTICS (Note 5)						
Drain-Source Breakdown Voltage	BV_{DSS}	-30	—	—	V	V_{GS} = 0V, I_D = -250μA
Zero Gate Voltage Drain Current	I_{DSS}	—	—	-1	μA	V_{DS} = -30V, V_{GS} = 0V
Gate-Source Leakage	I_{GSS}	—	—	±1	μA	V_{GS} = ±20V, V_{DS} = 0V
ON CHARACTERISTICS (Note 5)						
Gate Threshold Voltage	$V_{GS(th)}$	-1.1	1.6	-2.1	V	V_{DS} = V_{GS}, I_D = -250μA
Static Drain-Source On-Resistance	$R_{DS\ (ON)}$	— —	6.3 7.9	7.5 10.2	mΩ	V_{GS} = -10V, I_D = -13A V_{GS} = -4.5V, I_D = -10A
Forward Transconductance	g_{fs}	—	26	—	S	V_{DS} = -15V, I_D = -13A
Diode Forward Voltage (Note 5)	V_{SD}	—	-0.7	-1.0	V	V_{GS} = 0V, I_S = -2.7A
DYNAMIC CHARACTERISTICS						
Input Capacitance	C_{iss}	—	4965	—	pF	V_{DS} = -15V, V_{GS} = 0V f = 1.0MHz
Output Capacitance	C_{oss}	—	1487	—	pF	
Reverse Transfer Capacitance	C_{rss}	—	711	—	pF	
Gate Resistance	R_G	—	7.3	—	Ω	V_{DS} = 0V, V_{GS} = 0V f = 1.0MHz
SWITCHING CHARACTERISTICS						
Total Gate Charge	Q_G	—	46	—	nC	V_{DS} = -15V, V_{GS} = -5V I_D = -13A
Gate-Source Charge	Q_{GS}	—	17	—		
Gate-Drain Charge	Q_{GD}	—	16	—		
Turn-On Delay Time	$t_{d(on)}$	—	15	—	ns	V_{DS} = -15V, V_{GS} = -10V, I_D = -1A, R_G = 6.0Ω
Rise Time	t_r	—	9	—		
Turn-Off Delay Time	$t_{d(off)}$	—	160	—		
Fall Time	t_f	—	66	—		

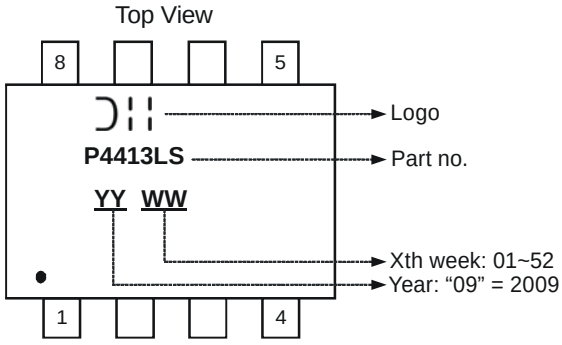
Notes: 5. Short duration pulse test used to minimize self-heating effect.

Ordering Information (Note 6)

Part Number	Case	Packaging
DMG4413LSS-13	SOP-8L	2500/Tape & Reel

Notes: 6. For packaging details, go to our website at http://www.diodes.com/datasheets/ap02007.pdf.

Marking Information



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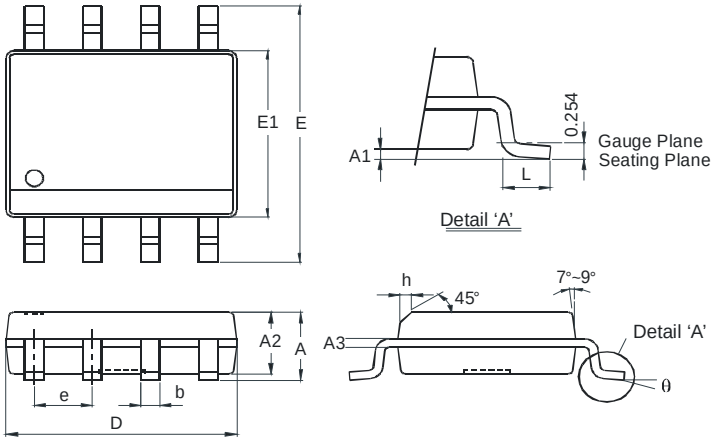
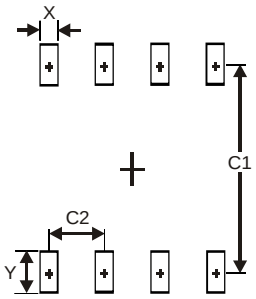
Package Outline Dimensions



SOP-8L		
Dim	**Min**	**Max**
A	-	1.75
A1	0.10	0.20
A2	1.30	1.50
A3	0.15	0.25
b	0.3	0.5
D	4.85	4.95
E	5.90	6.10
E1	3.85	3.95
e	1.27 Typ	
h	-	0.35
L	0.62	0.82
θ	0°	8°
All Dimensions in mm		

Suggested Pad Layout



Dimensions	Value (in mm)
X	0.60
Y	1.55
C1	5.4
C2	1.27

NEW PRODUCT

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