



**CHENMKO ENTERPRISE CO.,LTD**

**SURFACE MOUNT**

**P-Channel Enhancement Mode Field Effect Transistor**

**VOLTAGE 30 Volts CURRENT 4.1 Ampere**

**CHM2323PT**

*Lead free devices*

#### APPLICATION

- \* Servo motor control.
- \* Power MOSFET gate drivers.
- \* Other switching applications.

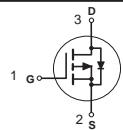
#### FEATURE

- \* Small flat package. (SC-59 )
- \* High density cell design for extremely low R<sub>DS(ON)</sub>.
- \* Rugged and reliable.
- \* High saturation current capability.

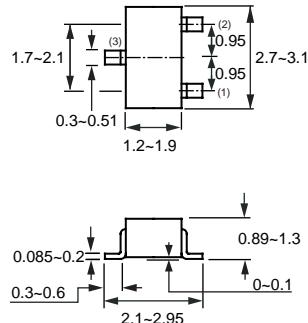
#### CONSTRUCTION

- \* P-Channel Enhancement

#### CIRCUIT



**SC-59/SOT-346**



**SC-59/SOT-346**

#### Absolute Maximum Ratings

T<sub>A</sub> = 25°C unless otherwise noted

Symbol	Parameter	CHM2323PT	Units
V <sub>DSS</sub>	Drain-Source Voltage	-30	V
V <sub>GSS</sub>	Gate-Source Voltage	±20	V
I <sub>D</sub>	Maximum Drain Current - Continuous	-4.1	A
	- Pulsed (Note 3)	-16	
P <sub>D</sub>	Maximum Power Dissipation	1250	mW
T <sub>J</sub>	Operating Temperature Range	-55 to 150	°C
T <sub>STG</sub>	Storage Temperature Range	-55 to 150	°C

Note : 1. Surface Mounted on FR4 Board , t <=10sec  
 2. Pulse Test , Pulse width <= 300us , Duty Cycle <= 2%  
 3. Repetitive Rating , Pulse width limited by maximum junction temperature  
 4. Guaranteed by design , not subject to production testing

#### Thermal characteristics

R <sub>θJA</sub>	Thermal Resistance, Junction-to-Ambient (Note 1)	100	°C/W
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2008-01

## RATING CHARACTERISTIC CURVES ( CHM2323PT )

**Electrical Characteristics**  $T_A = 25^\circ\text{C}$  unless otherwise noted

Symbol	Parameter	Conditions	Min	Typ	Max	Units
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### OFF CHARACTERISTICS

$BV_{DSS}$	Drain-Source Breakdown Voltage	$V_{GS} = 0 \text{ V}, I_D = -250 \mu\text{A}$	-30			V
$I_{DS(on)}$	Zero Gate Voltage Drain Current	$V_{DS} = -30 \text{ V}, V_{GS} = 0 \text{ V}$			-1	$\mu\text{A}$
$I_{GSSF}$	Gate-Body Leakage	$V_{GS} = 20 \text{ V}, V_{DS} = 0 \text{ V}$			+100	nA
$I_{GSSR}$	Gate-Body Leakage	$V_{GS} = -20 \text{ V}, V_{DS} = 0 \text{ V}$			-100	nA

### ON CHARACTERISTICS (Note 2)

$V_{GS(th)}$	Gate Threshold Voltage	$V_{DS} = V_{GS}, I_D = -250 \mu\text{A}$	-1.0		-3.0	V
$R_{DS(on)}$	Static Drain-Source On-Resistance	$V_{GS} = -10 \text{ V}, I_D = -4.1 \text{ A}$		40	48	$\text{m}\Omega$
		$V_{GS} = -4.5 \text{ V}, I_D = -3.2 \text{ A}$		60	80	
$g_{FS}$	Forward Transconductance	$V_{DS} = -15 \text{ V}, I_D = -4.1 \text{ A}$		4		S

### Dynamic Characteristics

$C_{iss}$	Input Capacitance	$V_{DS} = -15 \text{ V}, V_{GS} = 0 \text{ V}, f = 1.0 \text{ MHz}$		845		pF
$C_{oss}$	Output Capacitance			155		
$C_{rss}$	Reverse Transfer Capacitance			95		

### SWITCHING CHARACTERISTICS (Note 4)

$Q_g$	Total Gate Charge	$V_{DS} = -15 \text{ V}, I_D = -4.1 \text{ A}$ $V_{GS} = -10 \text{ V}$		13.8	18.3	nC
$Q_{gs}$	Gate-Source Charge			1.8		
$Q_{gd}$	Gate-Drain Charge			2.2		
$t_{on}$	Turn-On Time	$V_{DD} = -15 \text{ V}$ $I_D = -10 \text{ A}, V_{GS} = -10 \text{ V}$ $R_{GEN} = 6 \Omega$		11	22	nS
$t_r$	Rise Time			4	8	
$t_{off}$	Turn-Off Time			59	118	
$t_f$	Fall Time			23	46	

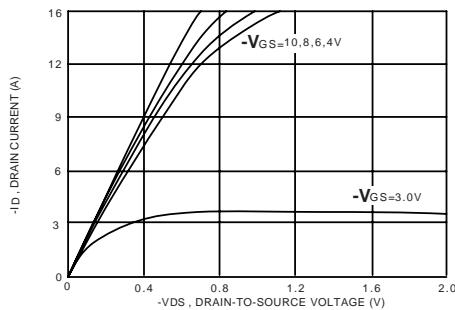
### DRAIN-SOURCE DIODE CHARACTERISTICS AND MAXIMUM RATINGS

$I_s$	Drain-Source Diode Forward Current	(Note 1)			-4.1	A
$V_{SD}$	Drain-Source Diode Forward Voltage	$I_s = -1.7 \text{ A}, V_{GS} = 0 \text{ V}$ (Note 2)			-1.2	V

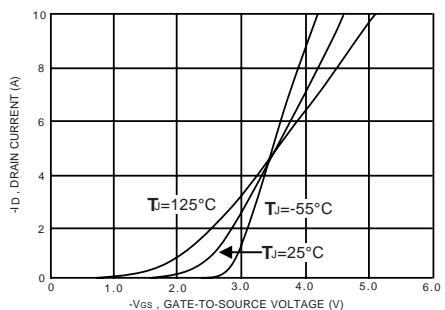
## RATING CHARACTERISTIC CURVES ( CHM2323PT )

### Typical Electrical Characteristics

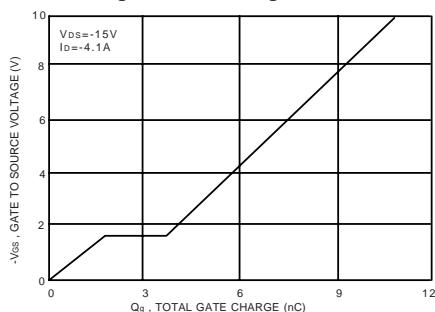
**Figure 1. Output Characteristics**



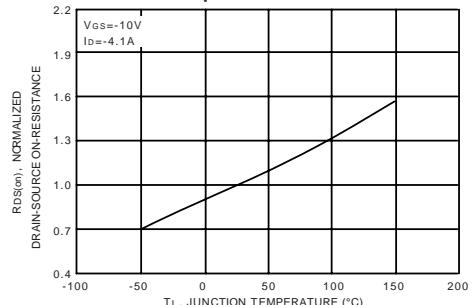
**Figure 2. Transfer Characteristics**



**Figure 3. Gate Charge**



**Figure 4. On-Resistance Variation with Temperature**



**Figure 5. Gate Threshold Variation with Temperature**

