



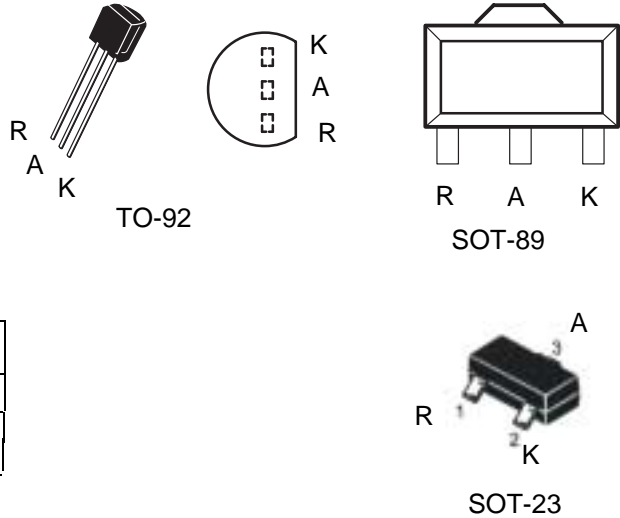
TL432A

Low Voltage Adjustable Precision Shunt Regulator

Features:

- Precise Reference Voltage to 1.24V
- Guaranteed 1% Reference Voltage Tolerance
- Sink Current Capability, 80 μ A to 100mA
- Quick Turn-on
- Adjustable Output Voltage, $V_o = V_{REF}$ to 18V
- 0.2 Ω Typical Output Impedance

Package and Pin Connections



Order Information

Order Number	Package
TL432ACPL	TO-92
TL432ACPK	SOT-89T&R
TL432ALT1	SOT-23T&R

Absolute Maximum Ratings

Symbol	Parameter	Rating	Unit
V_{KA}	Cathode voltage	18	V
I_K	Continuous cathode current range	100	mA
I_{REF}	Reference current range	3	mA
T_j	Operating Junction Temperature Range	- 40 to 150	$^{\circ}$ C

Electrical Characteristics $T_A = 25^{\circ}$ C (unless otherwise noted)

Symbol	Parameter	Test Conditions	TL432A			Unit
			Min	Typ	Max	
V_{REF}	Reference voltage	$V_{KA} = V_{REF}$, $I_K = 10\text{mA}$ (Fig. 1) $T_A = 25^{\circ}\text{C}$	1.228	1.240	1.252	V
V_{DEV}	V_{REF} Temp Deviation	$T_A = \text{full range}$ (see Note 1) $V_{KA} = V_{REF}$, $I_K = 10\text{mA}$ (Fig. 1)		10	25	mV
$\Delta V_{REF} / \Delta V_{KA}$	Ratio of Change in V_{REF} to Change in Cathode Voltage	$I_K = 10\text{mA}$, $V_{KA} = 18\text{V}$ to V_{REF} (Fig. 2)		-1	-2.7	mV / V
I_{REF}	Reference Input Current	$I_K = 10\text{mA}$, $R_1 = 10\text{k}\Omega$ $R_2 = \infty$ (Fig.2)		0.25	0.5	μ A
$I_{REF(DEV)}$	I_{REF} Temp Deviation	$T_K = \text{full range}$ (see Note 1), $R_1 = 10\text{k}\Omega$, $R_2 = \infty$, $I_K = 10\text{mA}$ (Fig. 2)		0.05	0.3	μ A
$I_{k(off)}$	Off-state cathode current	$V_{REF} = 0\text{V}$, (Fig.3) $V_K = 18\text{V}$		0.04	0.5	μ A
Z_{ka}	Dynamic Output Impedance	$V_{ka} = V_{ref}$, $I_k = 1\text{mA}$ to 100mA $F \leq 1\text{kHz}$ (Fig. 1)		0.2	0.4	Ω
$I_{K(MIN)}$	Minimum Operating Current	$V_{KA} = V_{REF}$ (Fig. 1)		60	80	μ A

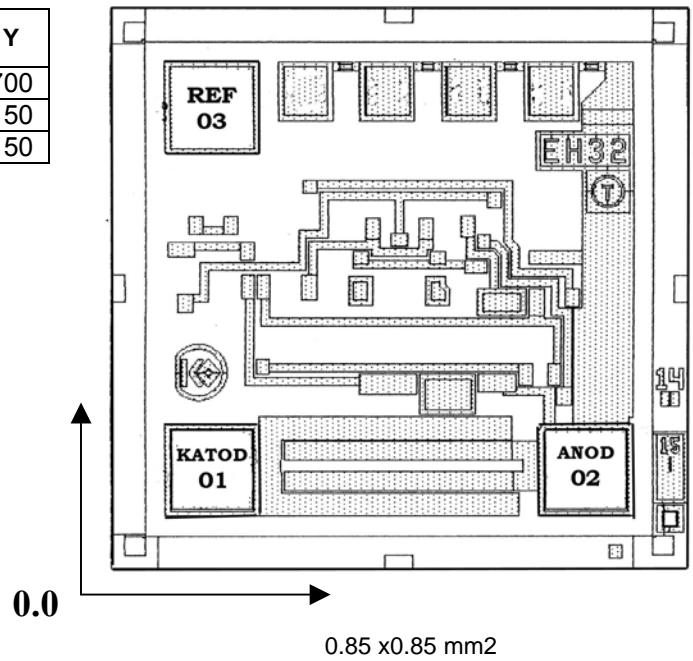
Notes: 1. Full temperature range is -40°C to 105°C for TL432



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Pad #	Pad Name	Description	Bond Pad (μm)	X	Y
1	K	CATHODE	130 x 130	150	700
2	A	ANODE	130 x 130	700	150
3	REF	REF	130 x 130	150	150



Test Figures

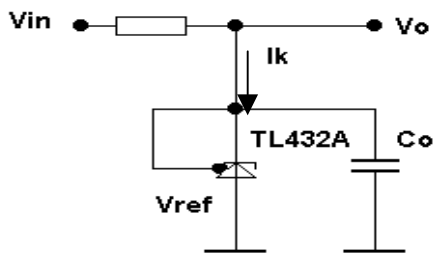


Figure 1. Test Circuit for $V_{ka}=V_{ref}$,
 $V_o=V_{ka}=V_{ref}$ $C_o=1\mu F^*$

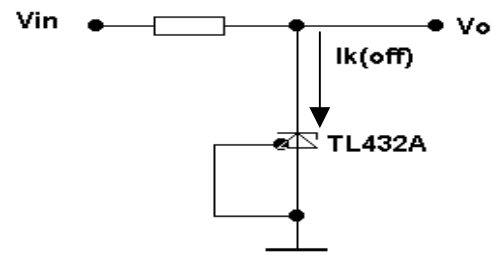


Figure 3. Test Circuit for $I_k(off)$

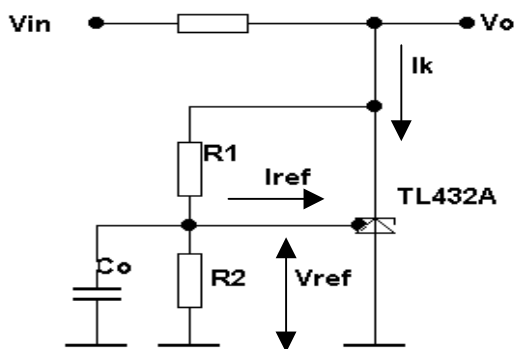


Figure 2. Test Circuit for $V_{ka}>V_{ref}$,
 $V_o=V_{ka}=V_{ref} * (1+R_1/R_2)+I_{ref1}*R_1$
 $C_o=1\mu F^*$