TOSHIBA Field Effect Transistor Silicon N Channel MOS Type (U-MOS IV)

TPC8041

Lithium Ion Battery Applications
Portable Equipment Applications
Notebook PC Applications

· Small footprint due to small and thin package

• Low drain-source ON-resistance: $RDS(ON) = 5.5 \text{ m}\Omega \text{ (typ.)}$

• High forward transfer admittance: $|Y_{fs}| = 26 \text{ S (typ.)}$

• Low leakage current: $IDSS = 10 \mu A (max) (VDS = 30 V)$

• Enhancement mode: $V_{th} = 1.3$ to 2.5 V ($V_{DS} = 10$ V, $I_{D} = 1$ mA)

Absolute Maximum Ratings (Ta = 25°C)

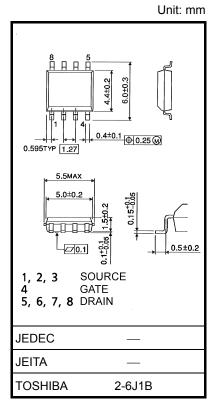
Characte	ristics	Symbol	Rating	Unit
Drain-source voltage	:	V_{DSS}	30	V
Drain-gate voltage (F	$R_{GS} = 20 \text{ k}\Omega$)	V_{DGR}	30	V
Gate-source voltage		V_{GSS}	±20	٧
Drain current	DC (Note 1)	ΙD	13	Α
Diam current	Pulse (Note 1)	I _{DP}	52	ζ
Drain power dissipati	on $(t = 10 s)$ (Note 2a)	P_{D}	1.9	W
Drain power dissipati	(Note 2b)	P _D	1.0	W
Single pulse avalanc	he energy (Note 3)	E _{AS}	44	mJ
Avalanche current		I _{AR}	13	Α
Repetitive avalanche	energy (Note 2a) (Note 4)	E _{AR}	0.066	mJ
Channel temperature)	T _{ch}	150	°C
Storage temperature range		T _{stg}	-55 to 150	°C

Note: Note 1, Note 2, Note 3 and Note 4: See the next page.

Using continuously under heavy loads (e.g. the application of high temperature/current/voltage and the significant change in

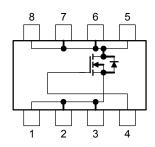
temperature, etc.) may cause this product to decrease in the reliability significantly even if the operating conditions (i.e. operating temperature/current/voltage, etc.) are within the absolute maximum ratings. Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook ("Handling Precautions"/"Derating Concept and Methods") and individual reliability data (i.e. reliability test report and estimated failure rate, etc).

This transistor is an electrostatic-sensitive device. Please handle with caution.



Weight: 0.08 g (typ.)

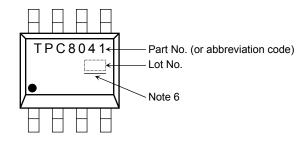
Circuit Configuration



Thermal Characteristics

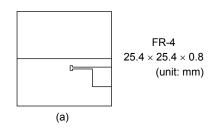
Characteristics	Symbol	Max	Unit
Thermal resistance, channel to ambient (t = 10 s) (Note 2a)	R _{th (ch-a)}	65.8	°C/W
Thermal resistance, channel to ambient $(t = 10 \text{ s})$ (Note 2b)	R _{th (ch-a)}	125	°C/W

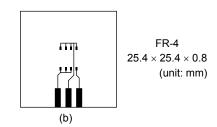
Marking (Note 5)



Note 1: Ensure that the channel temperature does not exceed 150°C.

Note 2: (a) Device mounted on a glass-epoxy board (b) Device mounted on a glass-epoxy board (b)



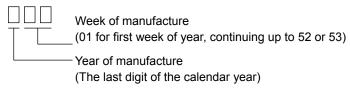


Note 3: $V_{DD} = 24 \text{ V}$, $T_{ch} = 25^{\circ}\text{C}$ (initial), L = 0.2 mH, $I_{AR} = 13 \text{ A}$

Note 4: Repetitive rating: pulse width limited by max channel temperature

Note 5: • on the lower left of the marking indicates Pin 1.

* Weekly code: (Three digits)



Note 6: A line under a Lot No. identifies the indication of product Labels.

Not underlined: [[Pb]]/INCLUDES > MCV

Underlined: [[G]]/RoHS COMPATIBLE or [[G]]/RoHS [[Pb]]

Please contact your TOSHIBA sales representative for details as to environmental matters such as the RoHS compatibility of Product. The RoHS is the Directive 2002/95/EC of the European Parliament and of the Council of 27 January 2003 on the restriction of the use of certain hazardous substances in electrical and electronic equipment.

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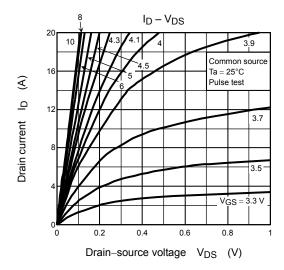
Electrical Characteristics (Ta = 25°C)

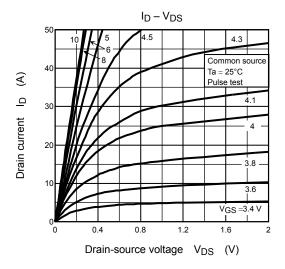
Cha	Characteristics		Test Condition	Min	Тур.	Max	Unit
Gate leakage cur	rent	I _{GSS}	$V_{GS} = \pm 20 \text{ V}, V_{DS} = 0 \text{ V}$	_	_	±100	nA
Drain cut-OFF cu	ırrent	I _{DSS}	V _{DS} = 30 V, V _{GS} = 0 V	_	_	10	μА
Drain-source bre	akdown voltago	V _{(BR) DSS}	$I_D = 10$ mA, $V_{GS} = 0$ V	30	_	_	V
Dialii-source bre	akdowii vollage	V _{(BR) DSX}	$I_D = 10 \text{ mA}, V_{GS} = -20 \text{ V}$	10	- +100 - 10 30	v	
Gate threshold vo	oltage	V _{th}	$V_{DS} = 10 \text{ V}, I_{D} = 1 \text{ mA}$	1.3	_	2.5	V
-		R _{DS} (ON)	V _{GS} = 4.5 V, I _D = 6.5 A	_	9	13.5	mΩ
Drain-source ON	rd transfer admittance		V _{GS} = 10 V, I _D = 6.5 A	_	5.5	7	
Forward transfer	admittance	Y _{fs}	V _{DS} = 10 V, I _D = 6.5 A	13	26	_	S
Input capacitance	9	C _{iss}		_	1270	_	pF
Reverse transfer	capacitance	C _{rss}	V _{DS} = 10 V, V _{GS} = 0 V, f = 1 MHz	_	240	_	
Output capacitance		C _{oss}		_	380	_	
Rise	Rise time	t _r	, 10 V ∏ I _D = 6.5 A	_	11	_	
Cuitabing time	Turn-ON time	t _{on}	V _{GS} 10 V I _D = 6.5 A V _{OUT} C C C C C C C C C	_	20	_	20
_	Fall time	tf	4.7 Ω 4.7 Ω 4.7 Ω 8.1 = 2:3	_	15	_	- ns
	Turn-OFF time	t _{off}	V _{DD} ≈ 15 V Duty ≤ 1%, t _W = 10 μs	_	39	_	
Total gate charge (gate-source plus	otal gate charge gate-source plus gate-drain)		$V_{DD} \approx 24 \text{ V}, V_{GS} = 10 \text{ V}, I_D = 13 \text{ A}$	_	27	_	nC
Gate-source charge 1		Q _{gs1}		_	4.2	_	
Gate-drain ("miller") charge		Q _{gd}		_	8.2	_	

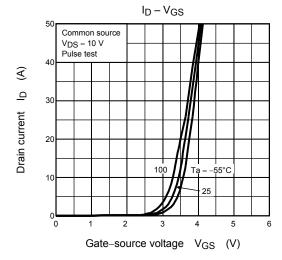
Source-Drain Ratings and Characteristics (Ta = 25°C)

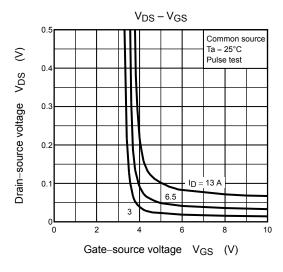
Characteristics		Symbol	Test Condition	Min	Тур.	Max	Unit	
Drain reverse current	Pulse	(Note 1)	I_{DRP}	_	_	_	52	Α
Forward voltage (diode)			V_{DSF}	$I_{DR} = 13 \text{ A}, V_{GS} = 0 \text{ V}$	_	_	-1.2	V

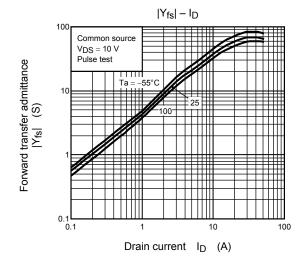
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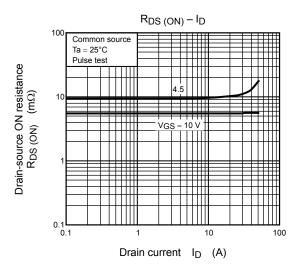


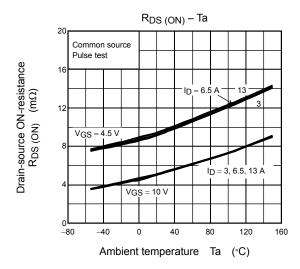


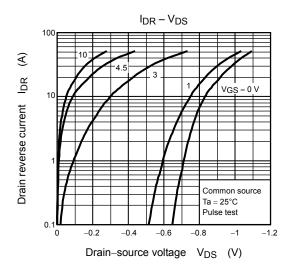


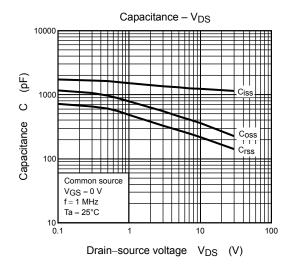


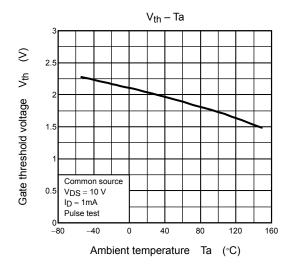


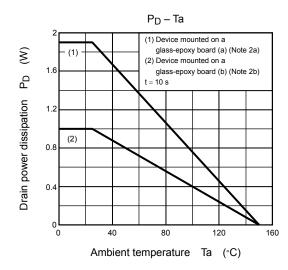


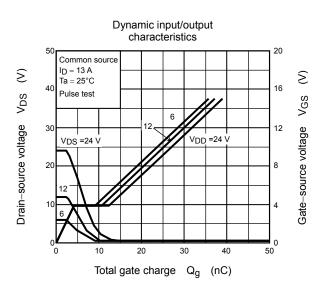


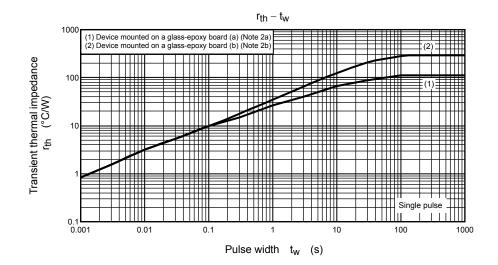


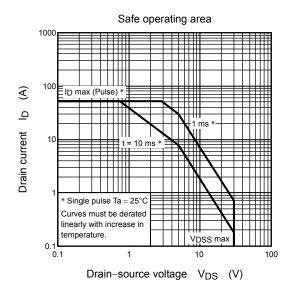












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