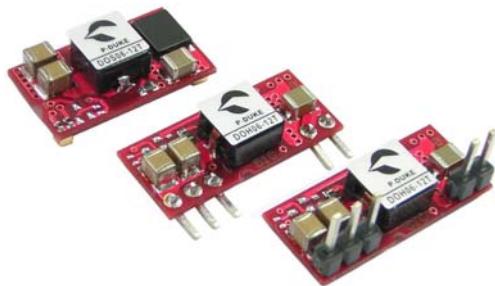




**POWER MATE
TECHNOLOGY CO.,LTD.**

DOS06-12T Non-isolated
DOH06-12T Point of load DC/DC converters



APPLICATIONS

Wireless Network
Telecom/Datacom
Industry Control System
Distributed Power Architectures
Semiconductor Equipment
Microprocessor Power Applications

FEATURES

- OUTPUT CURRENT UP TO 6A
- SMALL SIZE AND LOW PROFILE :
0.80" X 0.45" X 0.25" (SMD) ; 0.90" X 0.40" X 0.24" (SIP)
- HIGH EFFICIENCY - 89% @ 3.3V FULL LOAD
- INPUT RANGE FROM 8.3VDC TO 14.0VDC
- FIXED SWITCHING FREQUENCY (300KHZ)
- SMD & SIP PACKAGES
- OUTPUT VOLTAGE PROGRAMMABLE FROM 0.75VDC TO 5.0VDC VIA EXTERNAL RESISTOR
- INPUT UNDER-VOLTAGE LOCKOUT
- UL60950-1, EN60950-1 AND IEC60950-1 LICENSED
- ISO9001 CERTIFIED MANUFACTURING FACILITIES
- COMPLIANT TO RoHS EU DIRECTIVE 2002/95/EC

OPTIONS

Positive Logic Remote on/off

DESCRIPTION

DOS06-12T (SMD type), DOH06-12T (for Vertical Mounting SIP type) and DOH06-12TA (for Horizontal Mounting SIP type) are non-isolated DC/DC converters that can deliver up to 6A of output current with full load efficiency of 89% at 3.3V output.

TECHNICAL SPECIFICATION

All specifications are typical at nominal input, full load and 25°C otherwise noted

OUTPUT SPECIFICATIONS		
Output current	6A max	
Voltage accuracy	Full load and Vin(nom)	± 2%Vo(set)
Minimum load		0%
Line regulation	Vin=Vin(min) to Vin(max) at Full Load	± 0.3%Vo(set),typ
Load regulation	No Load to Full Load	± 0.4%Vo(set),typ
Ripple and noise (Note2)	20MHz bandwidth	20mVrms,max 50mVp-p,max
Temperature coefficient		±0.4%, typ
Dynamic load response (Note 2)	△Io / △t = 2.5A/μS ,Vin(nom) Load change step (50% to 100% or 100% to 50% of Io(max))	Peak deviation 200mV,typ Setting time (Vo<10%peak deviation) 25μS,typ
Dynamic load response (Note 3)	△Io / △t = 2.5A/μS ,Vin(nom) Load change step (50% to 100% or 100% to 50% of Io(max))	Peak deviation 50mV,typ Setting time (Vo<10%peak deviation) 50μS,typ
Output current limit		200%,typ
Output short-circuit current		Hiccup, automatics recovery
External load capacitance	ESR≥1mΩ ESR≥10mΩ	1000μF,max 3000μF,max
Output voltage overshoot-startup F.L.	Vin=Vin(min) to Vin(max);	1%Vo(set)
Voltage adjustability (see fig.1) (Note 4)	(Note 4)	0.7525V ~ 5.0V
GENERAL SPECIFICATIONS		
Efficiency	See table	
Isolation voltage		None
Switching frequency		300KHz, typ
Approvals and standard	IEC60950-1, UL60950-1, EN60950-1	
Dimensions	SMD SIP	0.80 X 0.45 X 0.25 Inch (20.3 X 11.4 X 6.5 mm) 0.90 X 0.40 X 0.24 Inch (22.9 X 10.2 X 6.0 mm)
Weight		2.8g(0.1oz)
MTBF (Note 1)	BELLCORE TR-NWT-000332 MIL-HDBK-217F	1.990 x 10 ⁷ hrs 3.007 x 10 ⁶ hrs

INPUT SPECIFICATIONS		
Input voltage range	Vo(set) ≤3.63V Vo(set) >3.63V	Vin(nom) =12V 8.3 – 14VDC 8.3 – 13.2VDC
Maximum input current	Vin=Vin(min); Io=Io(max)	4.5A
Input filter (Note 5)		C filter
Input no load current (Vin=12V, Io=0, module enabled)	Vo(set) =0.75Vdc Vo(set) =5.0Vdc	17mA,typ 100mA,typ
Input under voltage lockout	Start-up voltage Shutdown voltage	7.9V,typ 7.8V,typ
Input reflected ripple current	5~20MHz, 1μH source impedance	30mA-p-p
ENVIRONMENTAL SPECIFICATIONS		
Operating ambient temperature		-40°C ~ +85°C(with derating)
Storage temperature range		-55°C ~ +125°C
Thermal shock		MIL-STD-810F
Over temperature protection		140°C,typ
FEATURE SPECIFICATIONS		
Remote ON/OFF(Note 6)		
Negative logic(standard)	ON = 0V < Vr < 0.3V OFF =2.5V < Vr < Vin(max)	I _{IN} =10μA,max I _{IN} =1mA,max
Positive logic(option)	ON = (Vin-4) < Vr < Vin(max) OFF=0V < Vr < 0.3V	I _{IN} =10μA,max I _{IN} =1mA,max
Input current of Remote control pin		10μA~1.0mA
Remote off state input current	Nominal Vin	1.2mA,typ
Rise time	Time for Vo to rise from 10% to 90%of Vo(set)	6ms,max.
Turn-on delay time	Case 1 (Note 7) Case 2 (Note 8)	3ms,typ 3ms,typ





Model Name	ON/OFF Logic	Package	Input Voltage	Output Voltage	Output Current		Efficiency (%) 12Vin, 3.3Vdc@6A
					Min. Load	Max. Load	
DOS06-12T	Negative	SMD	V _o (set) ≤ 3.63V Vin = 8.3-14Vdc	0.75 ~ 5.0Vdc	0A	6A	89%
DOS06-12T-P	Positive						
DOH06-12T	Negative	Vertical Mounting SIP	V _o (set) > 3.63V Vin = 8.3-13.2Vdc				
DOH06-12T-P	Positive						
DOH06-12TA	Negative	Horizontal Mounting SIP					
DOH06-12TA-P	Positive						

Note

1. BELLCORE TR-NWT-000332. Case 1: 50% Stress, Temperature at 40°C.
MIL-HDBK-217F Notice2 @Ta=25 °C, Full load(Ground, Benign, controlled environment).
2. External with C_{out} = 1μF ceramic//10μF tantalum capacitors.
3. External with C_{out} = 2x150μF polymer capacitors.
4. Output voltage programmable from 0.7525V to 5V by connecting a single resistor (shown as Rtrim in Table 1) between the TRIM and GND pins of the module. To calculate the value of the resistor **Rtrim** for a particular output voltage **V_o**, use the following equation:

$$R_{trim} = \left[\frac{10500}{V_o - 0.7525} - 1000 \right] \Omega$$

5. It's necessary to equip the external input capacitors at the input of the module. The capacitors should connect as close as possible to the input terminals that ensuring module stability. The external C_{in} is 2x47μF ceramic capacitors at least.
6. Device code with suffix “-P” – Positive logic(On/Off is open collector/drain logic input; Signal referenced to GND)
Device code with no suffix – Negative logic (On/Off pin is open collector/drain logic input with external pull –up resistor; signal referenced to GND)
7. Case 1 :On/Off input is set to logic low (module on) and then input power is applied (delay from instant at which Vin=Vin(min) until V_o=10% of V_o(set))
8. Case 2 :Input power is applied for at least one second and then the On/Off input is set to logic low (delay form instant at which Von/off=0.3V until V_o=10% of V_o(set))

CAUTION: This power module is not internally fused. An input line fuse must always be used.

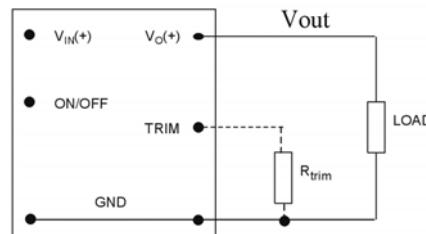
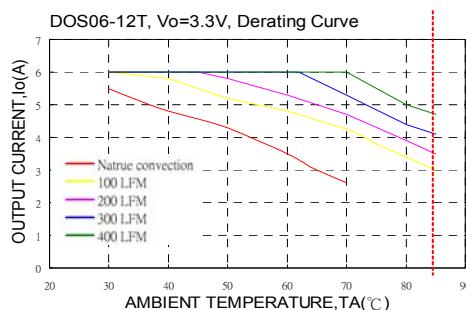


Fig. 1

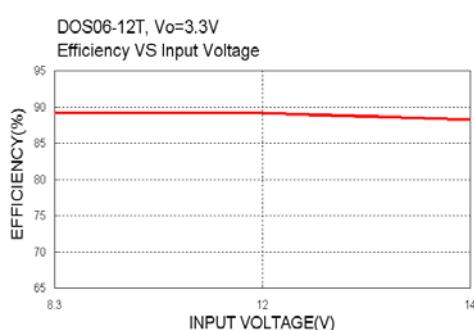
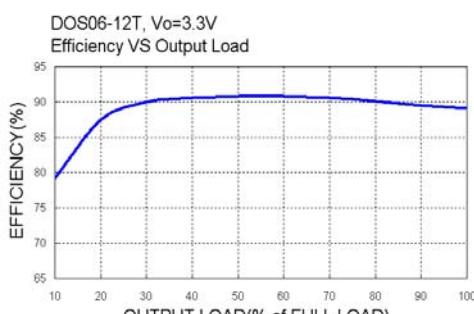


Table 1	
V _o (set) (V)	R _{trim} (KΩ)
0.7525	Open
1.2	22.46
1.5	13.05
1.8	9.024
2.5	5.009
3.3	3.122
5	1.472

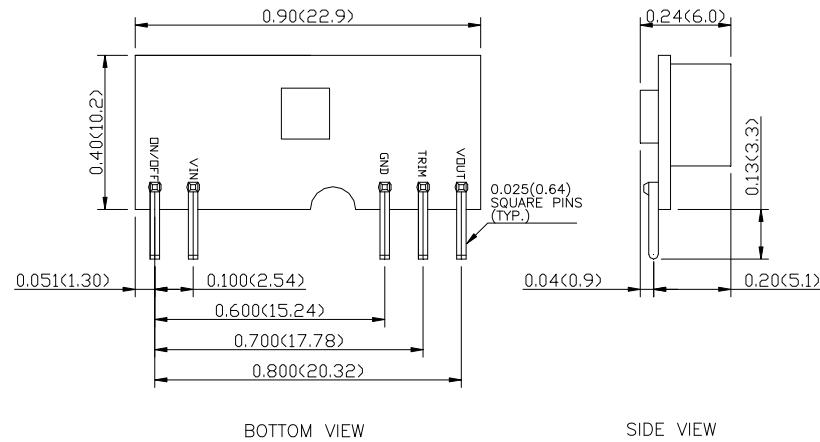




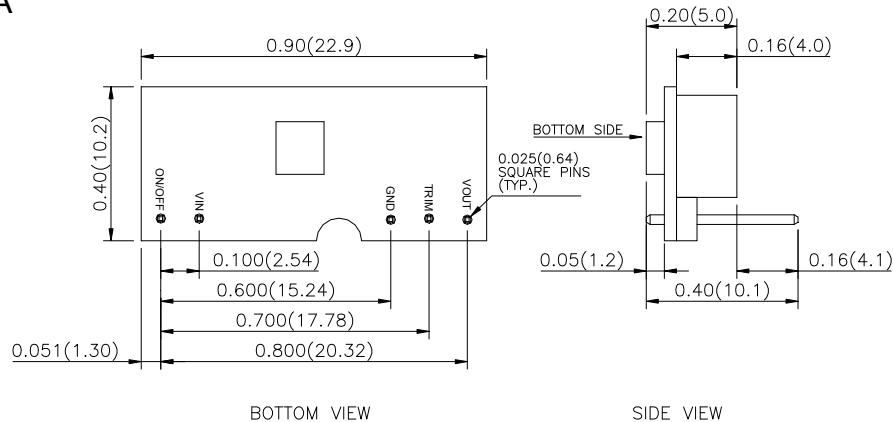
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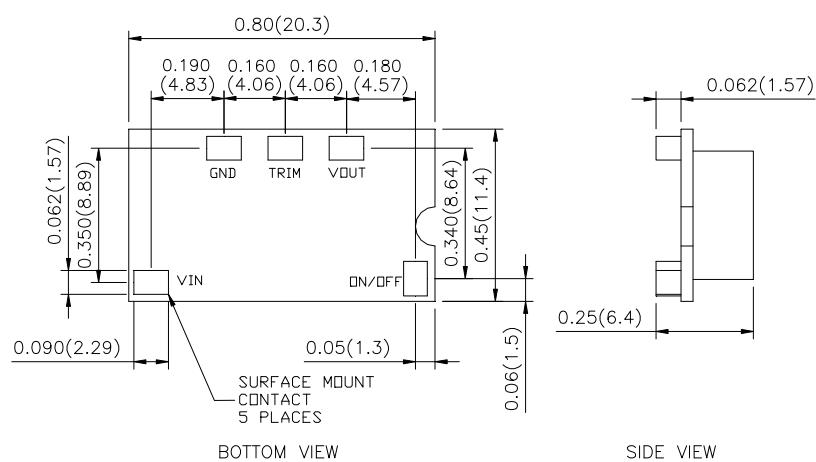
DOH06-12T



DOH06-12TA



DOS06-12T



1. All dimensions in Inches (mm)
Tolerance: X.XX±0.02 (X.X±0.5)
X.XXX±0.01 (X.XX±0.25)
2. Pin pitch tolerance ±0.01(0.25)
3. Pin dimension tolerance ±0.004 (0.1)



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