

BYV34X-600

Dual rectifier diode ultrafast

Rev. 01 — 13 September 2007

Product data sheet

1. Product profile

1.1 General description

Ultrafast, dual common cathode, epitaxial rectifier diode in a SOT186A (TO-220F)) plastic package.

1.2 Features

- Fast switching
- Soft recovery characteristics
- Low forward voltage drop
- Low thermal resistance
- Isolated package
- High thermal cycling performance

1.3 Applications

- Output rectifiers in high frequency switched-mode power supplies
- Discontinuous Current Mode (DCM) Power Factor Correction (PFC)

1.4 Quick reference data

- $V_{RRM} \leq 600 \text{ V}$
- $V_F \leq 1.16 \text{ V}$
- $I_{O(AV)} \leq 20 \text{ A}$
- $t_{rr} \leq 60 \text{ ns}$

2. Pinning information

Table 1. Pinning

Pin	Description	Simplified outline	Symbol
1	anode 1		
2	cathode		
3	anode 2		
mb	mounting base; isolated		

SOT186A (3-lead TO-220F)

3. Ordering information

Table 2. Ordering information

Type number	Package		Version
	Name	Description	
BYV34X-600	TO-220F	plastic single-ended package; isolated heatsink mounted; 1 mounting hole; 3-lead TO-220 'full pack'	SOT186A

4. Limiting values

Table 3. Limiting values

In accordance with the Absolute Maximum Rating System (IEC 60134).

Symbol	Parameter	Conditions	Min	Max	Unit
V_{RRM}	repetitive peak reverse voltage		-	600	V
V_{RWM}	crest working reverse voltage		-	600	V
V_R	reverse voltage	square waveform; $\delta = 1.0$; $T_h \leq 100$ °C	-	600	V
$I_{O(AV)}$	average output current	square waveform; $\delta = 0.5$; $T_h \leq 44$ °C; both diodes conducting	-	20	A
I_{FRM}	repetitive peak forward current	$t = 25$ μ s; square waveform; $\delta = 0.5$; $T_h \leq 44$ °C; per diode	-	20	A
I_{FSM}	non-repetitive peak forward current	$t = 10$ ms; sinusoidal waveform; per diode	-	120	A
		$t = 8.3$ ms; sinusoidal waveform; per diode	-	132	A
T_{stg}	storage temperature		-40	+150	°C
T_j	junction temperature		-	150	°C

5. Thermal characteristics

Table 4. Thermal characteristics

Symbol	Parameter	Conditions	Min	Typ	Max	Unit
$R_{th(j-h)}$	thermal resistance from junction to heatsink	with heatsink compound; per diode; see Figure 1	-	-	5.0	K/W
		with heatsink compound; both diodes conducting	-	-	4.0	K/W
		without heatsink compound; per diode	-	-	7.0	K/W
$R_{th(j-a)}$	thermal resistance from junction to ambient	in free air	-	55	-	K/W

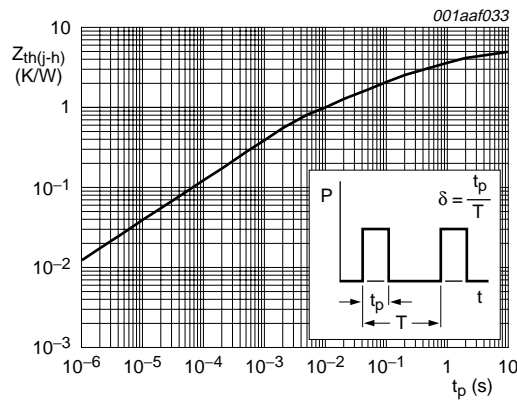


Fig 1. Transient thermal impedance from junction to heatsink as a function of pulse width

6. Isolation characteristics

Table 5. Isolation limiting values and characteristics

$T_h = 25^\circ C$ unless otherwise specified.

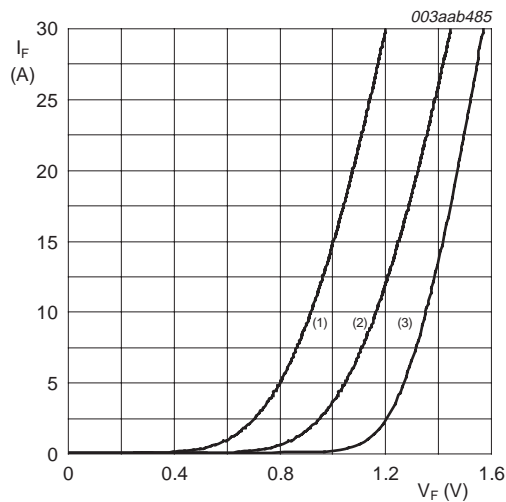
Symbol	Parameter	Conditions	Min	Typ	Max	Unit
$V_{isol(RMS)}$	RMS isolation voltage	from all terminals to external heatsink; $f = 50$ Hz to 60 Hz; sinusoidal waveform; relative humidity $\leq 65\%$; clean and dust free	-	-	2500	V
C_{isol}	isolation capacitance	from cathode to external heatsink; $f = 1$ MHz	-	10	-	pF

7. Characteristics

Table 6. Characteristics

$T_j = 25\text{ °C}$ unless otherwise specified.

Symbol	Parameter	Conditions	Min	Typ	Max	Unit
Static characteristics						
V_F	forward voltage	$I_F = 10\text{ A}$; $T_j = 150\text{ °C}$; see Figure 2	-	0.92	1.16	V
		$I_F = 10\text{ A}$; see Figure 2	-	1.07	1.36	V
I_R	reverse current	$V_R = 600\text{ V}$	-	10	50	μA
		$V_R = 600\text{ V}$; $T_j = 100\text{ °C}$	-	0.2	0.6	mA
Dynamic characteristics						
Q_r	recovered charge	$I_F = 2\text{ A}$ to $V_R \geq 30\text{ V}$; $di_F/dt = 20\text{ A}/\mu\text{s}$; see Figure 3	-	40	70	nC
t_{rr}	reverse recovery time	$I_F = 1\text{ A}$ to $V_R \geq 30\text{ V}$; $di_F/dt = 100\text{ A}/\mu\text{s}$; see Figure 3	-	50	60	ns
I_{RM}	peak reverse recovery current	$I_F = 10\text{ A}$ to $V_R \geq 30\text{ V}$; $di_F/dt = 50\text{ A}/\mu\text{s}$; $T_j = 100\text{ °C}$; see Figure 3	-	3	5	A
V_{FR}	forward recovery voltage	$I_F = 10\text{ A}$; $di_F/dt = 10\text{ A}/\mu\text{s}$; see Figure 4	-	3.2	-	V



- (1) $T_j = 150\text{ °C}$; typical values
- (2) $T_j = 150\text{ °C}$; maximum values
- (3) $T_j = 25\text{ °C}$; maximum values

Fig 2. Forward current as a function of forward voltage

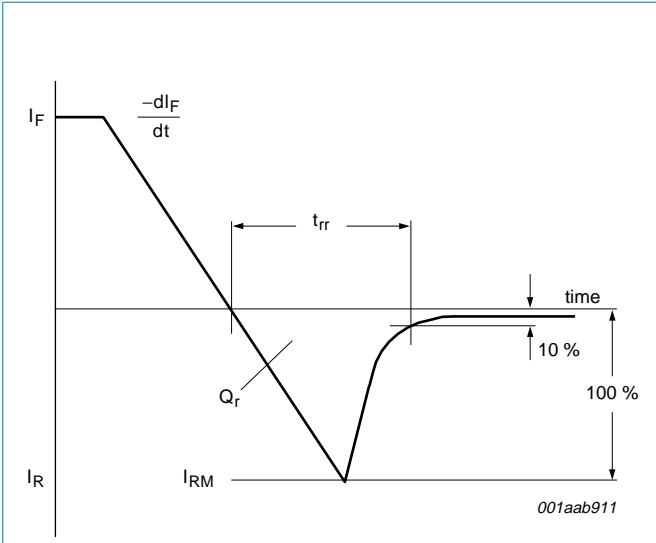


Fig 3. Reverse recovery definitions

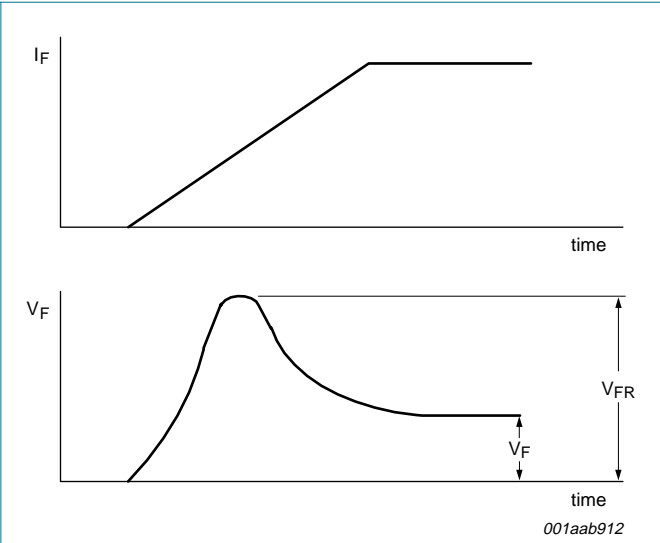
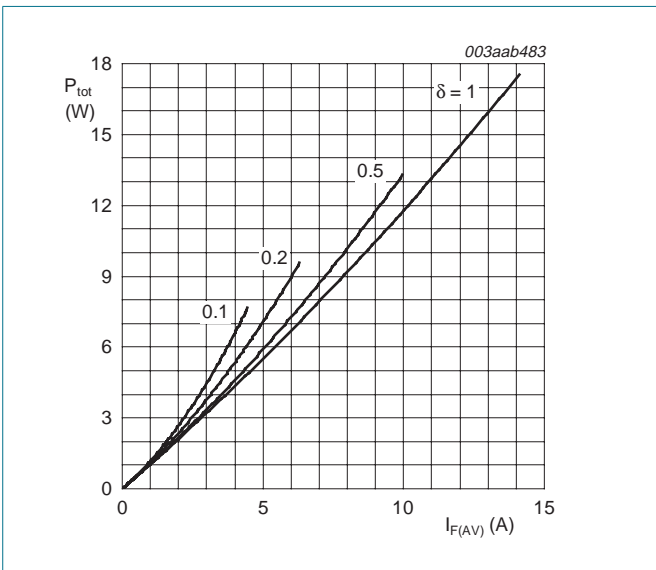
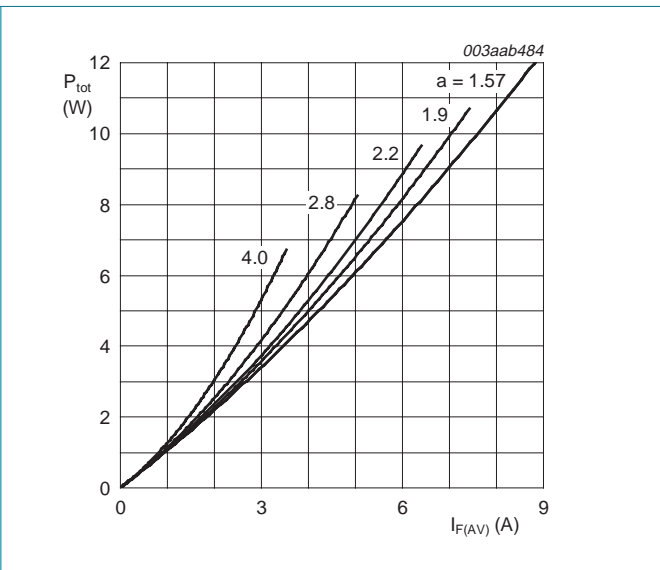


Fig 4. Forward recovery definitions



$$I_{F(AV)} = I_{F(RMS)} \times \sqrt{\delta}$$

Fig 5. Forward power dissipation as a function of average forward current; square waveform; maximum values



$$a = \text{form factor} = I_{F(RMS)} / I_{F(AV)}$$

Fig 6. Forward power dissipation as a function of average forward current; sinusoidal waveform; maximum values

8. Package outline

Plastic single-ended package; isolated heatsink mounted;
1 mounting hole; 3-lead TO-220 'full pack'

SOT186A

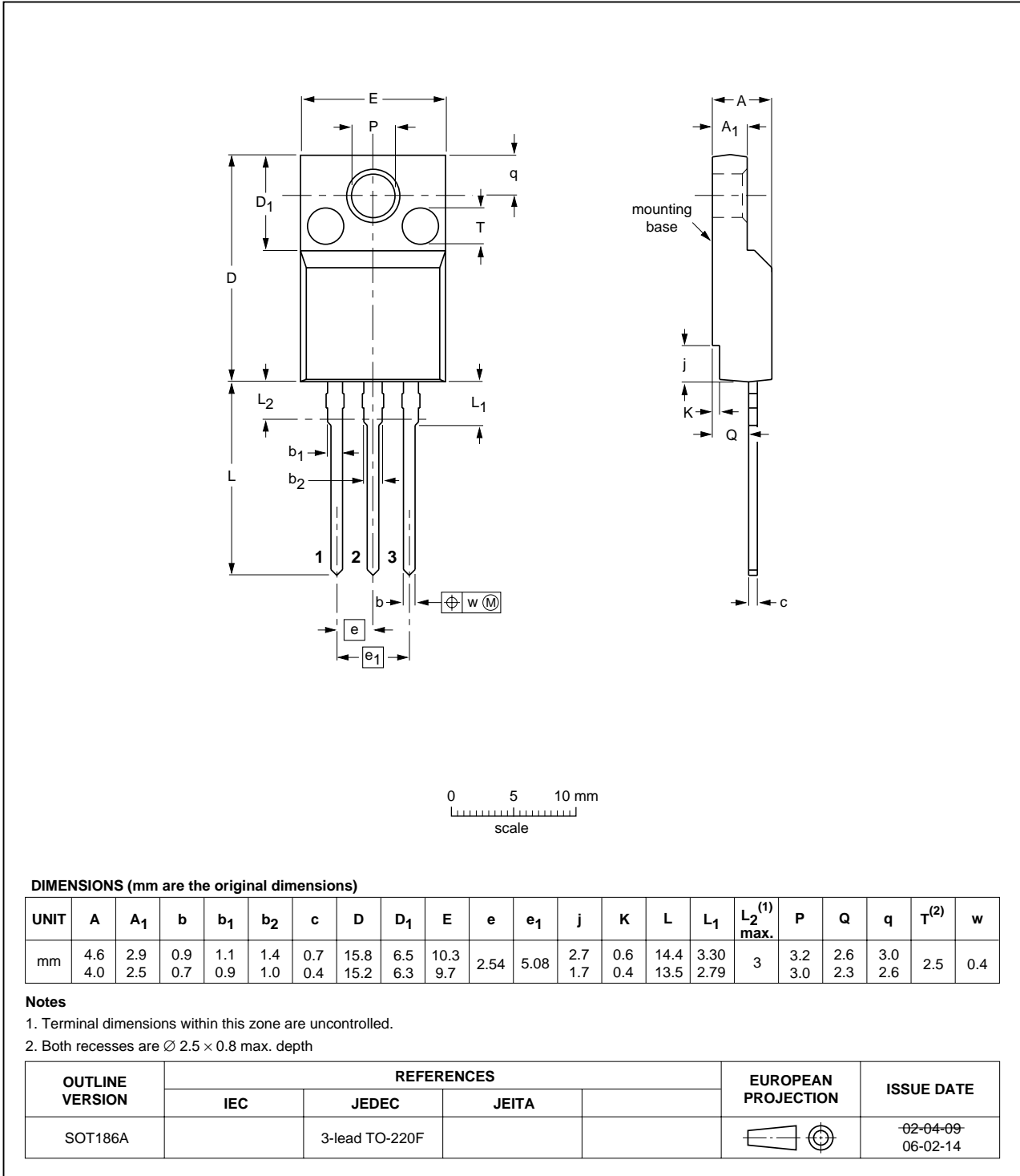


Fig 7. Package outline SOT186A (3-lead TO-220F)

9. Revision history

Table 7. Revision history

Document ID	Release date	Data sheet status	Change notice	Supersedes
BYV34X-600_1	20070913	Product data sheet	-	-

10. Legal information

10.1 Data sheet status

Document status ^{[1][2]}	Product status ^[3]	Definition
Objective [short] data sheet	Development	This document contains data from the objective specification for product development.
Preliminary [short] data sheet	Qualification	This document contains data from the preliminary specification.
Product [short] data sheet	Production	This document contains the product specification.

[1] Please consult the most recently issued document before initiating or completing a design.

[2] The term 'short data sheet' is explained in section "Definitions".

[3] The product status of device(s) described in this document may have changed since this document was published and may differ in case of multiple devices. The latest product status information is available on the Internet at URL <http://www.nxp.com>.

10.2 Definitions

Draft — The document is a draft version only. The content is still under internal review and subject to formal approval, which may result in modifications or additions. NXP Semiconductors does not give any representations or warranties as to the accuracy or completeness of information included herein and shall have no liability for the consequences of use of such information.

Short data sheet — A short data sheet is an extract from a full data sheet with the same product type number(s) and title. A short data sheet is intended for quick reference only and should not be relied upon to contain detailed and full information. For detailed and full information see the relevant full data sheet, which is available on request via the local NXP Semiconductors sales office. In case of any inconsistency or conflict with the short data sheet, the full data sheet shall prevail.

10.3 Disclaimers

General — Information in this document is believed to be accurate and reliable. However, NXP Semiconductors does not give any representations or warranties, expressed or implied, as to the accuracy or completeness of such information and shall have no liability for the consequences of use of such information.

Right to make changes — NXP Semiconductors reserves the right to make changes to information published in this document, including without limitation specifications and product descriptions, at any time and without notice. This document supersedes and replaces all information supplied prior to the publication hereof.

Suitability for use — NXP Semiconductors products are not designed, authorized or warranted to be suitable for use in medical, military, aircraft, space or life support equipment, nor in applications where failure or

malfunction of a NXP Semiconductors product can reasonably be expected to result in personal injury, death or severe property or environmental damage. NXP Semiconductors accepts no liability for inclusion and/or use of NXP Semiconductors products in such equipment or applications and therefore such inclusion and/or use is at the customer's own risk.

Applications — Applications that are described herein for any of these products are for illustrative purposes only. NXP Semiconductors makes no representation or warranty that such applications will be suitable for the specified use without further testing or modification.

Limiting values — Stress above one or more limiting values (as defined in the Absolute Maximum Ratings System of IEC 60134) may cause permanent damage to the device. Limiting values are stress ratings only and operation of the device at these or any other conditions above those given in the Characteristics sections of this document is not implied. Exposure to limiting values for extended periods may affect device reliability.

Terms and conditions of sale — NXP Semiconductors products are sold subject to the general terms and conditions of commercial sale, as published at <http://www.nxp.com/profile/terms>, including those pertaining to warranty, intellectual property rights infringement and limitation of liability, unless explicitly otherwise agreed to in writing by NXP Semiconductors. In case of any inconsistency or conflict between information in this document and such terms and conditions, the latter will prevail.

No offer to sell or license — Nothing in this document may be interpreted or construed as an offer to sell products that is open for acceptance or the grant, conveyance or implication of any license under any copyrights, patents or other industrial or intellectual property rights.

10.4 Trademarks

Notice: All referenced brands, product names, service names and trademarks are the property of their respective owners.

11. Contact information

For additional information, please visit: <http://www.nxp.com>

For sales office addresses, send an email to: salesaddresses@nxp.com

12. Contents

1	Product profile	1
1.1	General description	1
1.2	Features	1
1.3	Applications	1
1.4	Quick reference data	1
2	Pinning information	1
3	Ordering information	2
4	Limiting values	2
5	Thermal characteristics	3
6	Isolation characteristics	3
7	Characteristics	4
8	Package outline	6
9	Revision history	7
10	Legal information	8
10.1	Data sheet status	8
10.2	Definitions	8
10.3	Disclaimers	8
10.4	Trademarks	8
11	Contact information	8
12	Contents	9

Please be aware that important notices concerning this document and the product(s) described herein, have been included in section 'Legal information'.



© NXP B.V. 2007.

All rights reserved.

For more information, please visit: <http://www.nxp.com>

For sales office addresses, please send an email to: salesaddresses@nxp.com

Date of release: 13 September 2007

Document identifier: BYV34X-600_1