# **AIMB-262**

# Intel<sup>®</sup> LGA775 Core<sup>TM</sup> 2 Duo Mini-ITX with VGA, 4 COM, and LAN



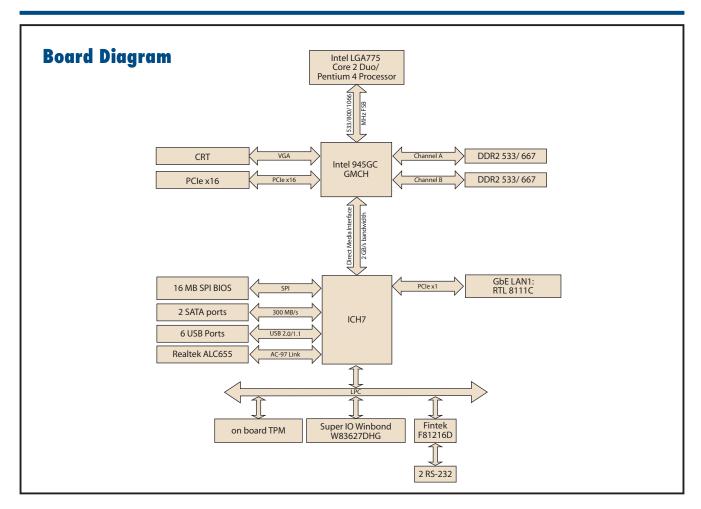
#### **Features**

- Supports Intel<sup>®</sup> LGA775 Core 2<sup>™</sup> Duo/Pentium<sup>®</sup> Dual-Core/Pentium<sup>®</sup> 4/ Celeron<sup>®</sup> processors with FSB 533/800/1066 MHz
- Intel<sup>®</sup> 945GC and ICH7
- Two SODIMM sockets support up to 2 GB DDR2 533/667 SDRAM
- Supports onboard VGA and PCIe x16 expansion for additional graphics card
- Supports Embedded Software API and Utility

Software APIs:	H/W Monitor	Watchdog
Utility:	BIOS flash	Monitoring

#### **Specifications**

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	CPU	Intel Core 2 Duo	Intel Pentium Dual-Core	Intel Pentium 4	Intel Celeron
Processor System Annual Content of Content o	Max. Speed	E7400/E6700, 2.8/2.66 GHZ	E2200 2.2GHz	651 3.4 GHz	440 2.0 GHz
		1066 MHz	1066/800 MHz	800 MHz	800 MHz
TUCESSUL SYSTELLI	L2 Cache	3/2 MB	1 MB	2 MB	512 K
	Chipset	Intel 945GC + ICH7			
	BIOS	Award 16 Mbit, SPI			
	PCI	-			
Expansion Slot	Mini-PCI	-			
	PCIe x16	4 GB/s per direction, 1 slot			
	Technology	Dual channel DDR2 533/ 667 SI	DRAM		
Vemory	Max. Capacity	2 GB			
	Socket	2 x 200-pin SODIMM			
Graphics	Controller	Intel Integrated Graphics Media	Accelerator 950		
alaphilos	VRAM	Shared system memory up to 22	24 MB		
	Interface	10/100/1000 Mbps			
Ethernet	Controller	GbE LAN1: Realtek RTL8111C			
	Connector	RJ-45 x 1			
SATA	Max Data Transfer Rate	300 MB/s			
AIA	Channel	2			
	VGA	1			
	Ethernet	1			
	USB	4 (USB 2.0 compliant)			
Rear I/O	Audio	3 (Mic-in, Line-out, Line-in)			
	Serial	2 (1 of RS-232, 1 of RS-232/422	2/485)		
	Parallel	-			
	PS/2	2 (1 x keyboard and 1 x mouse)			
	USB	2 (USB 2.0 compliant)			
	Serial	2 (RS-232)			
	IDE	-			
	SATA	2			
Internal Connector	CompactFlash	-			
	Parallel	1			
	IrDA	-			
	DIO	-			
	Output	System reset			
Watchdog Timer	Interval	Programmable 1 ~ 255 sec/min			
Po	Power On	5 V 3.3 V	12 V	5 Vsb	-12 V
Power Requirement		4 A 1.02 A	2.35 A	0.26 A	0.12 A
		Operating		Non-Operating	
Environment	Tomporaturo	0 ~ 60° Č (32 ~ 140° F), depend	ds on CPU speed and cooler	-20 ~ 70° C (-4 ~ 158° F)	
	Temperature	solution	-	$-20 \sim 10^{-1} \text{ U} (-4 \sim 158^{\circ} \text{ F})$	
Physical Characteristics	Dimensions	170 mm x 170 mm (6.69" x 6.69	9")		



### **Ordering Information**

Part Number	VGA	GbE LAN	COM	
AIMB-262VG-00A1E	Yes	1	4	

#### **Riser Card**

Part Number AIMB-R430P-03A2E

#### 2U riser card with 3 PCI slot expansion **Bracket View**

Description



## **Packing List**

Description	Quantity
AIMB-262	x 1
SATA HDD cable	х 2
SATA Power cable	х 2
Serial cable	х 2
I/O port bracket	x 1
Startup manual	x 1
Driver CD	x 1

#### Accessories

Part Number	Description
9680004525	TPM module
1700008809	Parallel port cable with bracket
1700002204	Dual port USB cable (27 cm) with bracket
1960022033T000	LGA775 CPU cooler for 2U and wallmount chassis
AIMB-DVI-00A1E	ADD2 DVI expansion card
AIMB-LVDS-00A1E	ADD2 LVDS expansion card
AIMB-VGA-00A1E	ADD2 VGA expansion card
AIMB-HDMI-00A1E	ADD2 HDMI expansion card

# Value-Added Software Services

**Software API:** An interface that defines the ways by which an application program may request services from libraries and/or operating systems. Provides not only the underlying drivers required but also a rich set of user-friendly, intelligent and integrated interfaces, which speeds development, enhances security and offers add-on value for Advantech platforms. It plays the role of catalyst between developer and solution, and makes Advantech embedded platforms easier and simpler to adopt and operate with customer applications.

#### **Software APIs**

#### Control



General Purpose Input/Output is a flexible parallel interface that allows a variety of custom connections. It allows users to monitor the level of signal input or set the output status to switch on/off a device. Our API also provides Programmable GPIO, which allows developers to dynamically set the GPIO input or output status.



SMBus is the System Management Bus defined by Intel<sup>®</sup> Corporation in 1995. It is used in personal computers and servers for low-speed system management communications. The SMBus API allows a developer to interface a embedded system environment and transfer serial messages using the SMBus protocols, allowing multiple simultaneous device control.



I<sup>2</sup>C is a bi-directional two wire bus that was developed by Philips for use in their televisions in the 1980s. The I<sup>2</sup>C API allows a developer to interface with an embedded system environment and transfer serial messages using the I<sup>2</sup>C protocols, allowing multiple simultaneous device control.

Display



Control

The Brightness Control API allows a developer to interface with an embedded device to easily control brightness.



The Backlight API allows a developer to control the backlight (screen) on/off in an embedded device.

Backlight

### **Software Utilities**



The BIOS Flash utility allows customers to update the flash ROM BIOS version, or use it to back up current BIOS by copying it from the flash chip to a file on customers' disk. The BIOS Flash utility also provides a command line version and API for fast implementation into customized applications.



The embedded application is the most important property of a system integrator. It contains valuable intellectual property, design knowledge and innovation, but it is easily copied! The Embedded Security ID utility provides reliable security functions for customers to secure their application data within embedded BIOS.



The Monitoring utility allows the customer to monitor system health, including voltage, CPU and system temperature and fan speed. These items are important to a device; if critical errors happen and are not solved immediately, permanent damage may be caused.

#### Monitor



A watchdog timer (WDT) is a device that performs a specific operation after a certain period of time if something goes wrong and the system does not recover on its own. A watchdog timer can be programmed to perform a warm boot (restarting the system) after a certain number of seconds.



The Hardware Monitor (HWM) API is a system health supervision API that inspects certain condition indexes, such as fan speed, temperature and voltage.



The Hardware Control API allows developers to set the PWM (Pulse Width Modulation) value to adjust fan speed or other devices; it can also be used to adjust the LCD brightness.

#### **Power Saving**



Make use of Intel SpeedStep technology to reduce power power consumption. The system will automatically adjust the CPU Speed depending on system loading.



Refers to a series of methods for reducing power consumption in computers by lowering the clock frequency. These APIs allow the user to lower the clock from 87.5% to 12.5%.



The eSOS is a small OS stored in BIOS ROM. It will boot up in case of a main OS crash. It will diagnose the hardware status, and then send an e-mail to a designated administrator. The eSOS also provides remote connection: Telnet server and FTP server, allowing the administrator to rescue the system.



Flash Lock is a mechanism that binds the board and CF card (SQFlash) together. The user can "Lock" SQFlash via the Flash Lock function and "Unlock" it via BIOS while booting. A locked SQFlash cannot be read by any card reader or boot from other platforms without a BIOS with the "Unlock" feature.

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