



CHENMKO ENTERPRISE CO.,LTD

SURFACE MOUNT

N-Channel Enhancement Mode Field Effect Transistor

VOLTAGE 30 Volts CURRENT 9 Ampere

CHM4416JPT

Lead free devices

APPLICATION

- * Servo motor control.
- * Power MOSFET gate drivers.
- * Other switching applications.

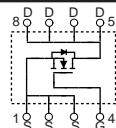
FEATURE

- * Small flat package. (SO-8)
- * Super high dense cell design for extremely low R_{DSON}.
- * High power and current handing capability.
- * Lead free product is acquired.

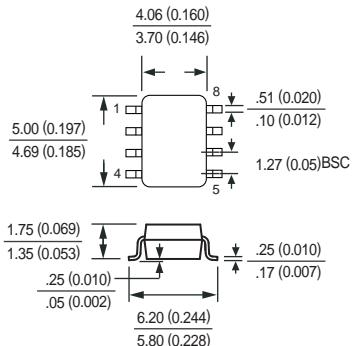
CONSTRUCTION

- * N-Channel Enhancement

CIRCUIT



SO-8



Dimensions in millimeters

SO-8

Absolute Maximum Ratings

T_A = 25°C unless otherwise noted

Symbol	Parameter	CHM4416JPT	Units
V _{DSS}	Drain-Source Voltage	30	V
V _{GSS}	Gate-Source Voltage	±20	V
I _D	Maximum Drain Current - Continuous	9	A
	- Pulsed (Note 3)	50	
P _D	Maximum Power Dissipation	2500	mW
T _J	Operating Temperature Range	-55 to 150	°C
T _{STG}	Storage Temperature Range	-55 to 150	°C

Note : 1. Surface Mounted on FR4 Board , t <=10sec

2. Pulse Test , Pulse width <= 300us , Duty Cycle <= 2%

3. Repetitive Rating , Pulse width limited by maximum junction temperature

4. Guaranteed by design , not subject to production testing

Thermal characteristics

R _{θJA}	Thermal Resistance, Junction-to-Ambient (Note 1)	50	°C/W
2006-02			

RATING CHARACTERISTIC CURVES (CHM4416JPT)

Electrical Characteristics $T_A = 25^\circ\text{C}$ unless otherwise noted

Symbol	Parameter	Conditions	Min	Typ	Max	Units
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OFF CHARACTERISTICS

BV_{DSS}	Drain-Source Breakdown Voltage	$V_{GS} = 0 \text{ V}, I_D = 250 \mu\text{A}$	30			V
$I_{DS(0)}$	Zero Gate Voltage Drain Current	$V_{DS} = 30 \text{ V}, V_{GS} = 0 \text{ V}$			1	μA
I_{GSSF}	Gate-Body Leakage	$V_{GS} = 20 \text{ V}, V_{DS} = 0 \text{ V}$			+100	nA
I_{GSSR}	Gate-Body Leakage	$V_{GS} = -20 \text{ V}, V_{DS} = 0 \text{ V}$			-100	nA

ON CHARACTERISTICS (Note 2)

$V_{GS(th)}$	Gate Threshold Voltage	$V_{DS} = V_{GS}, I_D = 250 \mu\text{A}$	1		3	V
$R_{DS(on)}$	Static Drain-Source On-Resistance	$V_{GS}=10 \text{ V}, I_D=9 \text{ A}$		15	18	$\text{m}\Omega$
		$V_{GS}=4.5 \text{ V}, I_D=7.3 \text{ A}$		23	28	
g_{FS}	Forward Transconductance	$V_{DS} = 15 \text{ V}, I_D = 9 \text{ A}$		14		S

SWITCHING CHARACTERISTICS (Note 4)

Q_g	Total Gate Charge	$V_{DS}=15 \text{ V}, I_D=9 \text{ A}$ $V_{GS}=5 \text{ V}$		15	20	nC
Q_{gs}	Gate-Source Charge			3		
Q_{gd}	Gate-Drain Charge			7		
t_{on}	Turn-On Time	$V_{DD}=15 \text{ V}$ $I_D = 1.0 \text{ A}, V_{GS} = 10 \text{ V}$ $R_{GEN}=6 \Omega$		17	25	nS
t_r	Rise Time			17	20	
t_{off}	Turn-Off Time			48	62	
t_f	Fall Time			12	20	

DRAIN-SOURCE DIODE CHARACTERISTICS AND MAXIMUM RATINGS

I_S	Drain-Source Diode Forward Current	(Note 1)			2.1	A
V_{SD}	Drain-Source Diode Forward Voltage	$I_S = 2.1 \text{ A}, V_{GS} = 0 \text{ V}$ (Note 2)			1.0	V

RATING CHARACTERISTIC CURVES (CHM4416JPT)

Typical Electrical Characteristics

Figure 1. Output Characteristics

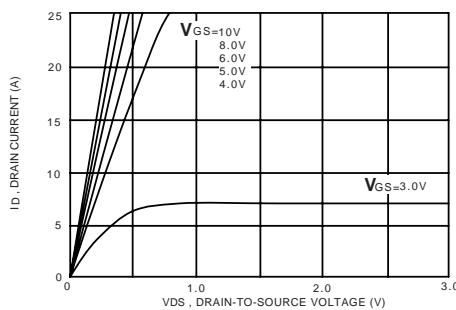


Figure 2. Transfer Characteristics

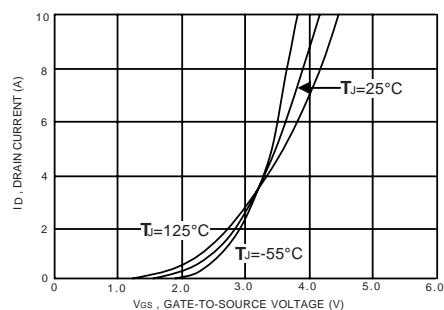


Figure 3. Gate Charge

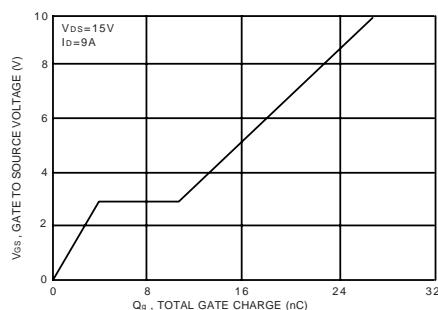


Figure 4. On-Resistance Variation with Temperature

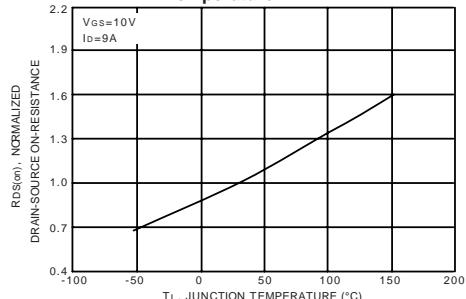


Figure 5. Gate Threshold Variation with Temperature

