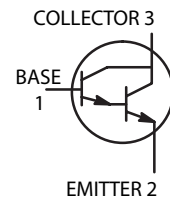


NPN Transistors Darlington Amplifier

* We declare that the material of product compliance with RoHS requirements.

(Pb) Lead(Pb)-Free



MAXIMUM RATINGS

Rating	Symbol	Value	Unit
Collector–Emitter Voltage	V_{CE0}	40	Vdc
Collector–Base Voltage	V_{CBO}	40	Vdc
Emitter–Base Voltage	V_{EBO}	12	Vdc
Collector Current — Continuous	I_C	500	mAdc

THERMAL CHARACTERISTICS

Characteristic	Symbol	Max	Unit
Total Device Dissipation FR–5 Board, (1) $T_A = 25^\circ\text{C}$ Derate above 25°C	P_D	225	mW
Thermal Resistance, Junction to Ambient	$R_{\theta JA}$	556	$^\circ\text{C/W}$
Total Device Dissipation Alumina Substrate, (2) $T_A = 25^\circ\text{C}$ Derate above 25°C	P_D	300	mW
Thermal Resistance, Junction to Ambient	$R_{\theta JA}$	417	$^\circ\text{C/W}$
Junction and Storage Temperature	T_J, T_{stg}	-55 to +150	$^\circ\text{C}$

DEVICE MARKING

MMBT6427 = 1V

ELECTRICAL CHARACTERISTICS ($T_A = 25^\circ\text{C}$ unless otherwise noted.)

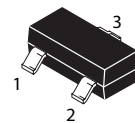
Characteristic	Symbol	Min	Max	Unit
----------------	--------	-----	-----	------

OFF CHARACTERISTICS

Collector–Emitter Breakdown Voltage(3) ($I_C = 10\text{ mAdc}, V_{BE} = 0$)	$V_{(BR)CEO}$	40	—	V
Collector–Base Breakdown Voltage ($I_C = 100\ \mu\text{Adc}, I_E = 0$)	$V_{(BR)CBO}$	40	—	V
Emitter–Base Breakdown Voltage ($I_E = 10\ \mu\text{Adc}, I_C = 0$)	$V_{(BR)EBO}$	12	—	V
Collector Cutoff Current ($V_{CE} = 25\text{Vdc}, I_B = 0$)	I_{CES}	—	1.0	μA
Collector Cutoff Current ($V_{CB} = 30\text{Vdc}, I_E = 0$)	I_{CBO}	—	50	nA
Emitter Cutoff Current ($V_{EB} = 10\text{Vdc}, I_C = 0$)	I_{EBO}	—	50	nA

1. FR–5 = 1.0 x 0.75 x 0.062 in.

2. Alumina = 0.4 x 0.3 x 0.024 in. 99.5% alumina.



SOT-23

ELECTRICAL CHARACTERISTICS (T_A = 25°C unless otherwise noted) (Continued)

Characteristic	Symbol	Min	Max	Unit
ON CHARACTERISTICS				
DC Current Gain (I _C = 10 mAdc, V _{CE} = 5.0 Vdc) (I _C = 100 mAdc, V _{CE} = 5.0Vdc) (I _C = 500 mAdc, V _{CE} = 5.0Vdc)	h _{FE}	10,000 20,000 14,000	100,000 200,000 140,000	—
Collector–Emitter Saturation Voltage (I _C = 50 mAdc, I _B = 0.5 mAdc) (I _C = 500 mAdc, I _B = 0.5 mAdc)	V _{CE(sat)} (3)	— —	1.2 1.5	V
Base–Emitter Saturation Voltage (I _C = 500 mAdc, I _B = 0.5 mAdc)	V _{BE(sat)}	—	2.0	V
Base–Emitter On Voltage (I _C = 50 mAdc, V _{CE} = 5.0Vdc)	V _{BE(on)}	—	1.75	V
SMALL-SIGNAL CHARACTERISTICS				
Output Capacitance (V _{CB} = 10 Vdc, I _E = 0, f = 1.0 MHz)	C _{obo}	—	7.0	pF
Input Capacitance (V _{EB} = 0.5 Vdc, I _C = 0, f = 1.0 MHz)	C _{ibo}	—	15	pF
Current Gain–High Frequency (V _{CE} = 5.0 Vdc, I _C = 10mAdc, f = 100 MHz)	h _{fe}	1.3	—	V
Noise Finure (V _{CE} = 5.0 Vdc, I _C = 1.0 mAdc, R _S = 100 kΩ, f = 1.0 kHz)	NF	—	10	dB

3. **Pulse Tent:** Pulse Width = 300μs, Duty Cycle = 2.0%

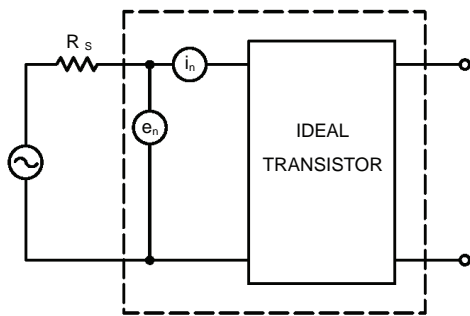


Figure 1. Transistor Noise Model

NOISE CHARACTERISTICS

(V_{CE} = 5.0 Vdc, T_A = 25°C)

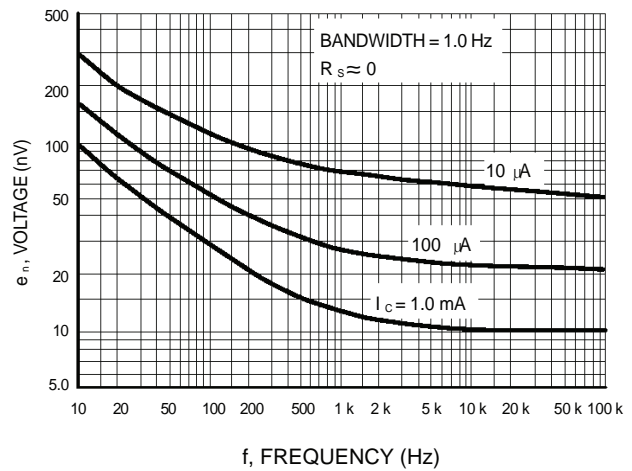
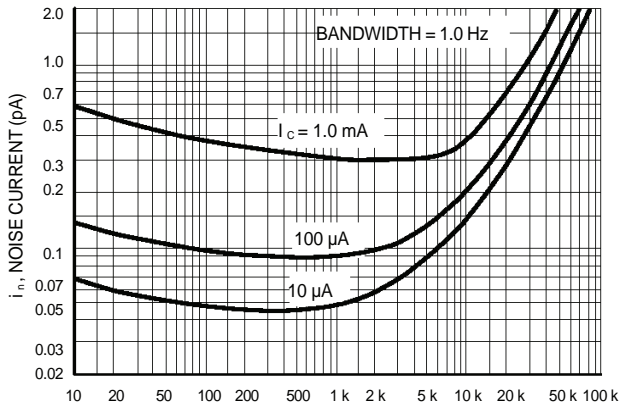


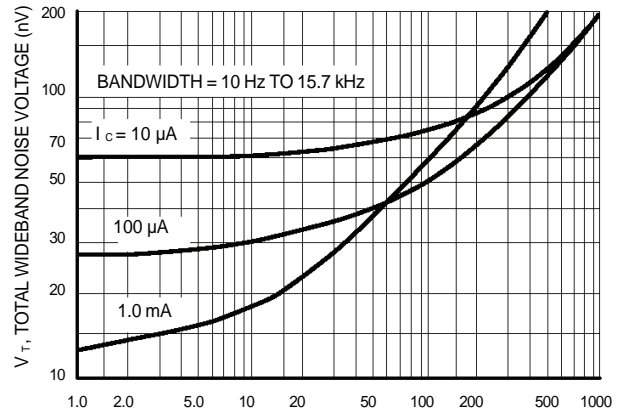
Figure 2. Noise Voltage

NOISE CHARACTERISTICS

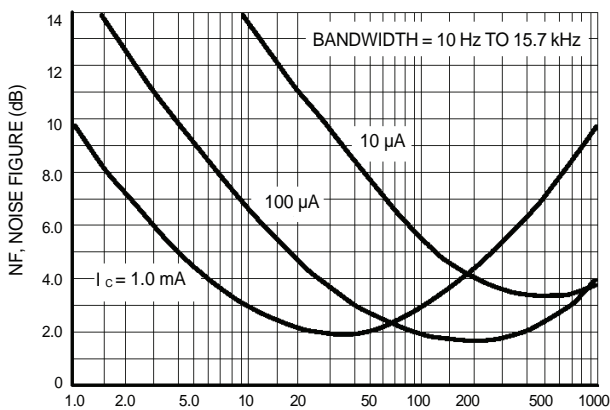
($V_{CE} = 5.0 \text{ Vdc}$, $T_A = 25^\circ\text{C}$)



f , FREQUENCY (Hz)
Figure 3. Noise Current

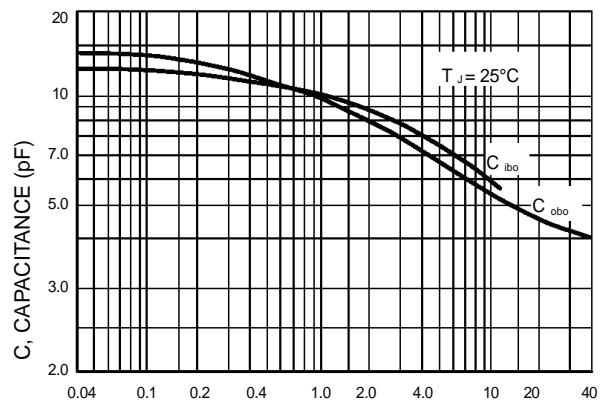


R_s , SOURCE RESISTANCE ($k\Omega$)
Figure 4. Total Wideband Noise Voltage



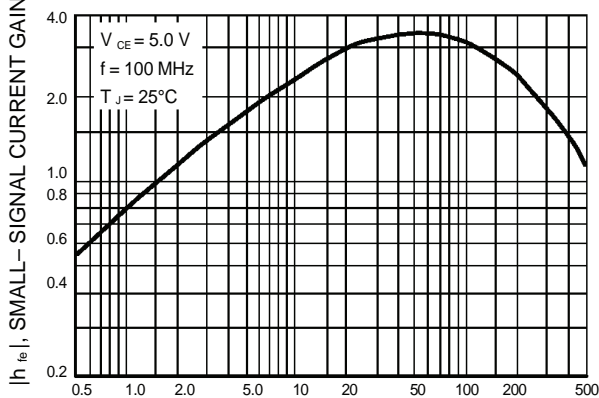
R_s , SOURCE RESISTANCE ($k\Omega$)
Figure 5. Wideband Noise Figure

SMALL-SIGNAL CHARACTERISTICS



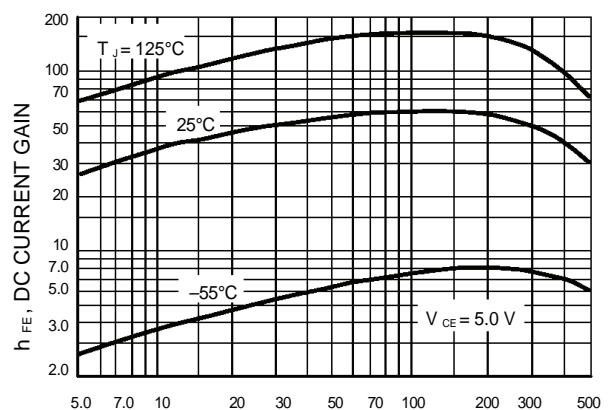
V_R , REVERSE VOLTAGE (VOLTS)
Figure 6. Capacitance

SMALL-SIGNAL CHARACTERISTICS



I_c , COLLECTOR CURRENT (mA)
Figure 7. High Frequency Current Gain

SMALL-SIGNAL CHARACTERISTICS



I_c , COLLECTOR CURRENT (mA)
Figure 8. DC Current Gain

SMALL-SIGNAL CHARACTERISTICS

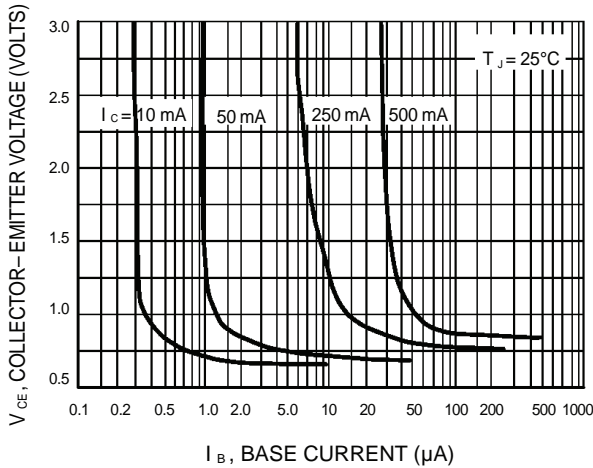


Figure 9. Collector Saturation Region

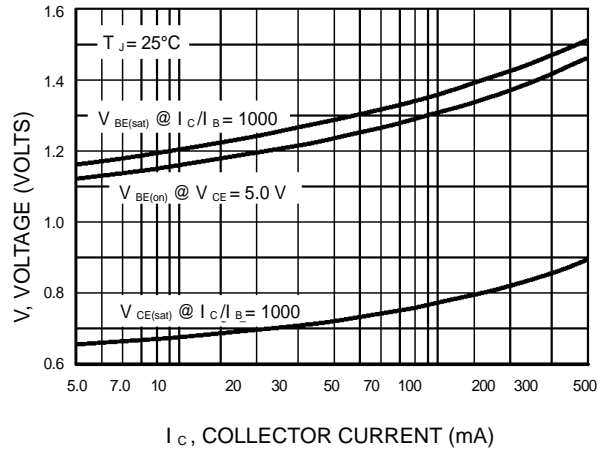


Figure 10. "On" Voltages

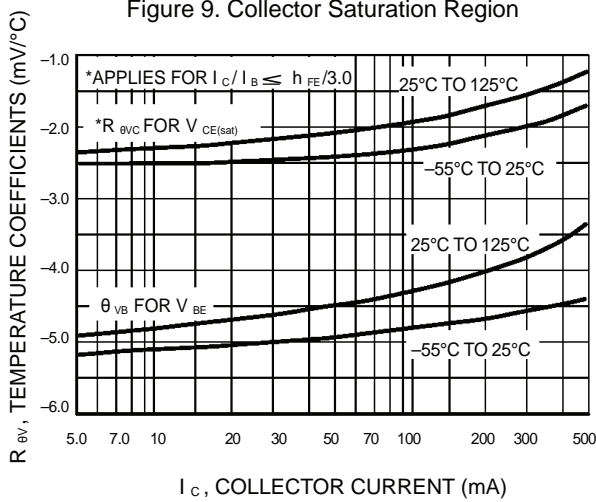
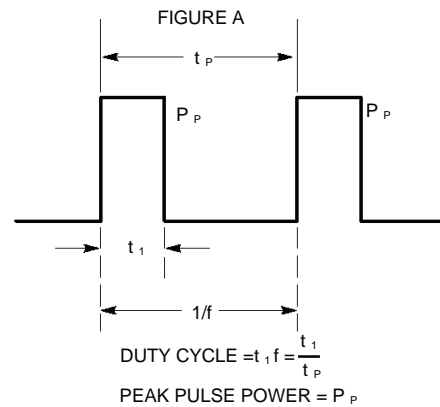


Figure 11. Temperature Coefficients



Design Note: Use of Transient Thermal Resistance Data

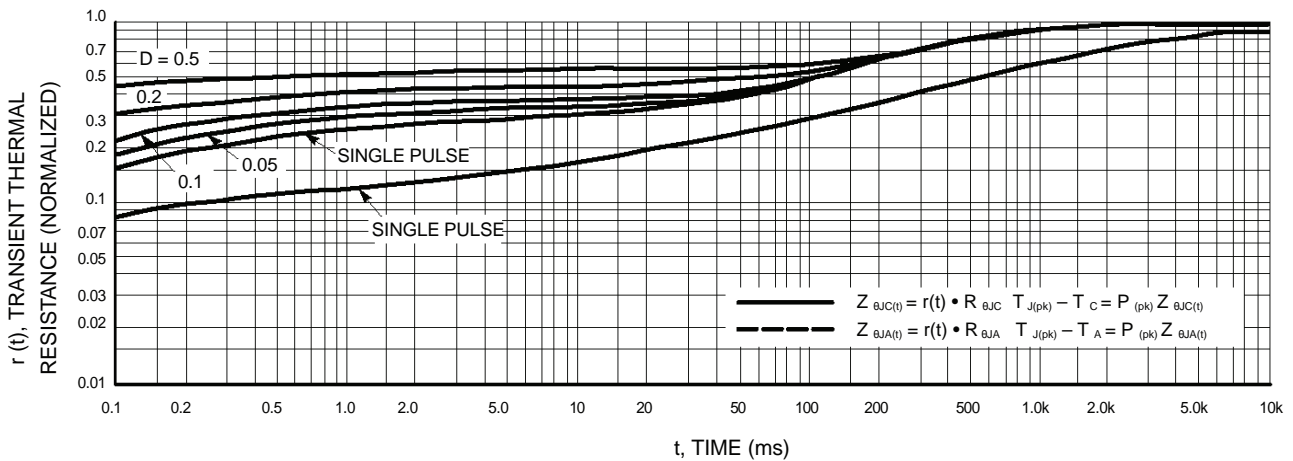
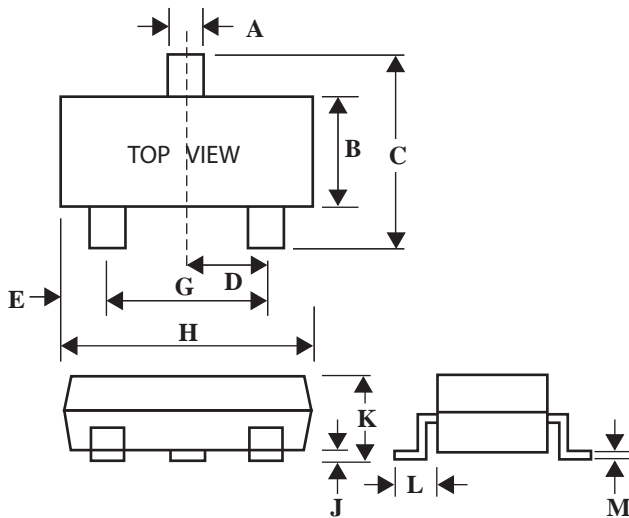


Figure 12. Thermal Response

SOT-23 Package Outline Dimensions

Unit:mm



Dim	Min	Max
A	0.35	0.51
B	1.19	1.80
C	2.10	3.00
D	0.85	1.05
E	0.46	1.00
G	1.70	2.10
H	2.70	3.10
J	0.01	0.13
K	0.89	1.60
L	0.30	0.61
M	0.076	0.25