

**Silicon NPN Power Transistors****MJE13005****DESCRIPTION**

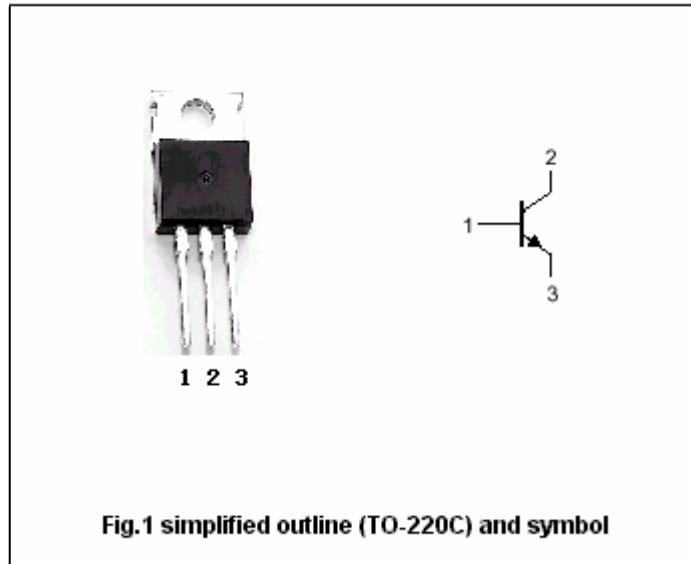
- With TO-220C package
- High voltage ,high speed

**APPLICATIONS**

- Particularly suited for 115V and 220V switchmode applications such as switching regulators,inverters ,motor controls,solenoid/relay drivers and deflection circuits

**PINNING**

PIN	DESCRIPTION
1	Base
2	Collector;connected to mounting base
3	Emitter

**Fig.1 simplified outline (TO-220C) and symbol****Absolute maximum ratings ( $T_c=25^\circ C$  )**

SYMBOL	PARAMETER	CONDITIONS	VALUE	UNIT
$V_{CBO}$	Collector-base voltage	Open emitter	700	V
$V_{CEO}$	Collector-emitter voltage	Open base	400	V
$V_{EBO}$	Emitter-base voltage	Open collector	9	V
$I_C$	Collector current		4	A
$I_{CM}$	Collector current-Peak		8	A
$I_B$	Base current		2	A
$I_{BM}$	Base current-Peak		4	A
$I_E$	Emitter current		6	A
$I_{EM}$	Emitter current-Peak		12	A
$P_D$	Total power dissipation	$T_a=25^\circ C$	2	W
		$T_c=25^\circ C$	75	
$T_j$	Junction temperature		150	
$T_{stg}$	Storage temperature		-65~150	

**THERMAL CHARACTERISTICS**

SYMBOL	PARAMETER	MAX	UNIT
$R_{th\ j-c}$	Thermal resistance from junction to case	1.67	/W

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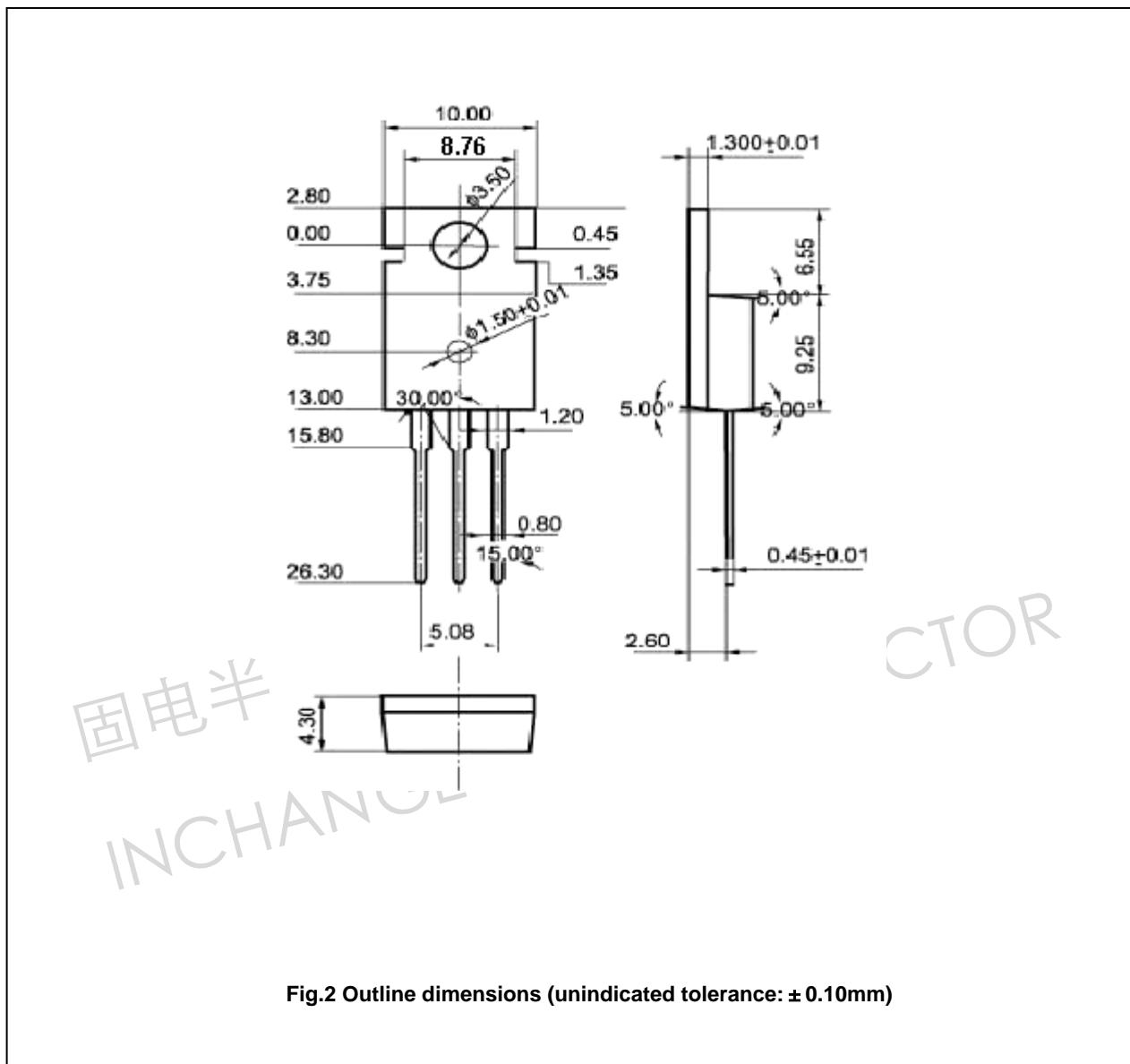
## CHARACTERISTICS

 $T_j=25^\circ\text{C}$  unless otherwise specified

SYMBOL	PARAMETER	CONDITIONS	MIN	TYP.	MAX	UNIT
$V_{CEO(\text{SUS})}$	Collector-emitter sustaining voltage	$I_C=10\text{mA}; I_B=0$	400			V
$V_{CEsat-1}$	Collector-emitter saturation voltage	$I_C=1\text{A}; I_B=0.2\text{A}$			0.5	V
$V_{CEsat-2}$	Collector-emitter saturation voltage	$I_C=2\text{A}; I_B=0.5\text{A}$ $T_C=100^\circ\text{C}$			0.6 1.0	V
$V_{CEsat-3}$	Collector-emitter saturation voltage	$I_C=4\text{A}; I_B=1\text{A}$			1.0	V
$V_{BEsat-1}$	Base-emitter saturation voltage	$I_C=1\text{A}; I_B=0.2\text{A}$			1.2	V
$V_{BEsat-2}$	Base-emitter saturation voltage	$I_C=2\text{A}; I_B=0.5\text{A}$ $T_C=100^\circ\text{C}$			1.6 1.5	V
$I_{CBO}$	Collector cut-off current	$V_{CB}=700\text{V}; I_E=0$ $T_C=100^\circ\text{C}$			1.0 5.0	mA
$I_{EBO}$	Emitter cut-off current	$V_{EB}=9\text{V}; I_C=0$			1.0	mA
$h_{FE-1}$	DC current gain	$I_C=1\text{A}; V_{CE}=5\text{V}$	10		60	
$h_{FE-2}$	DC current gain	$I_C=2\text{A}; V_{CE}=5\text{V}$	8		40	
$f_T$	Transition frequency	$I_C=0.5\text{A}; V_{CE}=10\text{V}; f=1\text{MHz}$	4			MHz
$C_{OB}$	Collector output capacitance	$I_E=0; f=1\text{MHz}; V_{CB}=10\text{V}$		65		pF

Switching times

$t_d$	Delay time	$V_{CC}=125\text{V}, I_C=2\text{A}$ $I_{B1}=-I_{B2}=0.4\text{A}$ $t_p=25\ \mu\text{s}$ duty cycle 1%			0.1	$\mu\text{s}$
$t_r$	Rise time				0.7	$\mu\text{s}$
$t_s$	Storage time				4.0	$\mu\text{s}$
$t_f$	Fall time				0.9	$\mu\text{s}$

**Silicon NPN Power Transistors****MJE13005****PACKAGE OUTLINE**

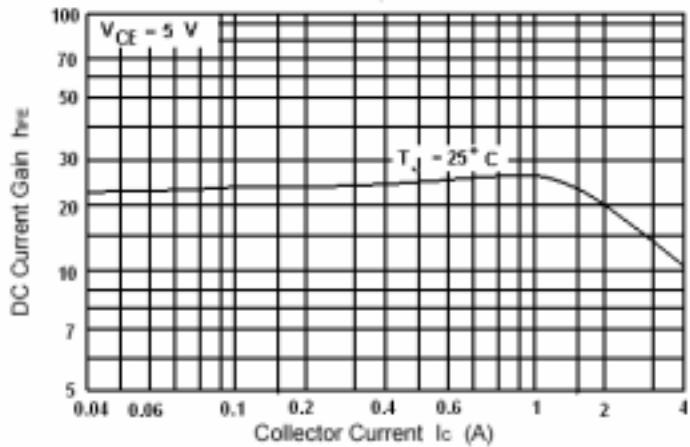
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Fig.3 DC current Gain

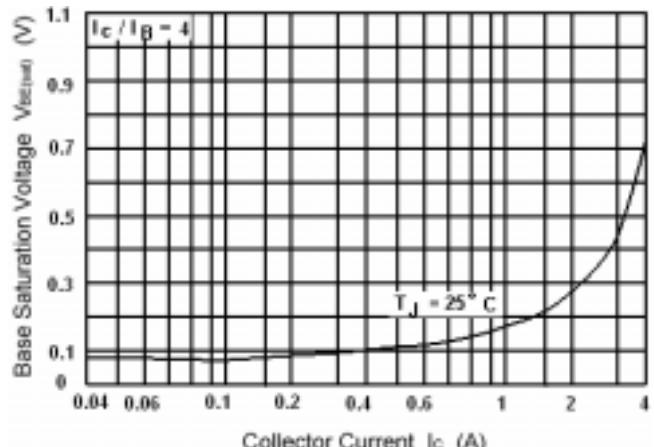


Fig.4 Base-Emitter Saturation Voltage

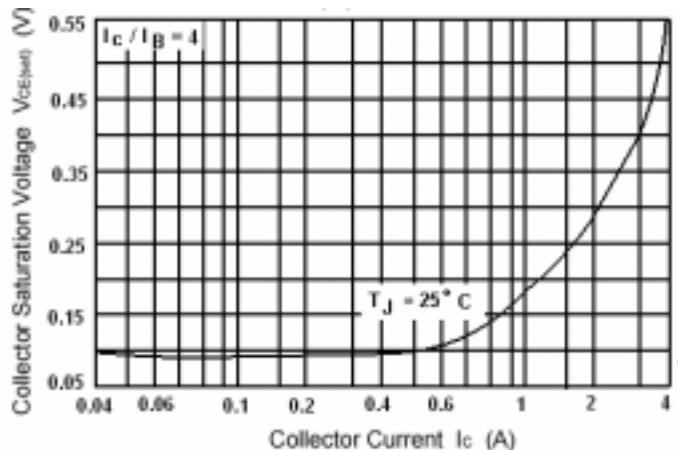


Fig.5 Collector-Emitter Saturation Voltage

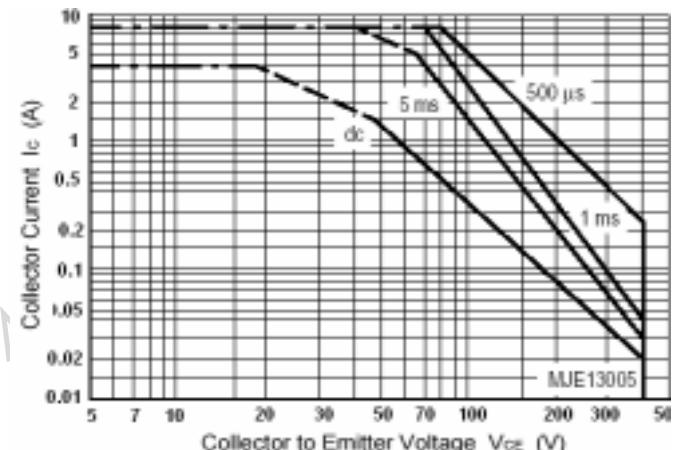


Fig.6 Safe Operating Area