TOSHIBA Field Effect Transistor Silicon N-Channel MOS Type (L^2−π−MOSV)

2SK2614

Chopper Regulator, DC-DC Converter and Motor Drive Applications

- 4-V gate drive
- Low drain-source ON-resistance : $R_{DS\ (ON)}$ = 0.032 Ω (typ.)
- High forward transfer admittance : $|Y_{fs}|$ = 13 S (typ.)
- Low leakage current : I_{DSS} = 100 μA (max) (V_{DS} = 50 V)
- Enhancement mode : V_{th} = 0.8 to 2.0 V (V_{DS} = 10 V, I_D = 1 mA)

Absolute Maximum Ratings (Ta = 25°C)

Characteristic		Symbol	Rating	Unit
Drain-source voltage		V_{DSS}	50	V
Drain-gate voltage (R_{GS} = 20 kΩ)		V_{DGR}	50	V
Gate-source voltage		V_{GSS}	±20	V
Drain current	DC (Note 1)	I_D	20	A
	Pulse (Note 1)	I_{DP}	50	A
Drain power dissipation (Tc = 25°C)		P_D	40	W
Channel temperature		T_{ch}	150	°C
Storage temperature range		T_{stg}	−55 to 150	°C

Note 1: Ensure that the channel temperature does not exceed 150°C.

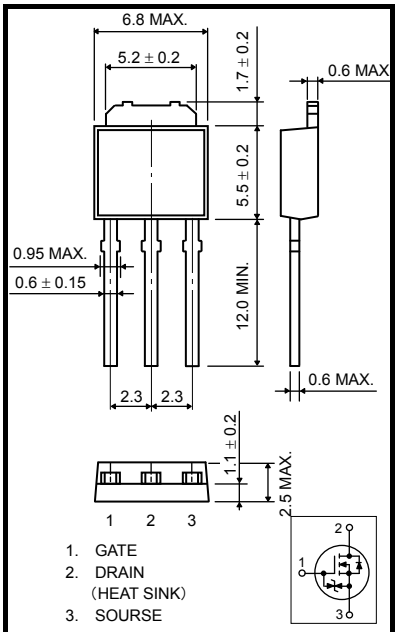
Note 2: Using continuously under heavy loads (e.g. the application of high temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the reliability significantly even if the operating conditions (i.e. operating temperature/current/voltage, etc.) are within the absolute maximum ratings. Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook (“Handling Precautions”/“Derating Concept and Methods”) and individual reliability data (i.e. reliability test report and estimated failure rate, etc).

Thermal Characteristics

Characteristic	Symbol	Max	Unit
Thermal resistance, channel to case	$R_{th\ (ch-c)}$	3.125	°C / W
Thermal resistance, channel to ambient	$R_{th\ (ch-a)}$	125	°C / W

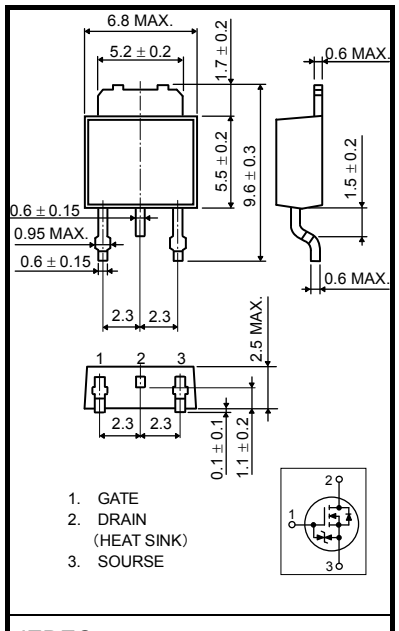
This transistor is an electrostatic-sensitive device. Handle with care.

Unit: mm



JEDEC	—
JEITA	SC-64
TOSHIBA	2-7B5B

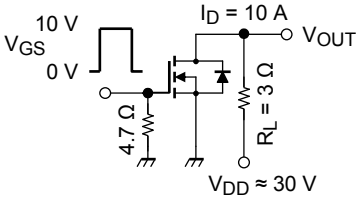
Weight: 0.36 g (typ.)



JEDEC	—
JEITA	—
TOSHIBA	2-7B7B

Weight: 0.36 g (typ.)

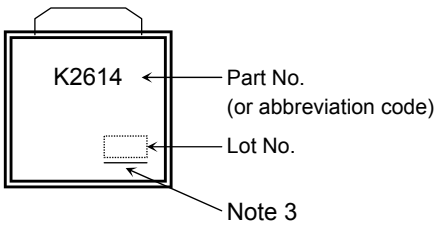
Electrical Characteristics (Ta = 25°C)

Characteristic		Symbol	Test Condition	Min	Typ.	Max	Unit
Gate leakage current		I_{GSS}	V_{GS} = ±16 V, V_{DS} = 0 V	—	—	±10	μA
Drain cutoff current		I_{DSS}	V_{DS} = 50 V, V_{GS} = 0 V	—	—	100	μA
Drain-source breakdown voltage		$V_{(BR)\ DSS}$	I_D = 10 mA, V_{GS} = 0 V	50	—	—	V
Gate threshold voltage		V_{th}	V_{DS} = 10 V, I_D = 1 mA	0.8	—	2.0	V
Drain-source ON-resistance		$R_{DS\ (ON)}$	V_{GS} = 4 V, I_D = 5 A	—	0.055	0.08	Ω
			V_{GS} = 10 V, I_D = 10 A	—	0.032	0.046	
Forward transfer admittance		$\|Y_{fs}\|$	V_{DS} = 10 V, I_D = 10 A	7	13	—	S
Input capacitance		C_{iss}	V_{DS} = 10 V, V_{GS} = 0 V, f = 1 MHz	—	900	—	pF
Reverse transfer capacitance		C_{rss}		—	130	—	
Output capacitance		C_{oss}		—	370	—	
Switching time	Rise time	t_r	 Duty ≤ 1%, t_w = 10 μs	—	15	—	ns
	Turn-on time	t_{on}		—	25	—	
	Fall time	t_f		—	30	—	
	Turn-off time	t_{off}		—	100	—	
Total gate charge (gate-source plus gate-drain)		Q_g	V_{DD} ≈ 40 V, V_{GS} = 10 V, I_D = 20 A	—	25	—	nC
Gate-source charge		Q_{gs}		—	19	—	
Gate-drain (“Miller”) charge		Q_{gd}		—	6	—	

Source-Drain Ratings and Characteristics (Ta = 25°C)

Characteristic	Symbol	Test Condition	Min	Typ.	Max	Unit
Continuous drain reverse current (Note 1)	I_{DR}	—	—	—	20	A
Pulse drain reverse current (Note 1)	I_{DRP}	—	—	—	50	A
Forward voltage (diode)	V_{DSF}	I_{DR} = 20 A, V_{GS} = 0 V	—	—	−1.7	V
Reverse recovery time	t_{rr}	I_{DR} = 20 A, V_{GS} = 0 V, dI_{DR} / dt = 50 A / μs	—	60	—	ns
Reverse recovery charge	Q_{rr}		—	45	—	μC

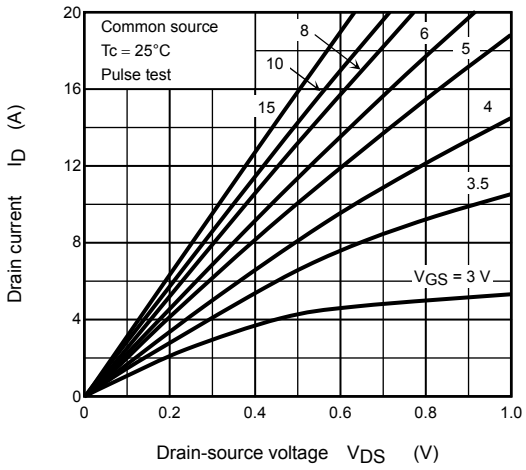
Marking



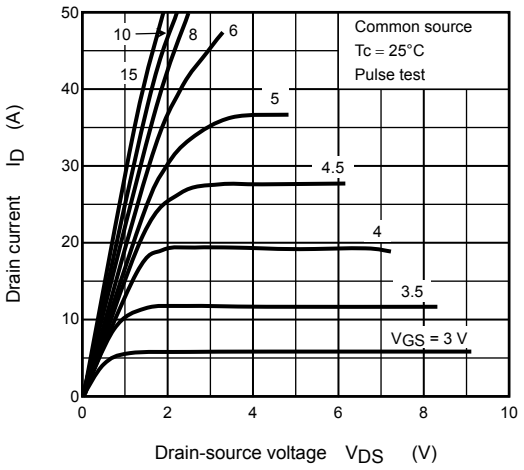
Note 3 : A line under a Lot No. identifies the indication of product Labels
[[G]]/RoHS COMPATIBLE or [[G]]/RoHS [[Pb]]

Please contact your TOSHIBA sales representative for details as to environmental matters such as the RoHS compatibility of Product. The RoHS is Directive 2002/95/EC of the European Parliament and of the Council of 27 January 2003 on the restriction of the use of certain hazardous substances in electrical and electronic equipment.

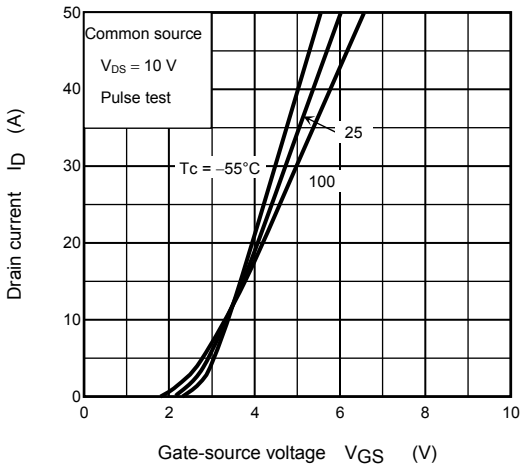
$I_D - V_{DS}$



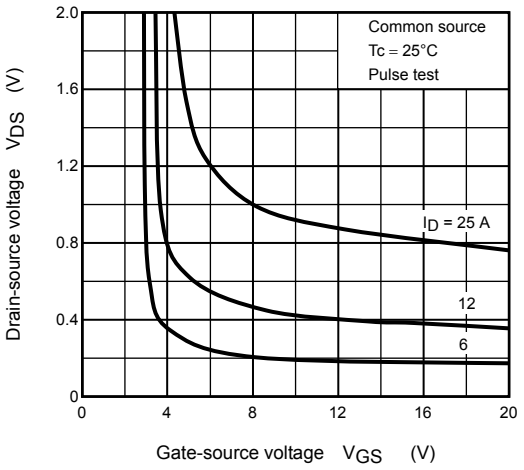
$I_D - V_{DS}$



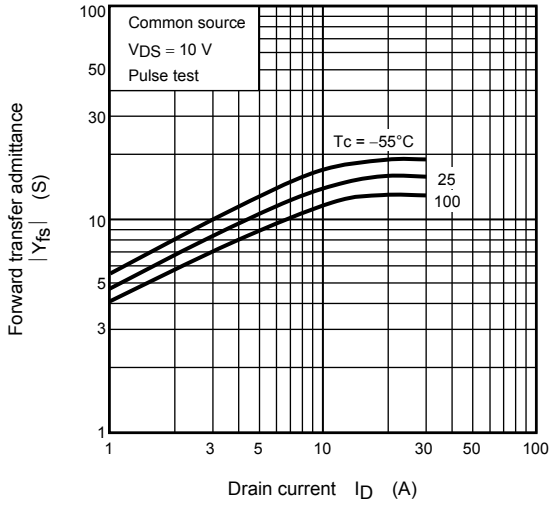
$I_D - V_{GS}$



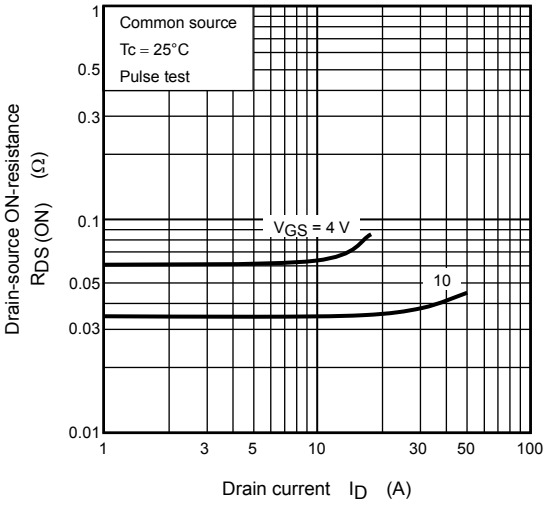
$V_{DS} - V_{GS}$



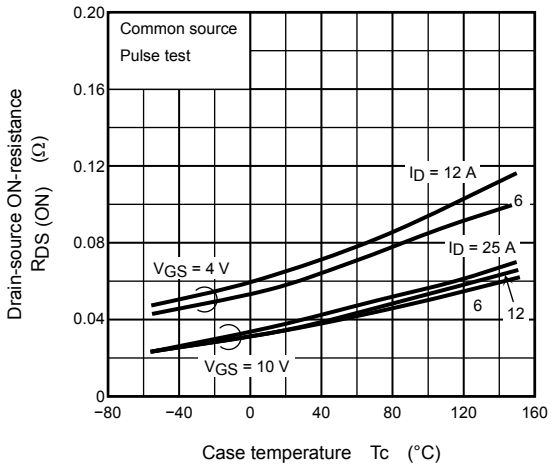
$|Y_{fs}| - I_D$



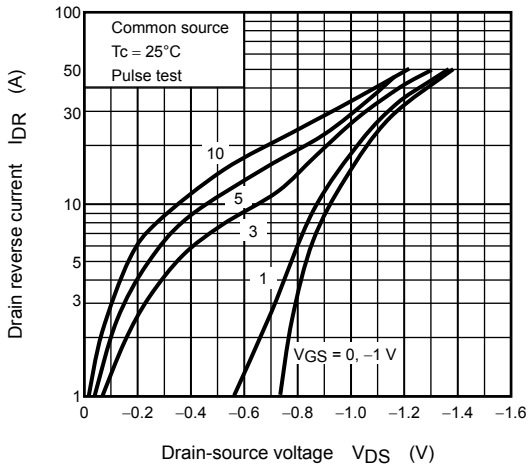
$R_{DS\ (ON)} - I_D$



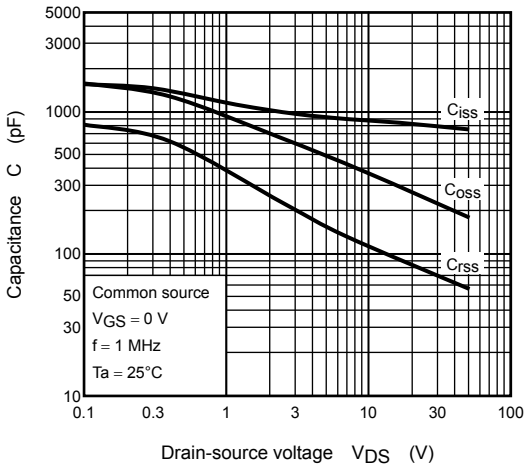
$R_{DS\ (ON)}$ – Tc



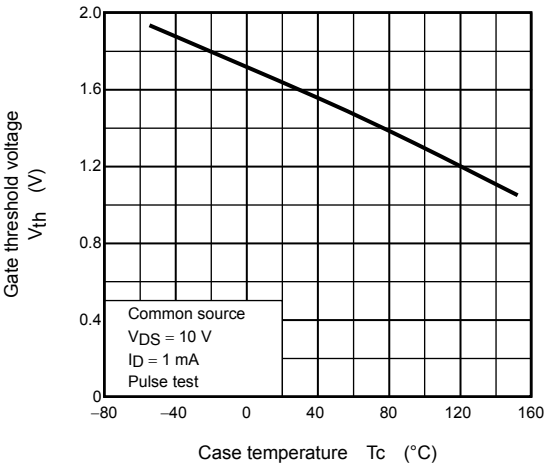
I_{DR} – V_{DS}



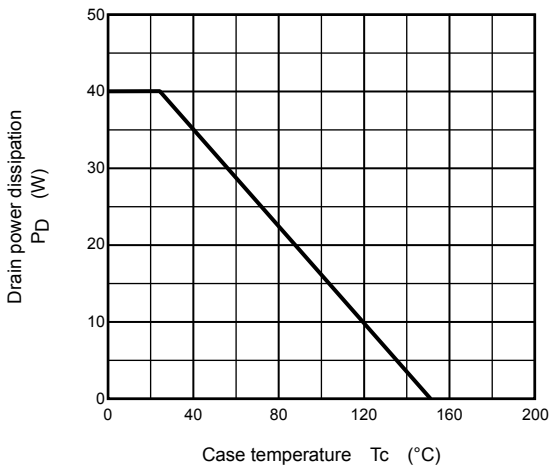
Capacitance – V_{DS}



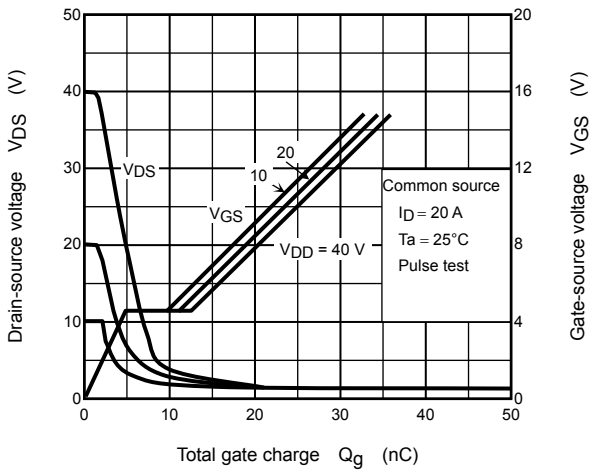
V_{th} – Tc

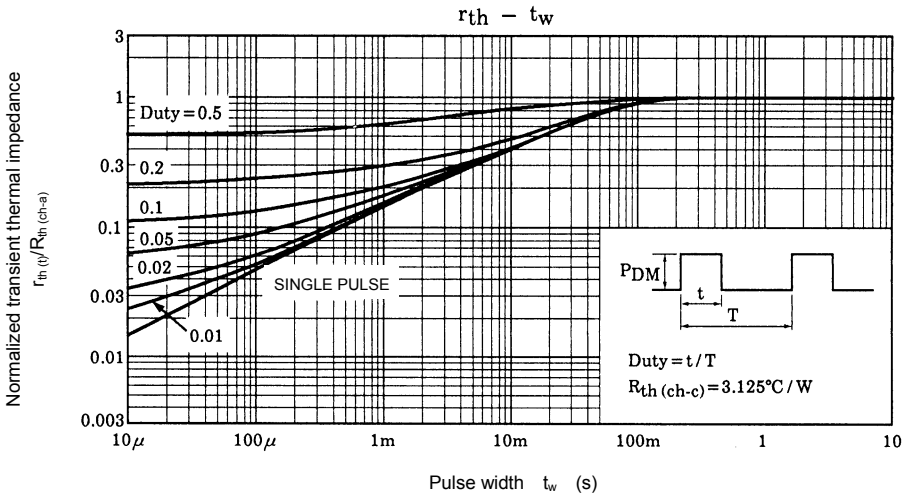


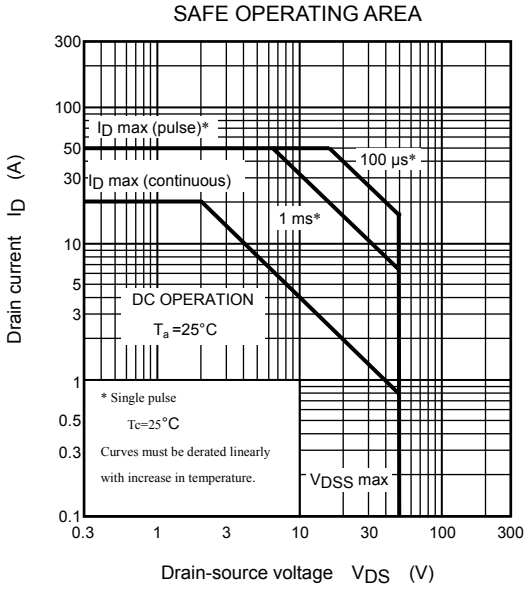
P_D – Tc



Dynamic input / output characteristics




r_{th} – t_w
Normalized transient thermal impedance $r_{th\ (t)}/R_{th\ (ch-a)}$
Duty = 0.5
0.2
0.1
0.05
0.02
0.01
SINGLE PULSE
P_{DM}
t
T
Duty = t / T
$R_{th\ (ch-c)}$ = 3.125°C / W
3
1
0.3
0.1
0.03
0.01
0.003
10μ
100μ
1m
10m
100m
1
10
Pulse width t_w (s)


SAFE OPERATING AREA
Drain current I_D (A)
I_D max (pulse)*
I_D max (continuous)
100 μs*
1 ms*
DC OPERATION
T_a = 25°C
* Single pulse
Tc=25°C
Curves must be derated linearly
with increase in temperature.
V_{DSS} max
300
100
50
30
10
5
3
1
0.5
0.3
0.1
0.3
1
3
10
30
100
300
Drain-source voltage V_{DS} (V)

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