

Symbol	Test Conditions		Characteristic Value	
I _D , I _R	T _{VJ} = T _{VJM} , V _R = V _{RRM} , V _D = V _{DRM}		≤	5 mA
V _T	I _T = 200A, T _{VJ} = 25°C		≤	1.75 V
V _{TO}	For power-loss calculations only (T _{VJ} = T _{VJM})			0.85 V
r _T				6 mΩ
V _{GT}	V _D = 6V	T _{VJ} = 25°C	≤	1.5 V
		T _{VJ} = -40°C	≤	1.6 V
I _{GT}	V _D = 6V	T _{VJ} = 25°C	≤	100 mA
		T _{VJ} = -40°C	≤	200 mA
V _{GD}	T _{VJ} = T _{VJM}	V _D = 2/3 V _{DRM}	≤	0.2 V
I _{GD}	T _{VJ} = T _{VJM}	V _D = 2/3 V _{DRM}	≤	5 mA
I _L	T _{VJ} = 25°C, t _p = 30μs I _G = 0.3A, di _G /dt = 0.3A/μs		≤	450 mA
I _H	T _{VJ} = 25°C, V _D = 6V, R _{GK} = ∞		≤	200 mA
t _{gd}	T _{VJ} = 25°C, V _D = ½ V _{DRM} I _G = 0.3A, di _G /dt = 0.3A/μs		≤	2 μs
t _q	T _{VJ} = T _{VJM} , I _T = 20A, t _p = 200μs, V _R = 100V -di/dt = 10A/μs, dv/dt = 15V/μs, V _D = 2/3 V _{DRM}			150 μs
R _{thJC}	per thyristor; sine 180°el			0.65 K/W
	per module			0.13 K/W
R _{thJK}	per thyristor; sine 180° el			0.8 K/W
	per module			0.16 K/W
d _s	Creeping distance on surface			10.0 mm
d _A	Creeping distance in air			9.4 mm
a	Max. allowable acceleration			50 m/s ²

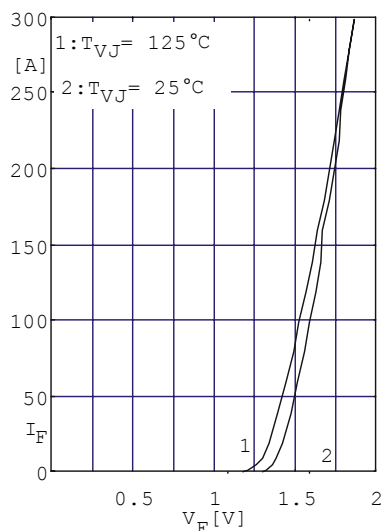


Fig. 1 Forward current vs. voltage drop per diode or thyristor

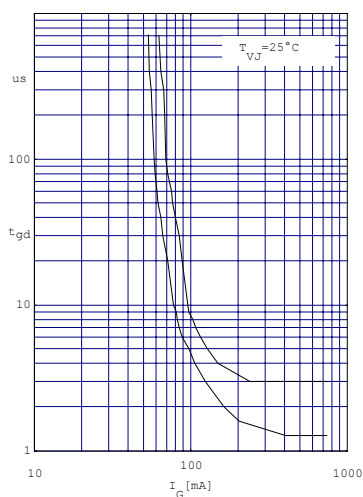


Fig. 2 Gate trigger delay time

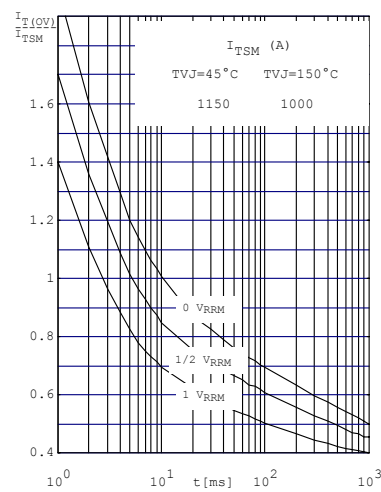


Fig. 3 Surge overload current per diode (or thyristor) I_{FSM} , I_{TSM} : Crest value t : duration

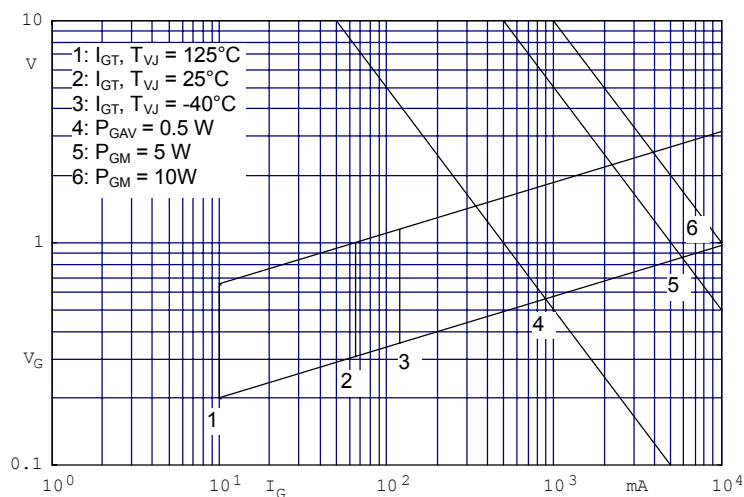


Fig.4 Gate trigger characteristic

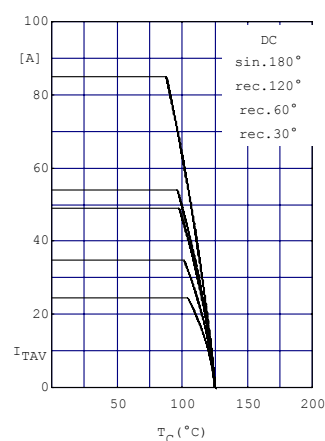


Fig.5 Maximum forward current at case temperature

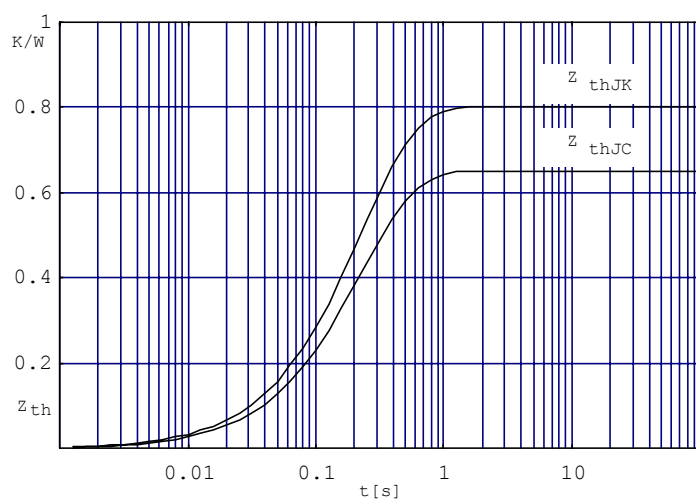


Fig.6 Transient thermal impedance per thyristor or diode (calculated)

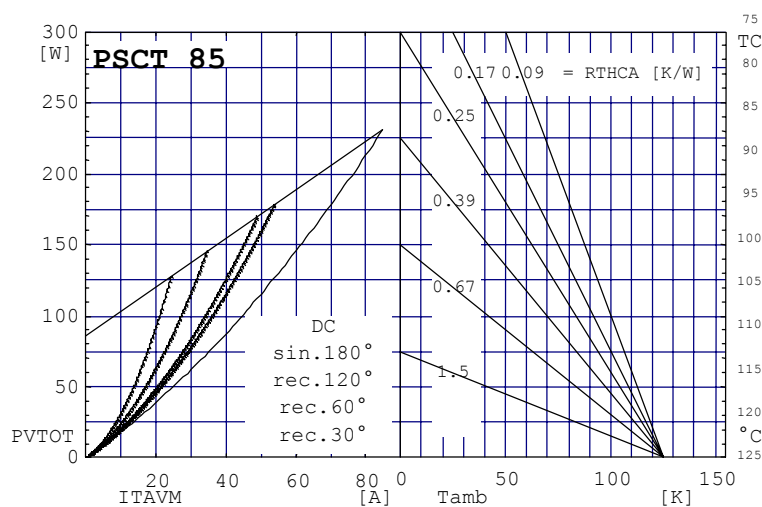


Fig. 7 Power dissipation vs. direct output current and ambient temperature