

Single N-channel MOSFET with schottky diode

ELM14702AA-N

■ General description

ELM14702AA-N uses advanced trench technology to provide excellent $R_{ds(on)}$ and low gate charge.

■ Features

- $V_{ds}=30V$
- $I_d=11A$
- $R_{ds(on)} < 16m\Omega$ ($V_{gs}=10V$)
- $R_{ds(on)} < 25m\Omega$ ($V_{gs}=4.5V$)
- Schottky diode
- $V_{ds(V)}=30V$
- $I_f=3A$
- $V_f < 0.5V@1A$

■ Maximum absolute ratings

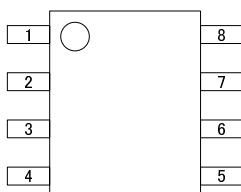
Parameter	Symbol	MOSFET	Schottky	Unit	Note
Drain-source voltage	V_{ds}	30		V	
Gate-source voltage	V_{gs}	± 20		V	
Continuous drain current	I_d	11.0		A	1
		9.3			
Pulsed drain current	I_{dm}	50		A	2
Schottky reverse voltage	V_{ka}		30	V	
Continuous forward current	I_f		4.4	A	1
			3.2		
Pulsed diode forward current	I_{fm}		30	A	2
Power dissipation	P_d	3	3	W	
		2	2		
Junction and storage temperature range	T_j, T_{stg}	-55 to 150	-55 to 150	°C	

■ Thermal characteristics

Parameter (MOSFET)	Symbol	Typ.	Max.	Unit	Note
Maximum junction-to-ambient	$R_{\theta ja}$	31	40	°C/W	1
Maximum junction-to-ambient		59	75	°C/W	
Maximum junction-to-lead	$R_{\theta jl}$	16	24	°C/W	3
Parameter (Schottky)	Symbol	Typ.	Max.	Unit	Note
Maximum junction-to-ambient	$R_{\theta ja}$	36	40	°C/W	1
Maximum junction-to-ambient		67	75	°C/W	
Maximum junction-to-lead	$R_{\theta jl}$	25	30	°C/W	3

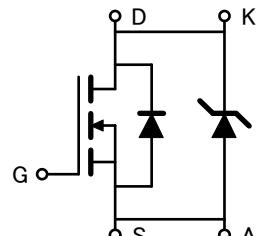
■ Pin configuration

SOP-8 (TOP VIEW)



Pin No.	Pin name
1	SOURCE
2	SOURCE
3	SOURCE
4	GATE
5	DRAIN
6	DRAIN
7	DRAIN
8	DRAIN

■ Circuit



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■ Electrical characteristics

T_a=25°C

Parameter	Symbol	Condition	Min.	Typ.	Max.	Unit
STATIC PARAMETERS						
Drain-source breakdown voltage	BV _{dss}	I _d =250 μA, V _{gs} =0V	30			V
Zero gate voltage drain current (Set by schottky leakage)	I _{dss}	V _r =30V		0.007	0.050	mA
		V _r =30V, T _j =125°C		3.200	10.000	
		V _r =30V, T _j =150°C		12.000	20.000	
Gate-body leakage current	I _{gss}	V _{ds} =0V, V _{gs} =±20V		100		nA
Gate threshold voltage	V _{gs(th)}	V _{ds} =V _{gs} , I _d =250 μA	1.0	1.8	3.0	V
On state drain current	I _{d(on)}	V _{gs} =4.5V, V _{ds} =5V	40			A
Static drain-source on-resistance	R _{ds(on)}	V _{gs} =10V		13.4	16.0	mΩ
		I _d =11A	T _j =125°C	16.8	21.0	
		V _{gs} =4.5V, I _d =8A		20.0	25.0	
Forward transconductance	G _f	V _{ds} =5V, I _d =11A		25		S
Diode+schottky forward voltage	V _{sd}	I _s =1A, V _{gs} =0V		0.45	0.50	V
Max. body-diode+schottky continuous current	I _s			5		A
DYNAMIC PARAMETERS						
Input capacitance	C _{iss}	V _{gs} =0V, V _{ds} =15V f=1MHz		1040	1250	pF
Output capacitance (FET+Schottky)	C _{oss}			212		pF
Reverse transfer capacitance	C _{rss}			121		pF
Gate resistance	R _g	V _{gs} =0V, V _{ds} =0V, f=1MHz		0.70	0.85	Ω
SWITCHING PARAMETERS						
Total gate charge (10V)	Q _g	V _{gs} =10V, V _{ds} =15V I _d =11A		19.8	24.0	nC
Total gate charge (4.5V)	Q _g			9.8	12.0	nC
Gate-source charge	Q _{gs}			2.5		nC
Gate-drain charge	Q _{gd}			3.5		nC
Turn-on delay time	t _{d(on)}	V _{gs} =10V, V _{ds} =15V R _l =1.35 Ω, R _{gen} =3 Ω		4.5	7.0	ns
Turn-on rise time	t _r			3.9	7.0	ns
Turn-off delay time	t _{d(off)}			17.4	30.0	ns
Turn-off fall time	t _f			3.2	5.7	ns
Body diode+schottky reverse recovery time	t _{rr}	I _f =11A, dI/dt=100A/μs		19	23	ns
Body diode+schottky reverse recovery charge	Q _{rr}	I _f =11A, dI/dt=100A/μs		9	11	nC

NOTE :

1. The value of R_{θja} is measured with the device mounted on 1in² FR-4 board of 2oz. Copper, in still air environment with T_a=25°C. The value in any given applications depends on the user's specific board design, The current rating is based on the t ≤ 10s thermal resistance rating.
2. Repetitive rating, pulse width limited by junction temperature.
3. The R_{θja} is the sum of the thermal impedance from junction to lead R_{θjl} and lead to ambient.
4. The static characteristics in Figures 1 to 6 are obtained using 80μs pulses, duty cycle 0.5%max.
5. These tests are performed with the device mounted on 1in² FR-4 board with 2oz. Copper, in a still air environment with T_a=25°C. The SOA curve provides a single pulse rating.

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■ Typical electrical and thermal characteristics

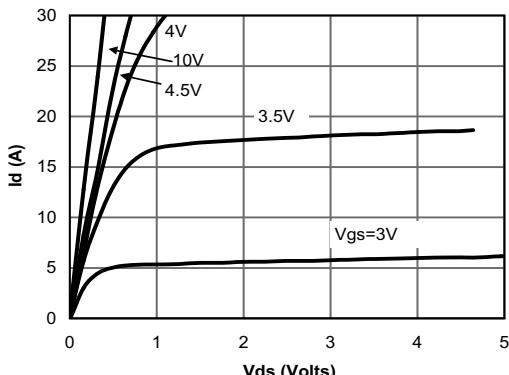


Fig 1: On-Region Characteristics

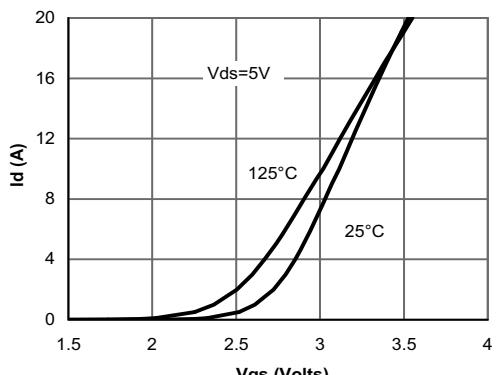


Figure 2: Transfer Characteristics

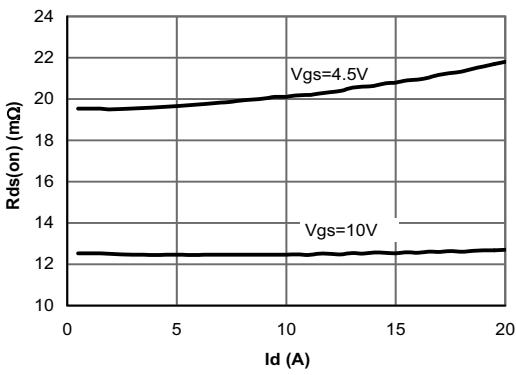


Figure 3: On-Resistance vs. Drain Current and Gate Voltage

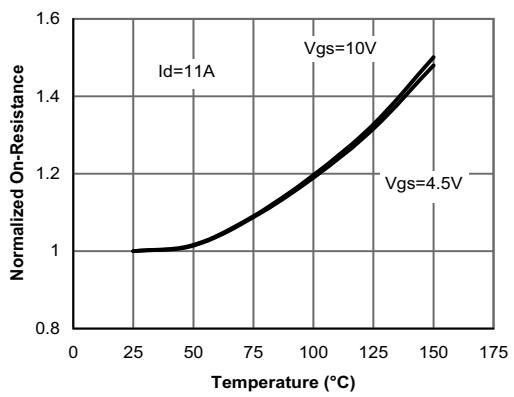


Figure 4: On-Resistance vs. Junction Temperature

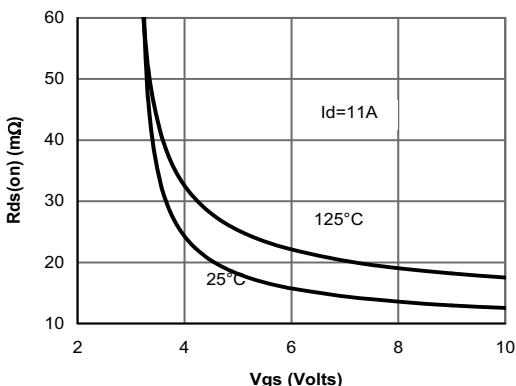


Figure 5: On-Resistance vs. Gate-Source Voltage

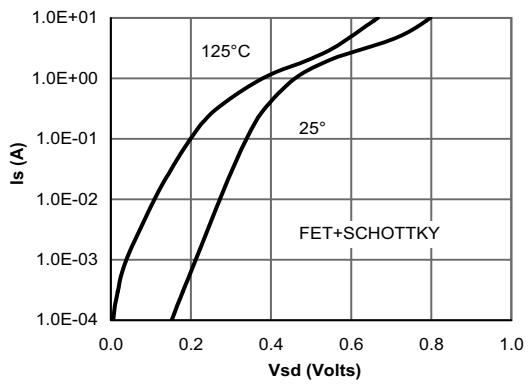


Figure 6: Body-Diode Characteristics

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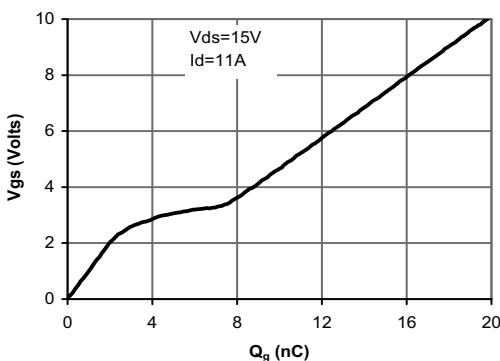


Figure 7: Gate-Charge Characteristics

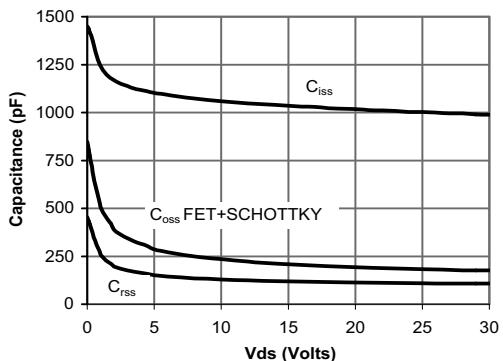


Figure 8: Capacitance Characteristics

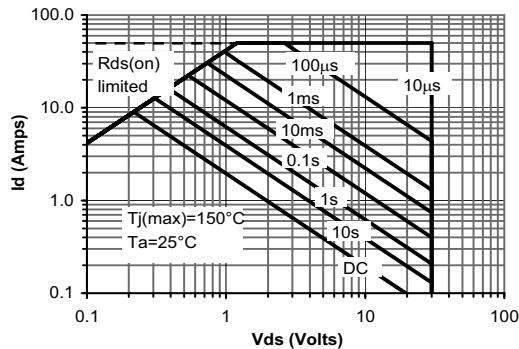


Figure 9: Maximum Forward Biased Safe Operating Area (Note E)

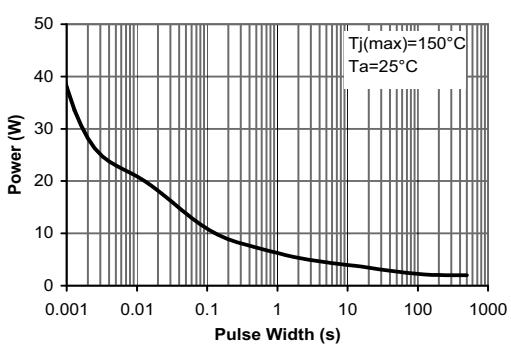


Figure 10: Single Pulse Power Rating Junction-to-Ambient (Note E)

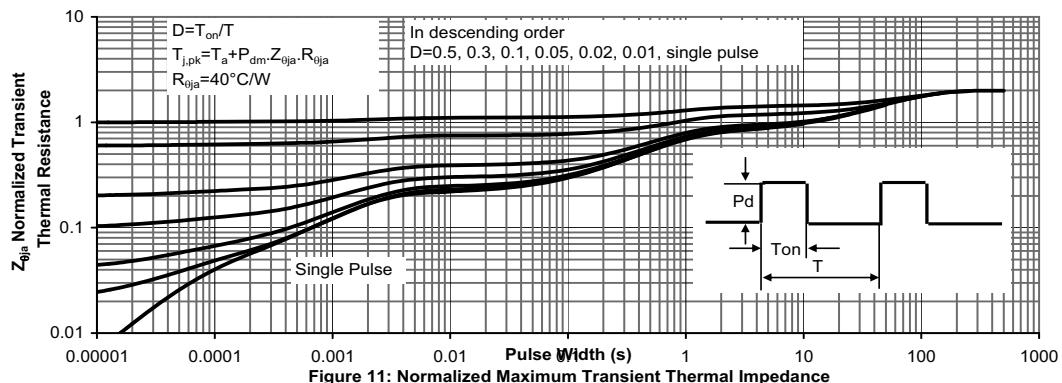


Figure 11: Normalized Maximum Transient Thermal Impedance