AT65-0263

Digital Attenuator
31.0 dB, 5-Bit, TTL Driver, DC-2.0 GHz

Rev. V9

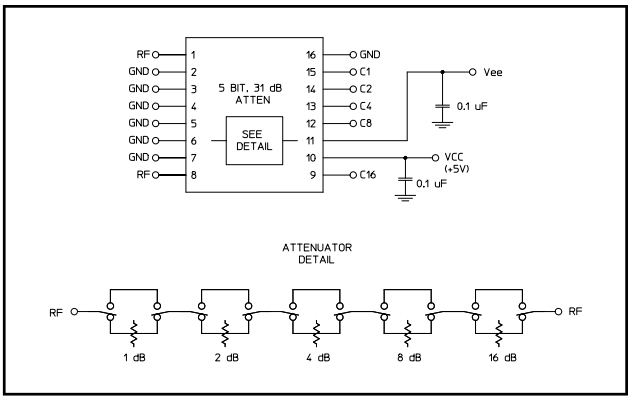
Features

- Attenuation: 1.0 dB Steps to 31 dB
- Low DC Power Consumption
- Plastic SOW, Wide Body, SMT Package
- Integral TTL Driver
- 50 ohm Impedance
- Test Boards are Available
- Tape and Reel Packaging Available
- SOW-16 Package

Description

M/A-COM's AT65-0263 is a GaAs FET 5-bit digital attenuator with integral TTL driver. Step size is 1.0 dB providing 31 dB total attenuation range. This device is in a SOW-16 plastic surface mount package. The AT65-0263 is ideally suited for use where accuracy, fast speed, very low power consumption and low costs are required.

Schematic with Off-Chip Components



Ordering Information

Part Number	Package
AT65-0263	Bulk Packaging
AT65-0263TR	1000 piece reel
AT65-0263-TB	Sample Test Board

Note: Reference Application Note M513 for reel size information.

Pin Configuration

Pin No.	Function	Pin No.	Function
1	RF	9	C16
2	GND	10	Vcc
3	GND	11	Vee
4	GND	12	C8
5	GND	13	C4
6	GND	14	C2
7	GND	15	C1
8	RF	16	GND

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Electrical Specifications: T_A = 25°C

Parameter	Test Conditions	Frequency	Units	Min	Typ	Max
Insertion Loss	—	DC-2.0 GHz	dB	—	2.8	3.2
Attenuation Accuracy	Individual Bits 1-2-4-8-16 Any Combination of bits 3 - 29 dB Any Combination of bits 30 - 31 dB	DC-2.0 GHz DC-2.0 GHz DC-2.0 GHz	dB dB dB	— — —	— — —	±(.5 +5% of atten setting) ±(.5 +5% of atten setting) ±(.7 +7% of atten setting)
VSWR	Full Range	DC-2.0 GHz	Ratio	—	1.5:1	1.8:1
Switching Speed	50% Cntl to 90%/10% RF 10% to 90% or 90% to 10%	— —	nS nS	— —	75 20	150 50
1 dB Compression	— —	50 MHz 0.5-2.0 GHz	dBm dBm	— —	+21 +24	— —
Input IP_3	Two-tone inputs up to +5 dBm	50 MHz 0.5-2.0 GHz	dB dB	— —	+35 +48	— —
Vcc[1] Vee[1]	— —	— —	V V	4.75 -8.0	5.0 -5.0	5.25 -4.75
V_{IL} V_{IH}	LOW-level input voltage HIGH-level input voltage	— —	V V	0.0 2.0	— —	0.8 5.0
Iin (Input Leakage Current)	Vin = V_{CC} or GND	—	uA	-1.0	—	1.0
Icc (Quiescent Supply Current)	Vcntrl = V_{CC} or GND	—	uA	—	250	400
ΔIcc[2] (Additional Supply Current Per TTL Input Pin)	V_{CC} = Max, Vcntrl = V_{CC} - 2.1 V	—	mA	—	—	1.0
I_{EE}	V_{EE} min to max, Vin = V_{IL} or V_{IH}	—	mA	-1.0	-0.2	—
Thermal Resistance θ_{JA}	PCB mount on FR4 material, copper trace, still air at +25°C	—	°C/W	—	90-130	—

1. Decoupling capacitors (.1 µF) are required on power supply lines.
2. For calculating ΔIcc, the number of TTL input pins is 6.

Absolute Maximum Ratings[3,4]

Parameter	Absolute Maximum
Max. Input Power 0.05 GHz 0.5 - 2.0 GHz	 +27 dBm +34 dBm
V_{CC}	-0.5V ≤ V_{CC} ≤ +7.0V
V_{EE}	-8.5V ≤ V_{EE} ≤ +0.5V
V_{CC} - V_{EE}	-0.5V ≤ V_{CC} - V_{EE} ≤ 14.5V
Vin[5]	-0.5V ≤ Vin ≤ V_{CC} + 0.5V
Operating Temperature	-40ºC to +85ºC
Storage Temperature	-65ºC to +125ºC

3. Exceeding any one or combination of these limits may cause permanent damage to this device.
4. M/A-COM does not recommend sustained operation near these survivability limits.
5. Standard CMOS TTL interface, latch-up will occur if logic signal is applied prior to power supply.

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Handling Procedures

Please observe the following precautions to avoid damage:

Static Sensitivity

Gallium Arsenide Integrated Circuits are sensitive to electrostatic discharge (ESD) and can be damaged by static electricity. Proper ESD control techniques should be used when handling these devices.

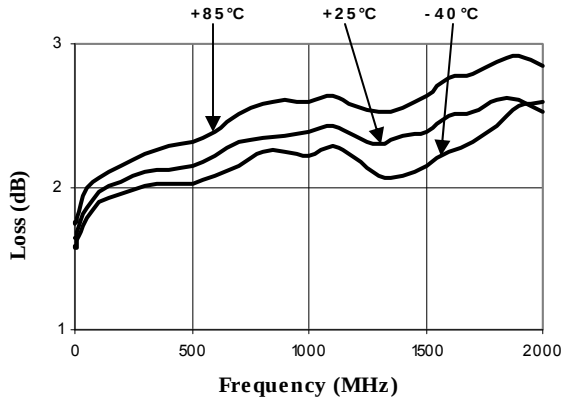
Truth Table (Digital Attenuator)

C16	C8	C4	C2	C1	Attenuation
0	0	0	0	0	Loss, Reference
0	0	0	0	1	1.0 dB
0	0	0	1	0	2.0 dB
0	0	1	0	0	4.0 dB
0	1	0	0	0	8.0 dB
1	0	0	0	0	16.0 dB
1	1	1	1	1	31.0 dB

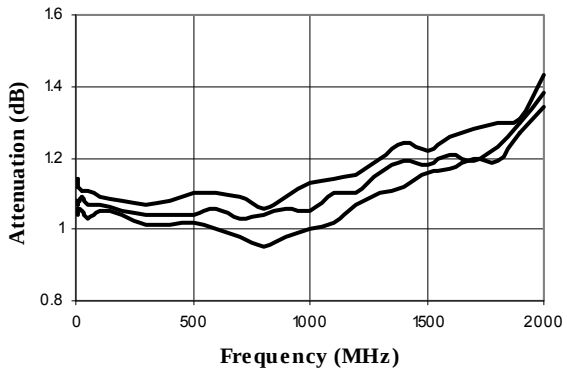
0 = TTL Low; 1 = TTL High

Typical Performance Curves

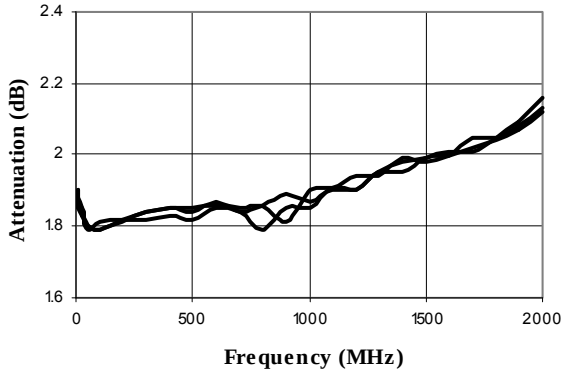
Insertion Loss @ R, H & C



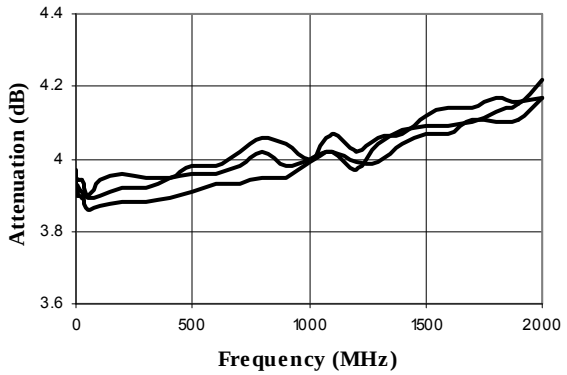
1 dB Bit @ R, H & C



2 dB Bit @ R, H & C



4 dB Bit @ R, H & C



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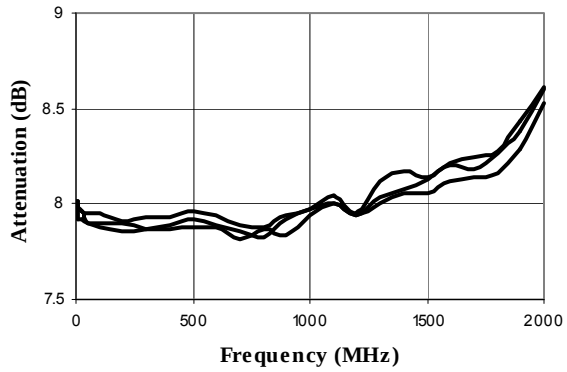
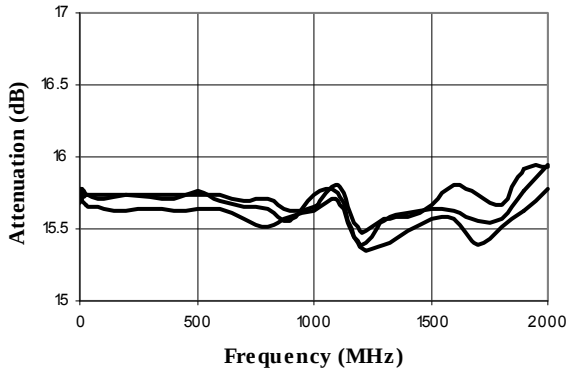
Typical Performance Curves

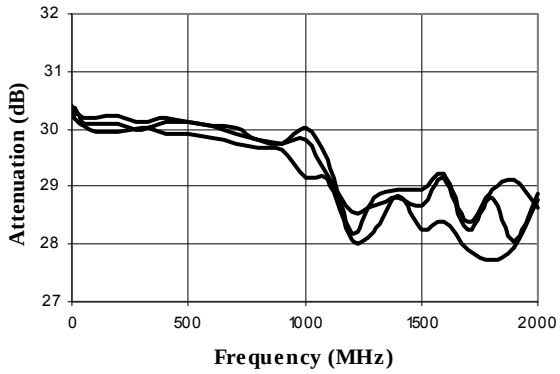
8 dB Bit @ R, H & C



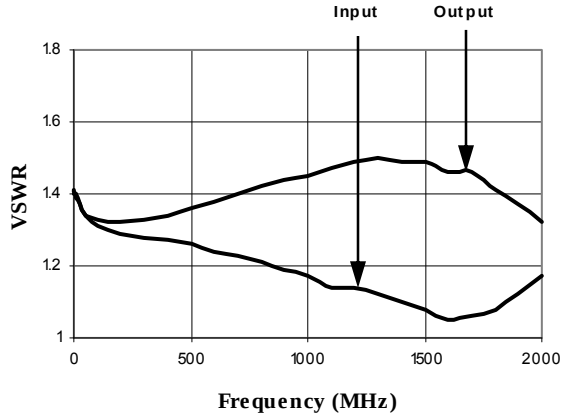
16 dB Bit @ R, H & C



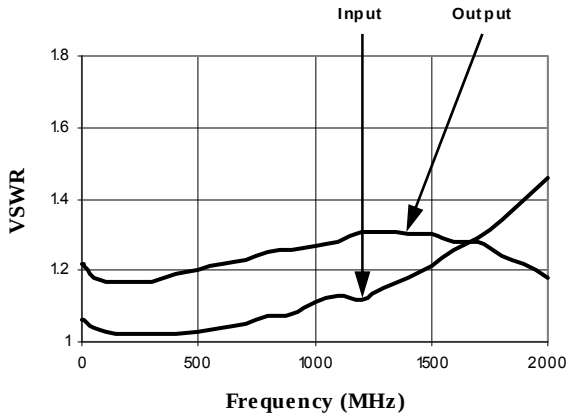
Max Attenuation @ R, H & C



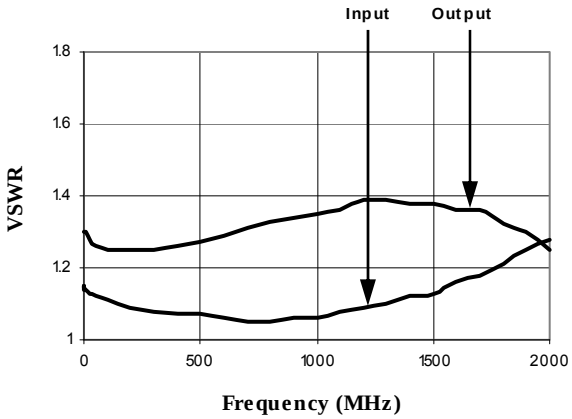
Maximum VSWR over Temp, Loss



Maximum VSWR over Temp, 1 dB Bit



Maximum VSWR over Temp, 2 dB Bit



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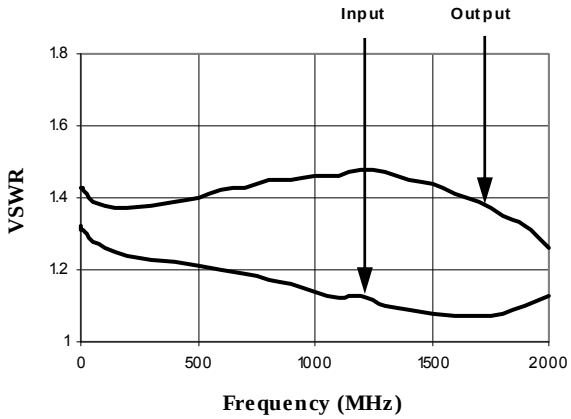
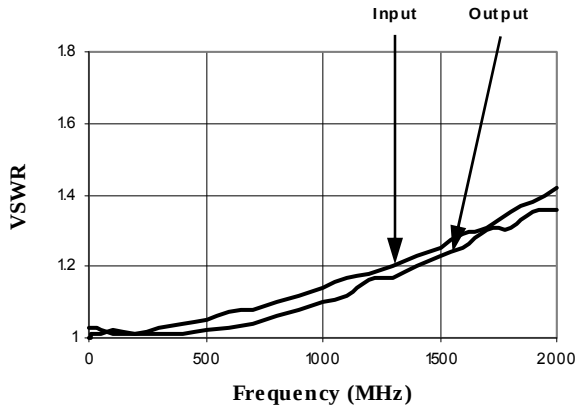
Typical Performance Curves

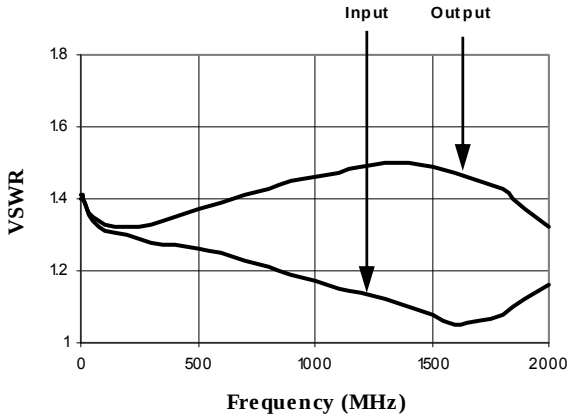
Maximum VSWR over Temp, 4 dB Bit



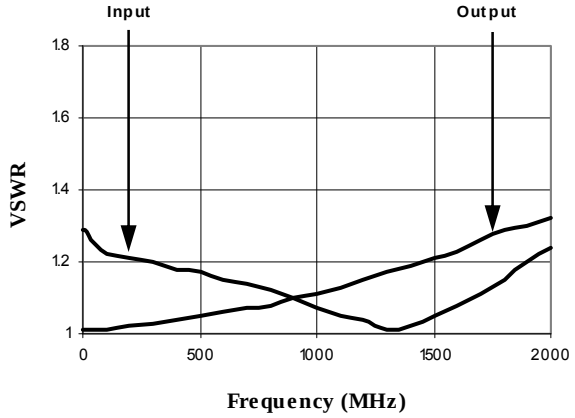
Maximum VSWR over Temp, 8 dB Bit



Maximum VSWR over Temp, 16 dB Bit



Maximum VSWR over Temp, Max Attenuation



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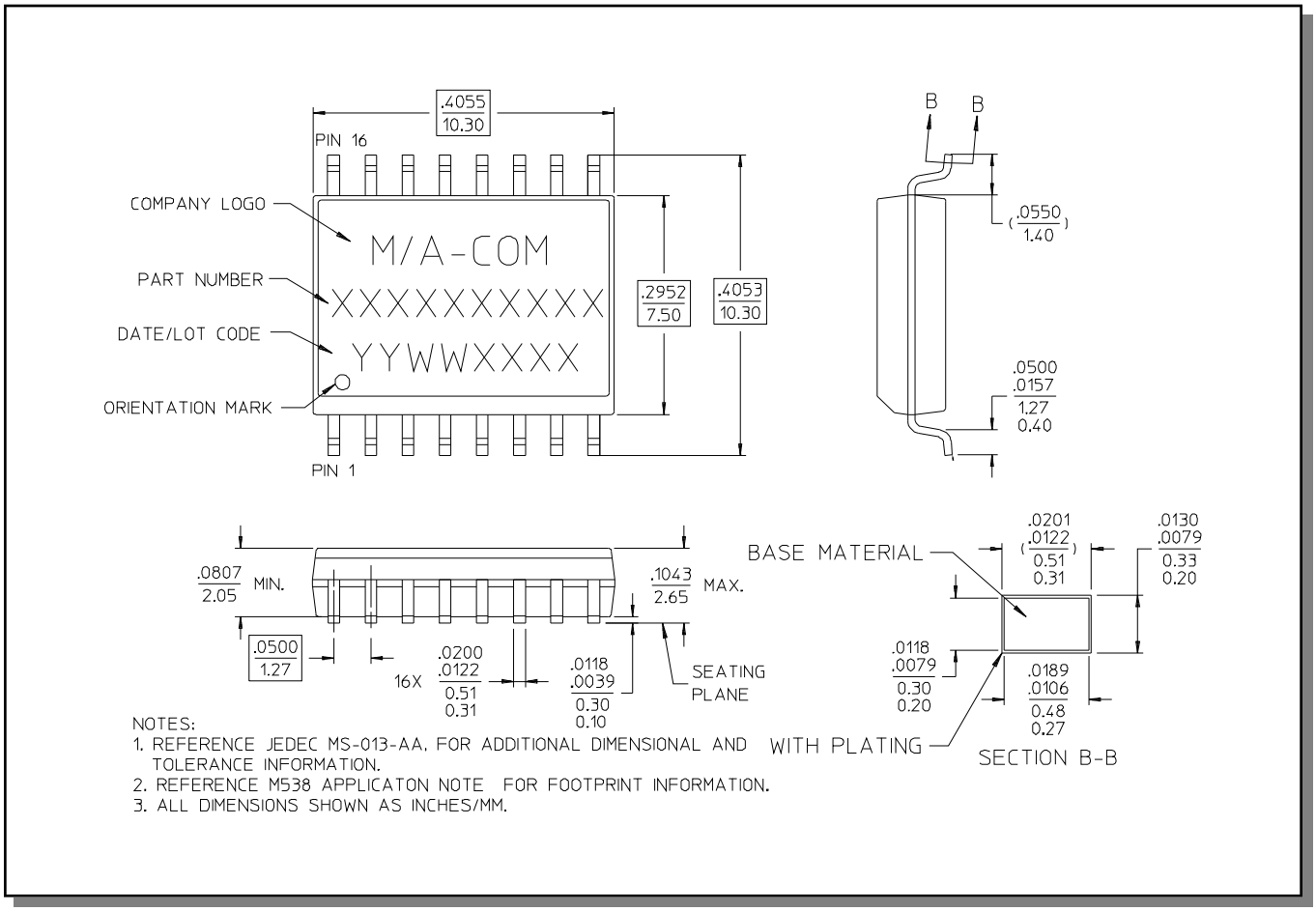
SOW-16†



† Reference Application Note M538 for lead-free solder reflow recommendations.