

Single N-channel MOSFET

ELM32404LA-S

■ General description

ELM32404LA-S uses advanced trench technology to provide excellent $R_{ds(on)}$, low gate charge and low gate resistance.

■ Features

- $V_{ds}=30V$
- $I_d=12A$
- $R_{ds(on)} < 25m\Omega$ ($V_{gs}=10V$)
- $R_{ds(on)} < 37m\Omega$ ($V_{gs}=4.5V$)

■ Maximum absolute ratings

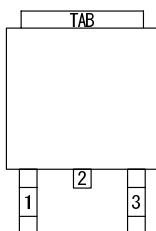
Parameter	Symbol	Limit	Unit	Note
Drain-source voltage	V_{ds}	30	V	
Gate-source voltage	V_{gs}	± 20	V	
Continuous drain current	I_d	12	A	3
		10		
Pulsed drain current	I_{dm}	30	A	3
Power dissipation	P_d	32	W	
		22		
Junction and storage temperature range	T_j, T_{stg}	-55 to 150	°C	

■ Thermal characteristics

Parameter		Symbol	Typ.	Max.	Unit	Note
Maximum junction-to-case	Steady-state	$R\theta_{jc}$		3	°C/W	
Maximum junction-to-ambient	Steady-state	$R\theta_{ja}$		75	°C/W	

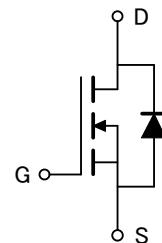
■ Pin configuration

TO-252-3 (TOP VIEW)



Pin No.	Pin name
1	GATE
2	DRAIN
3	SOURCE

■ Circuit



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■ Electrical characteristics

$T_a=25^\circ\text{C}$

Parameter	Symbol	Condition	Min.	Typ.	Max.	Unit	Note
STATIC PARAMETERS							
Drain-source breakdown voltage	BVdss	$\text{Id}=250\ \mu\text{A}, \text{Vgs}=0\text{V}$	30			V	
Zero gate voltage drain current	Idss	$\text{Vds}=24\text{V}, \text{Vgs}=0\text{V}$ $\text{Vds}=20\text{V}, \text{Vgs}=0\text{V}, \text{T}_j=55^\circ\text{C}$		1	10	μA	
Gate-body leakage current	Igss	$\text{Vds}=0\text{V}, \text{Vgs}=\pm 20\text{V}$			± 250	nA	
Gate threshold voltage	Vgs(th)	$\text{Vds}=\text{Vgs}, \text{Id}=250\ \mu\text{A}$	1.0	1.5	2.5	V	
On state drain current	Id(on)	$\text{Vgs}=10\text{V}, \text{Vds}=5\text{V}$	30			A	1
Static drain-source on-resistance	Rds(on)	$\text{Vgs}=10\text{V}, \text{Id}=12\text{A}$ $\text{Vgs}=4.5\text{V}, \text{Id}=6\text{A}$		18 25	25 37	$\text{m}\Omega$ $\text{m}\Omega$	1
Forward transconductance	Gfs	$\text{Vds}=5\text{V}, \text{Id}=12\text{A}$		19		S	1
Diode forward voltage	Vsd	$\text{If}=1\text{A}, \text{Vgs}=0\text{V}$			1	V	1
Max. body-diode continuous current	Is				1.3	A	
Pulsed body-diode current	Ism				2.6	A	3
DYNAMIC PARAMETERS							
Input capacitance	Ciss	$\text{Vgs}=0\text{V}, \text{Vds}=10\text{V}, f=1\text{MHz}$		790		pF	
Output capacitance	Coss			175		pF	
Reverse transfer capacitance	Crss			65		pF	
SWITCHING PARAMETERS							
Total gate charge	Qg	$\text{Vgs}=10\text{V}, \text{Vds}=15\text{V}, \text{Id}=12\text{A}$		16.0		nC	2
Gate-source charge	Qgs			2.5		nC	2
Gate-drain charge	Qgd			2.1		nC	2
Turn-on delay time	td(on)	$\text{Vgs}=10\text{V}, \text{Vds}=10\text{V}, \text{Id} \approx 1\text{A}$ $\text{R}_{\text{gen}}=6\ \Omega$		2.2	4.4	ns	2
Turn-on rise time	tr			7.5	15.0	ns	2
Turn-off delay time	td(off)			11.8	21.3	ns	2
Turn-off fall time	tf			3.7	7.4	ns	2

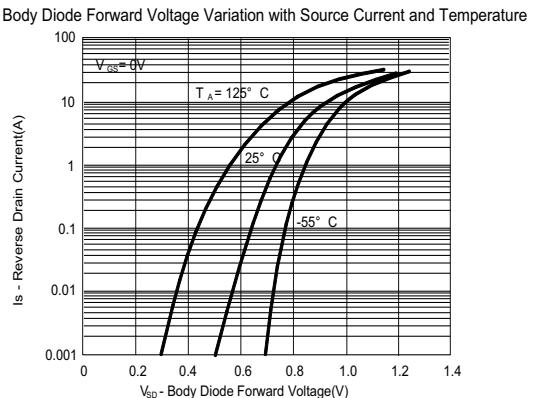
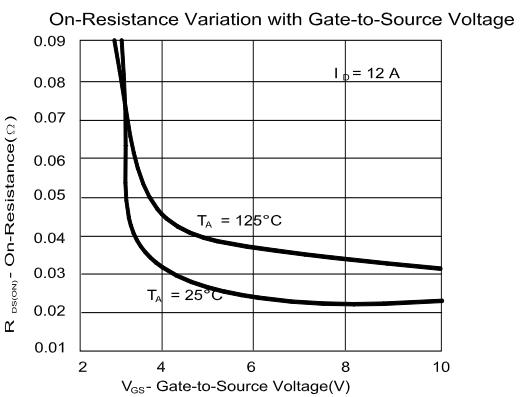
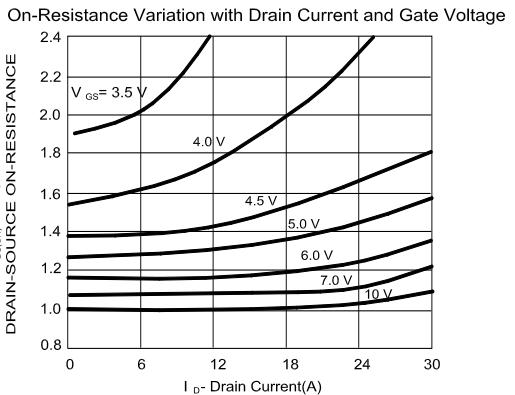
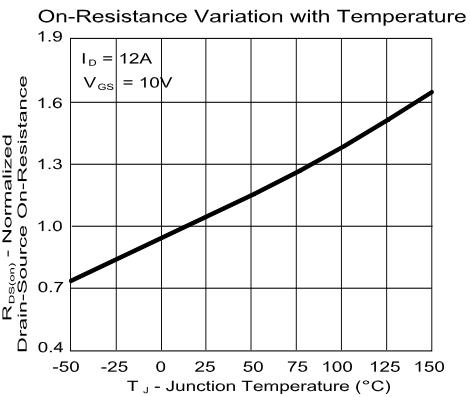
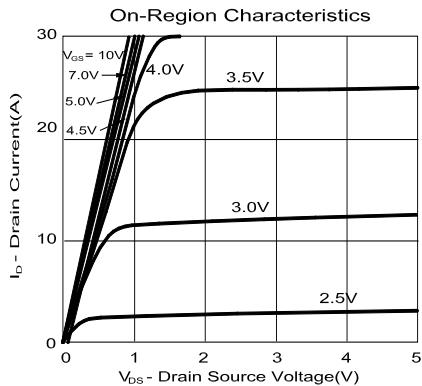
NOTE :

1. Pulse test : Pulsed width $\leq 300\ \mu\text{sec}$ and Duty cycle $\leq 2\%$.
2. Independent of operating temperature.
3. Pulsed width limited by maximum junction temperature.

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■ Typical electrical and thermal characteristics



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