



### 1.1 Features

#### Main LCD panel

Item	Standard Value
Display Type	128 (R · G · B) · 160 Dots
LCD Type	Active matrix TFT, Transmissive type
Screen size(inch)	1.79 (Diagonal)
Viewing Direction	12 O'clock
Color configuration	R,G,B, vertical stripe
Backlight Type	White LED B/L
Interface	16Bits data bus
Driver IC	NT3911 ( Support 65K Colors )

### 1.4 DC Electrical Characteristics

Module		V <sub>SS</sub> = 0V, T <sub>a</sub> = 25°C				
Item	Symbol	Condition	Min.	Typ.	Max.	Unit
Logic Supply Voltage	V <sub>DD</sub>	-	2.6	2.8	3.0	V
Input High Voltage	V <sub>IH</sub>	-	0.7V <sub>DD</sub>	-	V <sub>DD</sub>	V
Input Low Voltage	V <sub>IL</sub>	-	0	-	0.3V <sub>DD</sub>	V
Output High Voltage	V <sub>OH</sub>	-	V <sub>DD</sub> -0.4	-	-	V
Output Low Voltage	V <sub>OL</sub>	-	0	-	0.4	V
Supply Current	I <sub>DD</sub>	V <sub>DD</sub> = 2.8 V	-	4.0	7.5	mA

#### Main LCD

Item	Symbol	Condition	Min.	Typ.	Max.	Unit
LCD Driver Voltage	V <sub>OP</sub>	V <sub>RMS</sub> ( 25°C)	-	5.3	-	V

### 2.2 Interface Pin Description

NO	SYMBOL	FUNCTION																																			
1	LED+	LED backlight power supply : Anode																																			
2	LED+	LED backlight power supply : Anode																																			
3	LED-	LED backlight power supply : Cathode																																			
4	LED-	LED backlight power supply : Cathode																																			
5	IM0	Select the mode interfacing with MPU. <table border="1"> <thead> <tr> <th>IM2</th> <th>IM1</th> <th>IM0</th> <th>MPU interfacing mode</th> <th>DB pins</th> </tr> </thead> <tbody> <tr> <td>GND</td> <td>GND</td> <td>GND</td> <td>68-system 16-bit interface</td> <td>DB17-10,DB8-1</td> </tr> <tr> <td>GND</td> <td>GND</td> <td>Vcc</td> <td>68-system 8-bit interface</td> <td>DB17-10</td> </tr> <tr> <td>GND</td> <td>Vcc</td> <td>GND</td> <td>80-system 16-bit interface</td> <td>DB17-10,DB8-1</td> </tr> <tr> <td>GND</td> <td>Vcc</td> <td>Vcc</td> <td>80-system 8-bit interface</td> <td>DB17-10</td> </tr> <tr> <td>Vcc</td> <td>GND</td> <td>ID</td> <td>Serial Peripheral Interface</td> <td>DB17-10,DB8-1</td> </tr> <tr> <td>Vcc</td> <td>Vcc</td> <td>*</td> <td>Setting disabled</td> <td></td> </tr> </tbody> </table>	IM2	IM1	IM0	MPU interfacing mode	DB pins	GND	GND	GND	68-system 16-bit interface	DB17-10,DB8-1	GND	GND	Vcc	68-system 8-bit interface	DB17-10	GND	Vcc	GND	80-system 16-bit interface	DB17-10,DB8-1	GND	Vcc	Vcc	80-system 8-bit interface	DB17-10	Vcc	GND	ID	Serial Peripheral Interface	DB17-10,DB8-1	Vcc	Vcc	*	Setting disabled	
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6	IM1																																				
7	IM2	In Serial Peripheral Interface mode, IM0/ID pin is used for ID setting for the device code.																																			
8	/Reset	Reset pin. Initialize the LSI at low. Power-on reset required when turning on the power supply. Supply with either one of RESET1,2,3, and leave the unused pins open.																																			
9	D <sub>15</sub>																																				
10	D <sub>14</sub>																																				
11	D <sub>13</sub>																																				
12	D <sub>12</sub>																																				
13	D <sub>11</sub>	18-bit bi-directional data bus.																																			
14	D <sub>10</sub>	8-bit bus interface: DB17-10																																			
15	D <sub>9</sub>	9-bit bus interface: DB17-9																																			
16	D <sub>8</sub>	16-bit bus interface: DB17-10, 8-1																																			
17	D <sub>7</sub>	18-bit bus interface: DB17-0																																			
18	D <sub>6</sub>																																				
19	D <sub>5</sub>	The pins not used for data transfer must be fixed to Vcc or GND.																																			
20	D <sub>4</sub>	Serial data input pin (SDI) to input on the rising edge of SCL signal in SPI mode.																																			
21	D <sub>3</sub>																																				
22	D <sub>2</sub>																																				
23	D <sub>1</sub>																																				
24	D <sub>0</sub>																																				
25	/RD (R/W)	Read/write selection signal in 68-system bus interface. Low: Write, High: Read. Read strobe signal in 80-system bus interface, read data at low. Must be fixed to Vcc or GND in SPI mode.																																			

NO	SYMBOL	FUNCTION
26	/WR (E)	ENABLE signal to activate data read/write operation in 68-system bus interface. Write strobe signal in 80-system bus interface, write data at low. Synchronizing clock signal in SPI mode.
27	RS	Register selection signal. Low: Index/status. High: Control. Must be fixed to Vcc or GND in SPI mode.
28	/CS	Chip selection signal. Low: Select HD66773R and accessible. High: Not select HD66773R and inaccessible. Must be fixed to GND when not used.
29	NC	Dummy.
30	NC	Dummy.
31	Vcc	Logic Vcc: +2.2V ~ +3.3V
32	Vcc	
33	V <sub>DI</sub>	Power supply for analogue circuits. Connect to an external power supply of 2.5V ~ 3.3V.
34	V <sub>DI</sub>	
35	AGND	
36	AGND	Analogue-side ground, AGND: 0 V
37	AGND	
38	GND	Output GND level.
39	GND	Opposing GND for external elements (capacitors, diodes).
40	GND	

Okaya Electric America

Part Number:

**RH128160T-009-F**

Screen Size: 1.8" Diagonal

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