SILICON EPITAXIAL PLANAR SCHOTTKY BARRIER DIODE

## Features

- Small surface mounting type
- High reliability


## Applications

- Low current rectification

PINNING

| PIN | DESCRIPTION |
| :---: | :--- |
| 1 | Cathode |
| 2 | Anode |

Absolute Maximum Ratings $\left(\mathrm{T}_{\mathrm{a}}=25^{\circ} \mathrm{C}\right)$

| Parameter | Symbol | Value | Unit |
| :--- | :---: | :---: | :---: |
| Peak Reverse Voltage | $\mathrm{V}_{\mathrm{RM}}$ | 45 | V |
| Reverse Voltage | $\mathrm{V}_{\mathrm{R}}$ | 40 | V |
| Mean Rectifying Current | $\mathrm{I}_{\mathrm{O}}$ | 0.1 | A |
| Peak Forward Surge Current (60 Hz for 1 Cyc.) | $\mathrm{I}_{\mathrm{FSM}}$ | 1 | A |
| Junction Temperature | $\mathrm{T}_{\mathrm{j}}$ | 125 | ${ }^{\circ} \mathrm{C}$ |
| Storage Temperature Range | $\mathrm{T}_{\mathrm{s}}$ | -40 to +125 | ${ }^{\circ} \mathrm{C}$ |

Characteristics at $\mathrm{T}_{\mathrm{a}}=25^{\circ} \mathrm{C}$

| Parameter | Symbol | Typ. | Max. | Unit |
| :--- | :---: | :---: | :---: | :---: |
| Forward Voltage <br> at $\mathrm{I}_{\mathrm{F}}=100 \mathrm{~mA}$ <br> at $\mathrm{I}_{\mathrm{F}}=10 \mathrm{~mA}$ | $\mathrm{~V}_{\mathrm{F}}$ | - | 0.55 | V |
| Reverse Current <br> at $\mathrm{V}_{\mathrm{R}}=10 \mathrm{~V}$ | $\mathrm{~V}_{\mathrm{F}}$ | - | 0.34 | V |
| Capacitance Between Terminals <br> at $\mathrm{V}_{\mathrm{R}}=10 \mathrm{~V}, \mathrm{f}=1 \mathrm{MHz}$ | $\mathrm{C}_{\mathrm{T}}$ | - | 30 | $\mu \mathrm{~A}$ |

Note: ESD sensitive product handling required.


Fig. 1 Forward characteristics


Fig. 4 Derating curve (mounting on glass epoxy PCBs)


Fig. 2 Reverse characteristics


Fig. 3 Capacitance between terminals characteristics

## PACKAGE OUTLINE



| UNIT | A | $\mathrm{b}_{\mathrm{p}}$ | C | D | E | $\mathrm{H}_{\mathrm{E}}$ |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: |
| mm | 1.10 | 0.40 | 0.15 | 1.80 | 1.35 | 2.80 |
|  | 0.80 | 0.25 | 0.00 | 1.60 | 1.15 | 2.30 |

