## **INK0010AX SERIES**

High speed switching Silicon N-channel MOSFET

### **DESCRIPTION**

INK0010AX is a Silicon N-channel MOSFET.

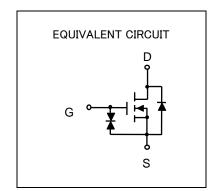
This product is most suitable for low voltage use such as portable machinery, because of low voltage drive and low on resistance.

#### **FEATURE**

- •Input impedance is high, and not necessary to consider a drive electric current.
- Vth is low, and drive by low voltage is possible.
   Vth=1.0~2.0V
- \*Low on Resistance. 
  $$\begin{split} &\text{R}_{\text{DS}}(\text{on})\text{=}4.0\,\Omega\,(\text{TYP})@I_{\text{D}}\text{=}100\text{mA},\,\,\text{V}_{\text{GS}}\text{=}4.0\text{V}\\ &\text{R}_{\text{DS}}(\text{on})\text{=}3.0\,\Omega\,(\text{TYP})@I_{\text{D}}\text{=}100\text{mA},\,\,\text{V}_{\text{GS}}\text{=}10\text{V} \end{split}$$
- ·High speed switching.
- ·Small package for easy mounting.

## **APPLICATION**

High speed switching, Analog switching



## **OUTLINE DRAWING** Unit: mm INK0010AT2(PRELIMINARY) INK0010AM1 2.1 0.425 1.25 8.0 2.0 .3 0.8 JEITA, JEDEC: -JEITA: SC-70 ISAHAYA: T-USM JEDEC: -TERMINAL CONNECTOR TERMINAL CONNECTOR (1): GATE 1:GATE ②:SOURCE ②:SOURCE 3: DRAIN 3:DRAIN INK0010AU1 INK0010AC1 1.6 2.5 0.4 8.0 0.4 0.5 90 1.6 0. JEITA: SC-75A JEITA: SC-59 JEDEC: -JEDEC: Similar to TO-236 TERMINAL CONNECTOR T TERMINAL CONNECTOR 1:GATE 1:GATE 2:SOURCE 2:SOURCE 3: DRAIN 3: DRAIN

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## MAXIMUM RATING(Ta=25°C)

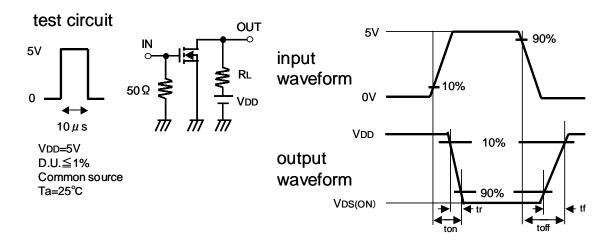
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SYMBOL	PARAMETER	RATING				
STWIDOL	PARAMETER	INK0010AT2	INK0010AU1	INK0010AM1	INK0010AC1	UNIT
V <sub>DSS</sub>	Drain-source voltage	60				
$V_{GSS}$	Gate-source voltage	±20				
Ι <sub>D</sub>	Drain current	100				
P <sub>D</sub>	Total power dissipation (Ta=25°C)	125(※)	150	200		mW
Tch	Channel temperature	+125	+150			°C
Tstg	Range of Storage temperature	−55 <b>~</b> +125	−55 <b>~</b> +150			°C

## ELECTRICAL CHARACTERISTICS (Ta=25°C)

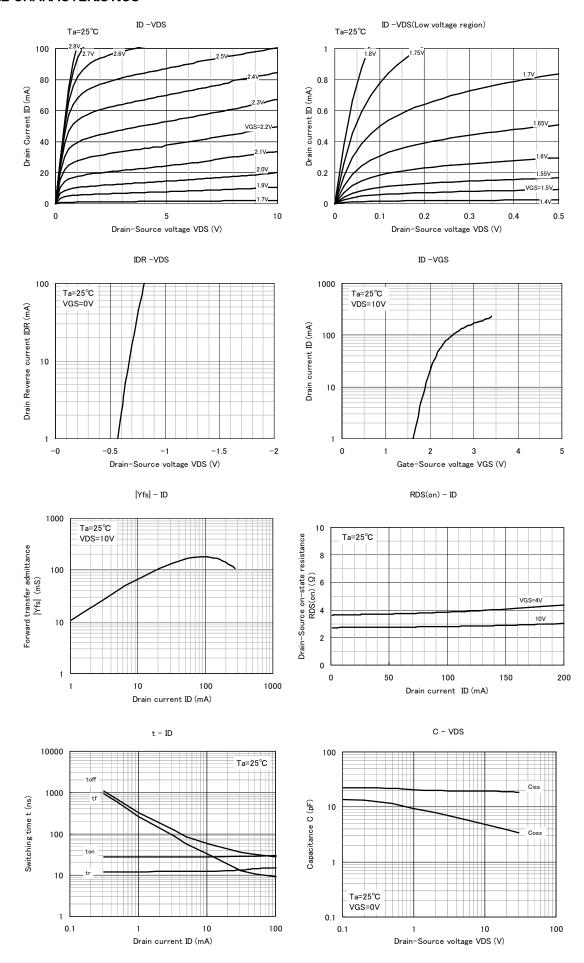
※package mounted on 9mm × 19mm × 1mm glass-epoxy substrate.

SYMBOL	PARAMETER	TEST CONDITION	LIMIT			UNIT	
			MIN	TYP	MAX	UNIT	
$V_{(BR)DSS}$	Drain-source breakdown voltage	$I_{D} = 100 \mu$ A, V $_{GS} = 0$ V	60	_	-	V	
I <sub>GSS</sub>	Gate-source leak current	$V_{gS} = \pm 15V, V_{DS} = 0V$	_	-	±1.0	μΑ	
I <sub>DSS</sub>	Zero gate voltage drain current	V <sub>DS</sub> =60V ,V <sub>GS</sub> =0V	_	-	1.0	μΑ	
$V_{th}$	Gate threshold voltage	I $_{\rm D}$ =250 $\mu$ A, V $_{\rm DS}$ = V $_{\rm GS}$	1.0	-	2.0	٧	
Y <sub>fs</sub>	Forward transfer admittance	V <sub>DS</sub> =10V, I <sub>D</sub> =100mA	_	200	_	mS	
R <sub>DS(on)</sub>	Static drain-source on-state resistance	I <sub>D</sub> =100mA, V <sub>GS</sub> =4.0V	_	4.0	_	Ω	
		I <sub>D</sub> =100mA, V <sub>GS</sub> =10.0V	_	3.0	-		
Ciss	Input capacitance	V <sub>DS</sub> =10V, V <sub>GS</sub> =0V,f=1MHz	_	20	_	pF	
Coss	Output capacitance	V <sub>DS</sub> =10V, V <sub>GS</sub> =0V,f=1MHz	-	5.0	-	pF	
ton	Switching time	V <sub>DD</sub> =5V , I <sub>D</sub> =10mA	_	27	-		
toff		V <sub>GS</sub> =0∼5V	_	58	_	ns	

## Switching time test condition



### TYPICAL CHARACTERISTICS





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