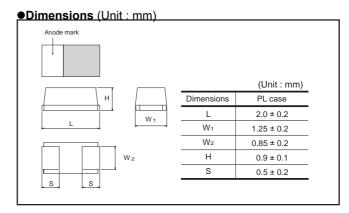


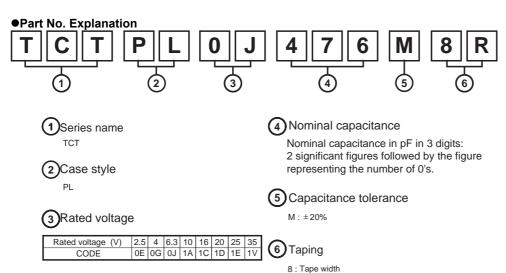
Chip tantalum capacitors (Bottom surface electrode type)

TCT Series PL Case

●Features (PL)

- 1) Vital for all hybrid integrated circuits board application.
- 2) Wide capacitance range.
- 3) Screening by thermal shock.





* This specification has possibility of charge, due to underdevelopment product. Please ask for latest specification to our sales.

R : Positive electrode on the side opposite to sprocket hole

TCT Series PL Case Data Sheet

Rated table

			I	Rated vo	ltage (V)		
(μF)	2.5 0E	4 0G	6.3 0J	10 1A	16 1C	20 1D	25 1E	35 1V
1.0 (105)								*PL
1.5 (155)							*PL	
2.2 (225)							*PL	
3.3 (335)						*PL		
4.7 (475)						*PL		
6.8 (685)						*PL		
10 (106)					*PL			
15 (156)				*PL				
22 (226)				PL				
33 (336)			PL	*PL				
47 (476)		PL	PL					
68 (686)	*PL	NEW PL	*PL					
100 (107)	*PL	PL						
150 (157)	*PL							
220 (227)								
330 (337)								
470 (477)								

Remark) Case size codes (PL) in the above show products line-up.

Marking

The indications listed below should be given on the surface of a capacitor.

- (1) Polarity : The polarity should be shown by □ bar. (on the anode side)
 (2) Rated DC voltage : Due to the small size of PL case, a voltage code is used as shown below.
 (3) Visual typical example (1) voltage code (2) capacitance code

Voltage Code	Rated DC Voltage (V)
е	2.5
g	4
j	6.3
А	10
С	16
D	20
E	25
V	35

Capacitance Code	Nominal Capacitance (μF)				
Α	1.0				
Е	1.5				
J	2.2				
N	3.3				
S	4.7				
W	6.8				
а	10				
е	15				
j	22				
n	33				
S	47				
W	68				
ā	100				
	•				

[PL case] note 1) $\overline{(1)}$ $\overline{(2)}$



manufacture code note 2) voltage code and capacitance code are variable with parts number

^{*} Under development

TCT Series PL Case Data Sheet

Characteristics

Cilaracter																	
Iter	n	Performance						Test	con	dit	tions (based o	on JIS C 5101-	1 and JIS C 5101–3)				
Operating Temp	perature	-5	5°C	to +	-125	°C						Voltage reduction when temperature exceeds +85°C					
Maximum operat temperature with derating	ing no voltage	+8	5°C														
Rated voltage (VDC)	2.5	4	6.3	10	10	3 20	25	3	5		at 8	5°C				
Category voltag	e (VDC)	1.6	2.5	4	6.3	10	13	16	2	2		at 12	25°C	;			
Surge voltage (VDC)	3.2	5.2	8	13	20	26	33	4	4		at 8	5°C				
DC Leakage cu	rrent			e sa dard			the v	oltaç	ge	on		As p	er 4.	.5.	JIS C 5101-1 .1 JIS C 5101 Rated voltage	-3	
Capacitance tol	erance		all b	e sa	atisfi	ed	allow	ance	e r	ange.		As p Mea Mea	As per 4.7 JIS C 5101-1 As per 4.5.2 JIS C 5101-3 Measuring frequency: 120±12Hz Measuring voltage : 0.5Vrms +1.5 to 2V.DC Measuring circuit : DC Equivalent series circuit				
Tangent of loss (Df, tan δ)	angle	Shall be satisfied the voltage on " Standard list "						As p Mea Mea	As per 4.8 JIS C 5101-1 As per 4.5.3 JIS C 5101-3 Measuring frequency: 120±12Hz Measuring voltage : 0.5Vrms +1.5 to 2V.DC Measuring circuit : DC Equivalent series circuit								
Impedance		Shall be satisfied the voltage on " Standard list "						As p Mea Mea	As per 4.10 JIS C 5101-1 As per 4.5.4 JIS C 5101-3 Measuring frequency: 100±10kHz Measuring voltage : 0.5Vrms or less Measuring circuit : DC Equivalent series circuit								
Resistance to Soldering heat	Appearance						no sig hould				rmality.	As per 4.6 JIS C 5101-3					
	L.C.	Le	ss t	han	200	%	of init	ial lir	mi	t		Sol	Dip in the solder bath Solder temp : 260±5°C				
	ΔC / C	Within ±20% of initial value										Duration : 5±0.5s Repetition : 1					
	Df (tan δ)	Less than 200% of initial limit							t		After the specimens, leave it at room temperature for over 24h and then measure the sample.						
Temperature cycle	Appearance						no siç hould				rmality.	As p	As per 4.16 JIS C 5101-1 As per 4.10 JIS C 5101-3				
	L.C.	Le	ss t	han	200	%	of init	ial lir	mi	t					: 5 cycles steps 1 to 4) w	vithout discontin	uation
	ΔC / C	Within ±20% of initial value							\ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \	(1 cycle : steps 1 to 4) without discontinuation. Temp. Time							
	Df (tan δ)	Le	ss t	han	200	%	of init	ial lir	mit	<u> </u>			1		-55±3°C	30±3min.	
	(13 0)												2		Room temp.	3min. or less	
													3		125±2°C	30±3min.	
													4		Room temp.	3min. or less	
											After the specimens, leave it at room to over 24h and then measure the sample						
Moisture resistance	Appearance						no siç hould				rmality.	As per 4.22 JIS C 5101-1 As per 4.12 JIS C 5101-3					
	L.C.	Le	ss t	han	200	%	of init	ial lir	mi	t						under such atr	
	ΔC / C	Wi	ithin	±20)% o	f ir	nitial v	alue	— Э			condition that the temperature and humidity are 60±2°C and 90 to 95% RH, respectively, for 500±12h					
	Df (tan δ)	Within ±20% of initial value Less than 200% of initial limit				leave it at room temperature for over 24h and then measure the sample.											

TCT Series PL Case Data Sheet

Iter	n	Performance	Test conditions (based on JIS C 5101–1 and JIS C 5101–3				
Temperature	Temp.	_55°C	As per 4.29 JIS C 5101-1				
Stability	ΔC / C	Within +15/0% of initial value	As per 4.13 JIS C 5101-3				
	Df (tan δ)	Shall be satisfied the voltage on " Standard list "					
	L.C.	-					
	Temp.	+85°C					
	ΔC / C	Within +15/0% of initial value					
	Df (tan δ)	Shall be satisfied the voltage on " Standard list "					
	L.C.	Less than 0.5CV					
	Temp.	+125°C					
	ΔC / C	Within +20/0% of initial value					
	Df (tan δ)	Shall be satisfied the voltage on " Standard list "					
	L.C.	Less than 0.625CV					
Surge voltage	Appearance	There should be no significant abnormality.	As per 4.26JIS C 5101-1 As per 4.14JIS C 5101-3 Apply the specified surge voltage every 5±0.5 min. for 30±5 s. each time in the atmospheric condition of 85±2°C. Repeat this procedure 1,000 times.				
	L.C.	Less than 200% of initial value					
	ΔC / C	Within ±20% of initial value					
	Df (tan δ)	Less than 200% of initial limit	After the specimens, leave it at room temperature for over 24h and then measure the sample.				
Loading at High temperature	Appearance	There should be no significant abnormality.	As per 4.23 JIS C 5101-1 As per 4.15 JIS C 5101-3 After applying the rated voltage for $2000+72/0$ h without discontinuation via the serial resistance of 3Ω or less at a temperature of $85\pm2^{\circ}$ C, leave the sample at room				
r ligir terriperature	L.C.	Less than 200% of initial limit					
	ΔC / C	Within ±20% of initial value					
	Df (tan δ)	Less than 200% of initial limit	temperature / humidity for over 24h and measure the value				
Terminal	Capacitance	The measured value should be stable.	As per 4.35 JIS C 5101-1				
strength	Appearance	There should be no significant abnormality.	As per 4.9 JIS C 5101-3 A force is applied to the terminal until it bends to 1mm and by a prescribed tool maintain the condition for 5s. (See the figure below) (Unit: mm) F (Apply force) thickness=1.6mm				

Ite	em	Performance	Test conditions (JIS C 5101–1 and JIS C 5101–3)			
Adhesiveness		The terminal should not come off.	As per 4.34 JIS C 5101-1 As per 4.8 JIS C 5101-3 Apply force of 5N in the two directions shown in the figure below for 10±1s after mounting the terminal on a circuit board.			
Dimensions		Refer to "External dimensions"	Measure using a caliper of JIS B 7507 Class 2 or higher grade.			
Resistance to solvents		The indication should be clear	As per 4.32 JIS C 5101-1 As per 4.18 JIS C 5101-3 Dip in the isopropyl alcohol for 30±5s, at room temperature.			
Solderability		3/4 or more surface area of the solder coated terminal dipped in the soldering bath should be covered with the new solder.	As per 4.15.2 JIS C 5101-1 As per 4.7 JIS C 5101-3 Dip speed=25±2.5mm / s Pre-treatment (accelerated aging): Leave the sample on the boiling distilled water for 1 h. Solder temp.: 245±5°C Duration : 3±0.5s Solder : M705 Flux : Rosin 25% IPA 75%			
Vibration	Capacitance	Measure value should not fluctuate during the measurement.	As per 4.17 JIS C 5101-1 Frequency: 10 to 55 to 10Hz/min. Amplitude: 1.5mm			
	Appearance	There should be no significant abnormality.	Time : 2h each in X and Y directions Mounting : The terminal is soldered on a print circuit board			

• Standard products list, TCT series

	Part No.	Rated voltage 85°C	Category voltage 125°C	Surge voltage 85°C	Cap. 120Hz	Tolerance	Leakage current 25°C		Df 120Hz (%)		Impedance 100kHz
		(V)	(V)	(V)	(μ F)	(%)	1WV.5min (μA)	–55°C	25°C 85°C	125°C	(Ω)
*	TCT PL 0E 686 □	2.5	1.6	3.3	68	±20	8.5	60	30	40	4
*	TCT PL 0E 107 □	2.5	1.6	3.3	100	±20	12.5	60	30	40	4
*	TCT PL 0E 157 □	2.5	1.6	3.3	150	±20	18.8	60	30	40	4
	TCT PL 0G 476 □	4	2.5	5.2	47	±20	9.4	30	20	30	4
	TCT PL 0G 686 □	4	2.5	5.2	68	±20	13.6	60	30	40	4
	TCT PL 0G 107 □	4	2.5	5.2	100	±20	20.0	60	30	40	4
	TCT PL 0J 336 □	6.3	4	8	33	±20	10.4	30	20	30	4
	TCT PL 0J 476 □	6.3	4	8	47	±20	14.8	60	30	40	4
*	TCT PL 0J 686 □	6.3	4	8	68	±20	21.4	60	30	40	4
*	TCT PL 1A 156□	10	6.3	13	15	±20	3.0	30	20	30	6
	TCT PL 1A 226□	10	6.3	13	22	±20	4.4	30	20	30	5
*	TCT PL 1A 336□	10	6.3	13	33	±20	16.5	60	30	40	4
*	TCT PL 1C 106 □	16	10	20	10	±20	3.2	30	20	30	6
*	TCT PL 1D 335 □	20	13	26	3.3	±20	1.32	30	20	30	8
*	TCT PL 1D 475 □	20	13	26	4.7	±20	1.88	30	20	30	6
*	TCT PL 1D 685 □	20	13	26	6.8	±20	2.72	30	20	30	6
*	TCT PL 1E 155 □	25	16	33	1.5	±20	0.75	30	20	30	8
*	TCT PL 1E 225 □	25	16	33	2.2	±20	1.10	30	20	30	8
*	TCT PL 1V 105 □	35	22	44	1.0	±20	0.70	30	20	30	8

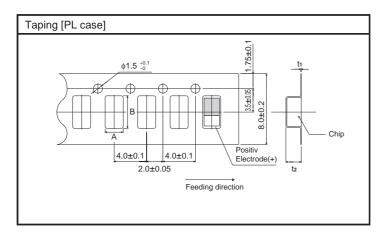
□=Tolerance (M: ±20%)

^{*=} Under development

TCT Series PL Case Data Sheet

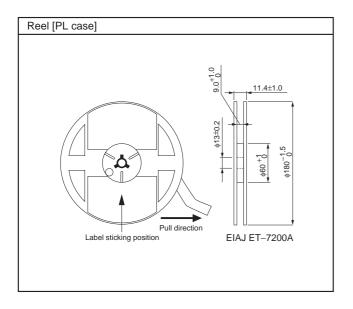
Packaging specifications

Case code	A±0.1	B±0.1	t1±0.05	t2±0.05
PL	1.6	2.4	0.25	1.05



Packaging style

Case code	Packaging	Packaç	ging style	Symbol	Basic ordering units
PL case	Taping	plastic taping	∮180mm Reel	R	3,000pcs



Notes

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