

# MKP, MKL, and MKV Series



- Industry Standard Package
- Wide Frequency Range
- Tape and Reel Available
- 1 pSec Jitter

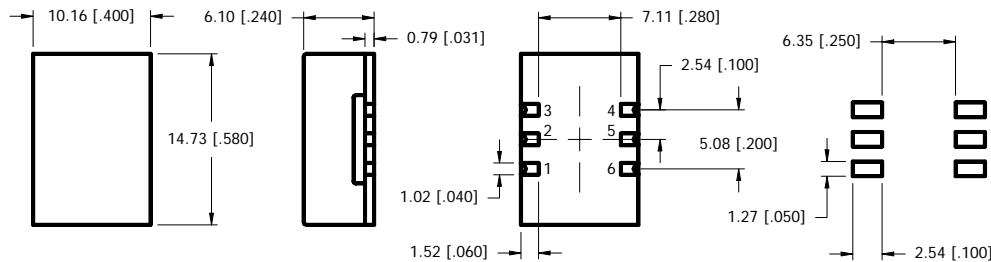


## ELECTRICAL SPECIFICATION:

|  |             | LVDS  | LVPECL              | PECL                |
|--|-------------|---|---------------------|---------------------|
| Frequency Range                        |             | 70.000MHz to 622.080MHz   |                     |                     |
| Frequency Stability*                   |             | (See Part Number Guide for Options)   |                     |                     |
| Operating Temp. Range                  |             | (See Part Number Guide for Options)   |                     |                     |
| Storage Temp. Range                    |             | -55°C to +125°C   |                     |                     |
| Aging                                  |             | ±5 ppm / yr max   |                     |                     |
| Logic "0"                              |             | 1.43V typ   | Vdd - 1.620 VDC max | Vdd - 1.620 VDC max |
| Logic "1"                              |             | 1.10V typ   | Vdd- 1.025 vdc min  | Vdd- 1.025 vdc min  |
| Supply Voltage (Vdd)<br>Supply Current | 2.5VDC ± 5% | 50 mA max   | 50 mA max           | N/A                 |
|  | 3.3VDC ± 5% | 60 mA max   | 60 mA max           | N/A                 |
|  | 5.0VDC ± 5% | N/A   | N/A                 | 140 mA max          |
| Symmetry (50% of waveform)             |             | (See Part Number Guide for Options)   |                     |                     |
| Rise / Fall Time (20% to 80%)          |             | 2nSec max   |                     |                     |
| Load                                   |             | 50 Ohms into Vdd-2.00 VDC   |                     |                     |
| Start Time                             |             | 10mSec max  |                     |                     |
| Jitter (RMS)                           |             | 1 pSec  |                     |                     |
| Tri-State Operation                    |             | Vih = 70% of Vdd min to Enable Output<br>Vil = 30% max or grounded to Disable Output (High Impedance) |                     |                     |

\* Inclusive of Temp., Load, Voltage and Aging

## MECHANICAL DIMENSIONS:

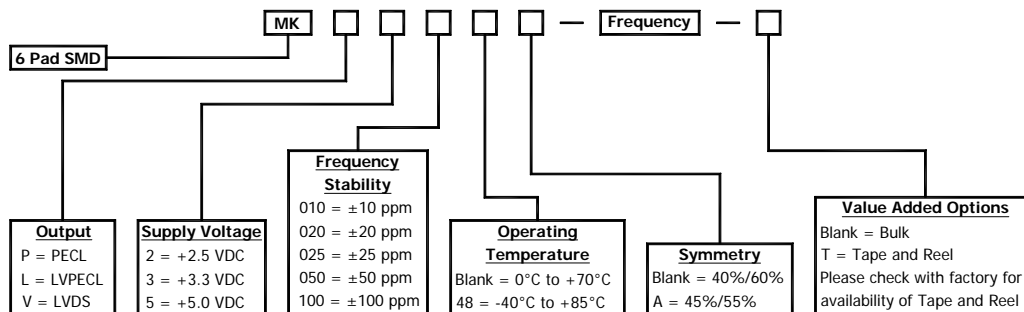


| PIN CONNECTIONS |                |
|-----------------|----------------|
| PIN 1           | NO CONNECTION  |
| PIN 2           | TRI-STATE      |
| PIN 3           | CASE GROUND    |
| PIN 4           | OUTPUT         |
| PIN 5           | COMPL. OUTPUT  |
| PIN 6           | SUPPLY VOLTAGE |

DIMENSION IN BRACKETS ARE IN INCHES  
EXTERNAL BYPASS CAPACITOR IS RECOMMENDED  
UNIT IS NOT HERMETICALLY SEALED

RECOMMENDED  
LAND PATTERN

## PART NUMBERING GUIDE:



Please Consult with MMD Sales Department for any other Parameters or Options

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