

VDSL Transformer

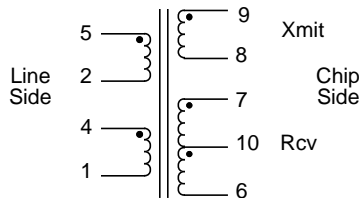
EPD5003GE

- Designed for use with Metalink Chipsets
- Excellent Insertion & Return Loss
 - Isolation : 1500Vrms
- Operating Temperature : -40°C to +85°C

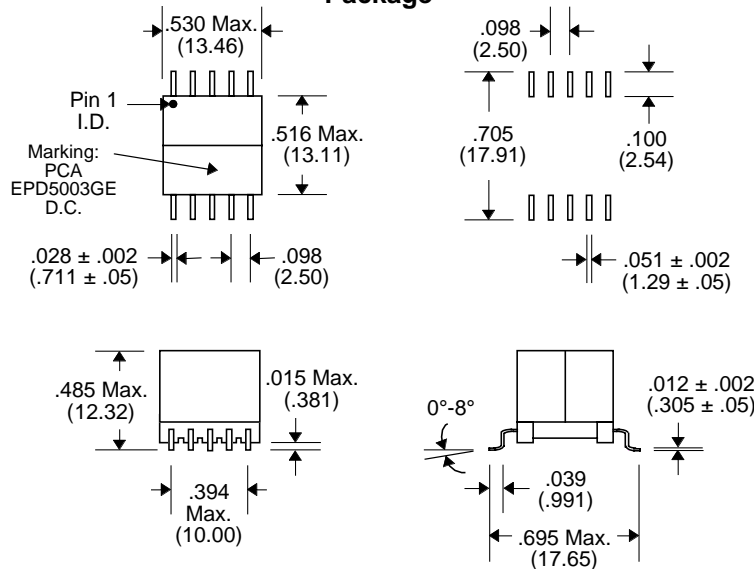
Electrical Parameters @ 25° C

OCL (mH ± 5%) Line Side	InsertionLoss (dB Max.)	Return Loss (dB Min.)		Turns Ratio (Chip:Line)		DCR (Ω Max.) Line Side
		0.12 MHz - 12 MHz	12 MHz - 30 MHz	Xmit	Rcv	
@ 100 KHz, 0.1 Vrms	0.12 MHz - 12 MHz	0.12 MHz - 12 MHz	12 MHz - 30 MHz	Xmit	Rcv	Line
1.13	0.4	-12	-6	1:3.6	2:3.6	1.0
Pins 5-1 with 2-4 shorted				Pins 2-4 shorted		Pins 5-1 with 2-4 shorted

Schematic



Package



Unless Otherwise Specified Dimensions are in Inches /mm ± .010 / .25