

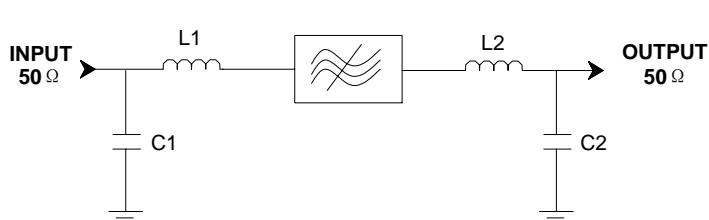
Specifications

Parameter	Unit	Minimum	Typical	Maximum
Center Frequency	MHz	71.95	72	72.05
Insertion Loss	dB	-	9	12.5
1 dB Bandwidth	MHz	-	0.24	-
3 dB Bandwidth	MHz	0.3	0.4	-
40 dB Bandwidth	MHz	-	1.11	1.3
50 dB Bandwidth	MHz	-	1.18	-
Passband Variation($f_0 \pm 0.075\text{MHz}$)	dB	-	0.1	1
Absolute Delay	usec	-	2.37	-
Ultimate Rejection	dB	40	52	-
Material Temperature coefficient	KHz/°C	0.072		
Ambient Temperature	°C	25		
Package Size	DIP2012 (20x12.5x4mm3)			

Notes:


1. All specifications are based on the test circuit shown
2. In production, devices will be tested at room temperature to a guardbanded specification to ensure electrical compliance over temperature
3. Electrical margin has been built into the design to account for the variations due to temperature drift and manufacturing tolerances
4. This is the optimum impedance in order to achieve the performance show

Matching Configuration

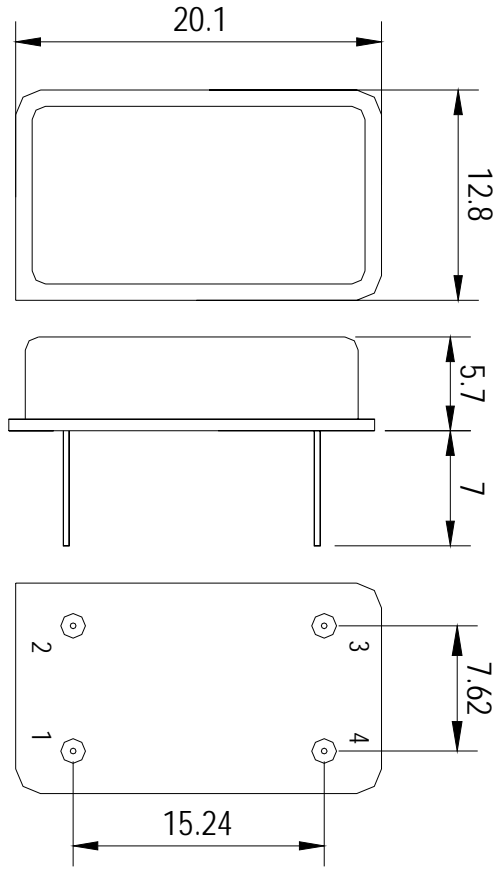


L1=L2=220nH
C1=C2=82pF
Source/Load Impedance=50 ohm

Notes - Component values may change depending on board layout.

	SIPAT Co., Ltd. (CETC No. 26 Research Institute) Nanping Huayuan Road No. 14 Chongqing, China, 400060	Part Number	LBS07203	
		Rev. Date	2006-8-30	
		Rev.	1.0	Page

Package Dimension



Package:DIP2012

Unit:mm

Input	1
Output	4
Ground	2,3

Package: DIP2012

Unit: mm

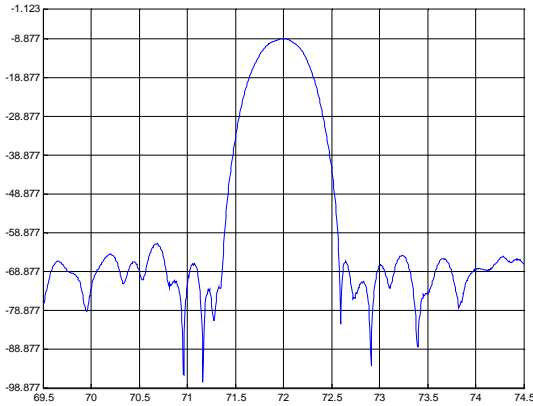


SIPAT Co., Ltd.
(CETC No. 26 Research Institute)
Nanping Huayuan Road No. 14
Chongqing, China, 400060

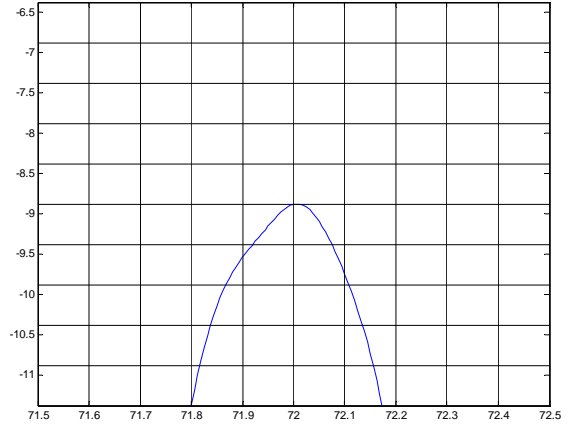
Part Number	LBS07203	
Rev. Date	2006-8-30	
Rev.	1.0	Page 2/3

Typical Performance

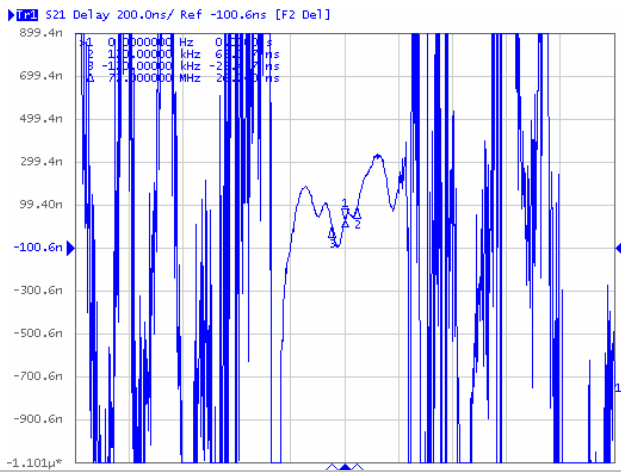
Frequency Respond



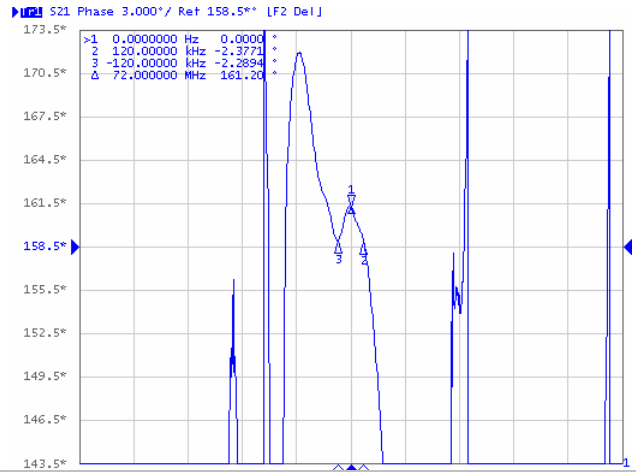
Passband Respond



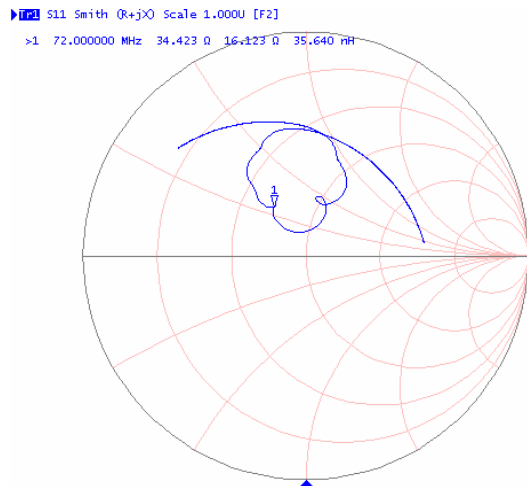
Group Delay Variation($f_0 \pm 0.12\text{MHz}$)



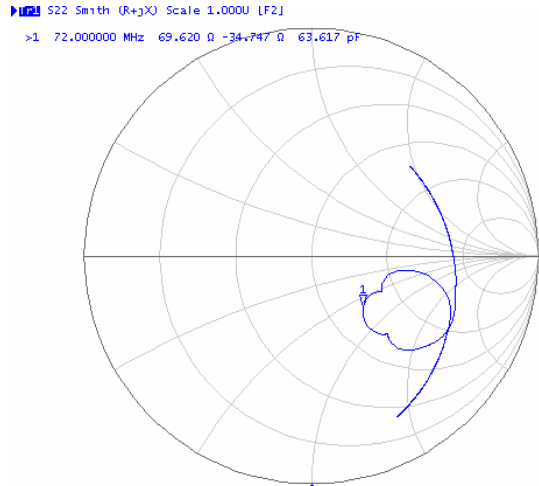
Phase Linearity($f_0 \pm 0.12\text{MHz}$)



Smith Chart S11



Smith Chart S22



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Part Number	LBS07203	
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Rev.	1.0	Page 3/3