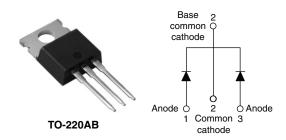




Vishay High Power Products

Schottky Rectifier, 2 x 20 A



PRODUCT SUMMARY				
I _{F(AV)} 2 x 20 A				
V_{R}	80/100 V			

FEATURES

- 175 °C T_J operation
- Center tap configuration
- Low forward voltage drop
- · High frequency operation
- Guard ring for enhanced ruggedness and long term reliability
- High purity, high temperature epoxy encapsulation for enhanced mechanical strength and moisture resistance
- Lead (Pb)-free ("PbF" suffix)
- Designed and qualified for industrial level

DESCRIPTION

This center tap Schottky rectifier series has been optimized for low reverse leakage at high temperature. The proprietary barrier technology allows for reliable operation up to 175 °C junction temperature. Typical applications are in switching power supplies, converters, freewheeling diodes, and reverse battery protection.

MAJOR RATINGS AND CHARACTERISTICS					
SYMBOL	CHARACTERISTICS	CHARACTERISTICS VALUES			
I _{F(AV)}	Rectangular waveform	40	Α		
V _{RRM}		80/100	V		
I _{FSM}	t _p = 5 μs sine	850	Α		
V _F	20 Apk, T _J = 125 °C (per leg)	0.67	V		
T _J	Range	- 55 to 175	°C		

VOLTAGE RATINGS				
PARAMETER	SYMBOL	43CTQ080GPbF	43CTQ100GPbF	UNITS
Maximum DC reverse voltage	V _R	80	100	V
Maximum working peak reverse voltage	V_{RWM}	00	100	V

ABSOLUTE MAXIMUM RATINGS					
PARAMETER	SYMBOL	TEST CONDITIONS		VALUES	UNITS
Maximum average per leg		50 % duty cycle at T _C = 135 °C, rectangular waveform		20	Α
See fig. 5 per device	I _{F(AV)}			40	ζ
Maximum peak one cycle non-repetitive surge current per leg	I _{FSM}	5 μs sine or 3 μs rect. pulse	Following any rated load condition and with rated V _{RRM} applied	850	A
See fig. 7		10 ms sine or 6 ms rect. pulse		275	
Non-repetitive avalanche energy per leg E _{AS}		$T_J = 25 ^{\circ}\text{C}, I_{AS} = 0.50 \text{A}, L = 60 \text{mH}$		7.50	mJ
Repetitive avalanche current per leg I _{AR}		Current decaying linearly to zero in 1 μ s Frequency limited by T_J maximum V_A = 1.5 x V_R typical		0.50	Α

^{*} Pb containing terminations are not RoHS compliant, exemptions may apply

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ELECTRICAL SPECIFICATIONS					
PARAMETER	SYMBOL	TEST CONDITIONS		VALUES	UNITS
Maximum forward voltage drop per leg See fig. 1	V _{FM} ⁽¹⁾	20 A	T _J = 25 °C	0.81	V
		40 A		0.98	
		20 A	T _J = 125 °C	0.67	
		40 A		0.81	
Maximum reverse leakage current per leg	I _{RM} ⁽¹⁾	T _J = 25 °C	V _R = Rated V _R	0.36	m ^
See fig. 2	'RM '''	T _J = 125 °C		13	mA
Threshold voltage	V _{F(TO)}	$T_J = T_J$ maximum		0.71	V
Forward slope resistance	r _t			0.43	mΩ
Maximum junction capacitance per leg	Ст	$V_R = 5 V_{DC}$ (test signal range 100 kHz to 1 MHz) 25 °C		1480	pF
Typical series inductance per leg	L _S	Measured lead to lead 5 mm from package body		8.0	nH
Maximum voltage rate of change	dV/dt	Rated V _R		10 000	V/µs

Note

 $^{^{(1)}\,}$ Pulse width < 300 $\mu s,$ duty cycle < 2 %

THERMAL - MECHANICAL SPECIFICATIONS						
PARAMETER		SYMBOL	TEST CONDITIONS	VALUES	UNITS	
Maximum junction and stora temperature range	ge	T _J , T _{Stg}		- 55 to 175	°C	
Maximum thermal resistance junction to case per leg	€,	D	D. DO wordfur			
Maximum thermal resistance junction to case per package	*	R_{thJC}	DC operation	1.0	°C/W	
Typical thermal resistance, case to heatsink		R _{thCS}	Mounting surface, smooth and greased Only for TO-220	0.50		
Approximate weight				2	g	
Approximate weight				0.07	OZ.	
Mounting torque —	minimum			6 (5)	kgf · cm	
	maximum			12 (10)	(lbf \cdot in)	
			Occupated TO 0004D	43CT	43CTQ080G	
Marking device			Case style TO-220AB	43CT	43CTQ100G	



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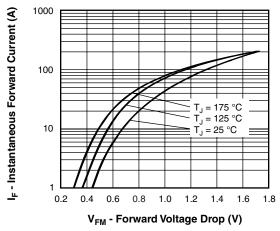


Fig. 1 - Maximum Forward Voltage Drop Characteristics (Per Leg)

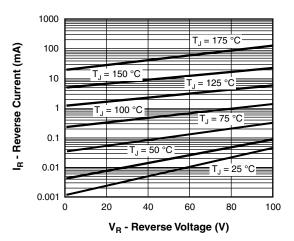


Fig. 2 - Typical Values of Reverse Current vs. Reverse Voltage (Per Leg)

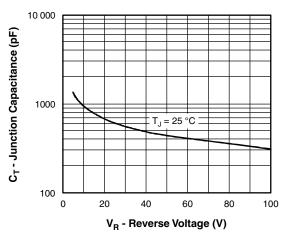


Fig. 3 - Typical Junction Capacitance vs. Reverse Voltage (Per Leg)

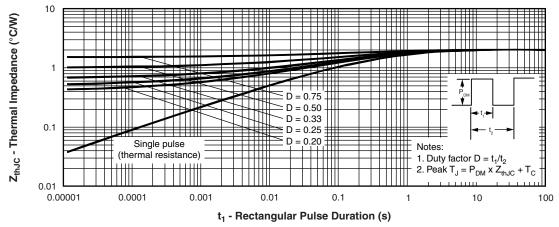


Fig. 4 - Maximum Thermal Impedance Z_{thJC} Characteristics (Per Leg)

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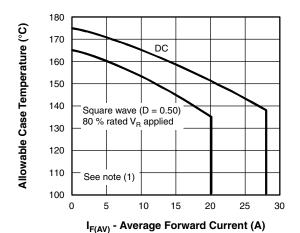


Fig. 5 - Maximum Allowable Case Temperature vs. Average Forward Current (Per Leg)

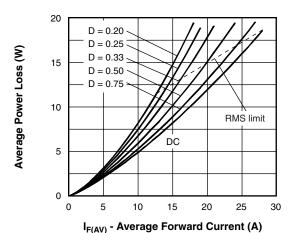


Fig. 6 - Forward Power Loss Characteristics (Per Leg)

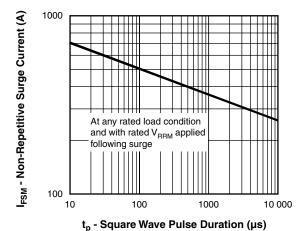


Fig. 7 - Maximum Non-Repetitive Surge Current (Per Leg)

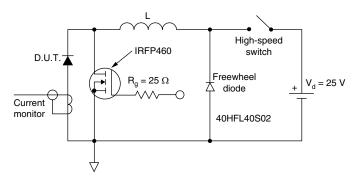


Fig. 8 - Unclamped Inductive Test Circuit

Note

 $^{(1)}$ Formula used: T_C = T_J - (Pd + Pd_{REV}) x R_{thJC}; Pd = Forward power loss = I_{F(AV)} x V_{FM} at (I_{F(AV)}/D) (see fig. 6); Pd_{REV} = Inverse power loss = V_{R1} x I_R (1 - D); I_R at V_{R1} = 10 V

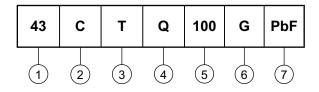


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ORDERING INFORMATION TABLE

Device code



- 1 Current rating (40 = 40 A)
- C = Common cathode
- **3** T = TO-220
- 4 Q = Schottky "Q" series
- 5 Voltage rating 080 = 80 V 100 = 100 V
- 7 • None = Standard production
 - PbF = Lead (Pb)-free

Tube standard pack quantity: 50 pieces

LINKS TO RELATED DOCUMENTS			
Dimensions http://www.vishay.com/doc?95222			
Part marking information	http://www.vishay.com/doc?95225		

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