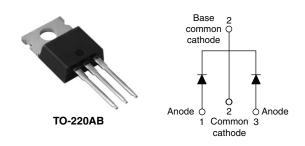


Vishay High Power Products

Schottky Rectifier, 2 x 15 A



PRODUCT SUMMARY				
I _{F(AV)}	2 x 15 A			
V_{R}	80/100 V			

FEATURES

- 175 °C T_J operation
- Center tap configuration
- · Low forward voltage drop
- · High frequency operation
- High purity, high temperature epoxy encapsulation for enhanced mechanical strength and moisture resistance
- Guard ring for enhanced ruggedness and long term reliability
- Designed and qualified for industrial level

DESCRIPTION

The center tap Schottky rectifier series has been optimized for low reverse leakage at high temperature. The proprietary barrier technology allows for reliable operation up to 175 °C junction temperature. Typical applications are in switching power supplies, converters, freewheeling diodes, and reverse battery protection.

MAJOR RATINGS AND CHARACTERISTICS				
SYMBOL	CHARACTERISTICS	VALUES	UNITS	
I _{F(AV)}	Rectangular waveform	30	Α	
V _{RRM}		80/100	V	
I _{FSM}	$t_p = 5 \mu s sine$	850	Α	
V _F	15 Apk, T _J = 125 °C (per leg)	0.67	V	
T _J	Range	- 55 to 175	°C	

VOLTAGE RATINGS					
PARAMETER	SYMBOL	30CTQ080	30CTQ100	UNITS	
Maximum DC reverse voltage	V_{R}	80 100		V	
Maximum working peak reverse voltage	V_{RWM}	80 100		V	

ABSOLUTE MAXIMUM RATINGS					
PARAMETER	SYMBOL	TEST CONDITIONS		VALUES	UNITS
Maximum average per device forward current		50 % duty cycle at T _C = 129 °C, rectangular waveform		30	Α
See fig. 5 per le	I _{F(AV)}			15	
Maximum peak one cycle non-repetitive		5 μs sine or 3 μs rect. pulse	Following any rated load condition and with rated V _{RRM} applied	850	А
surge current per leg See fig. 7	IFSM	10 ms sine or 6 ms rect. pulse		275	
Non-repetitive avalanche energy per leg E _{AS}		$T_J = 25 ^{\circ}\text{C}, I_{AS} = 0.50 \text{A}, L = 60 \text{mH}$		7.50	mJ
Repetitive avalanche current per leg I _{AR}		Current decaying linearly to zero in 1 μ s Frequency limited by T_J maximum $V_A = 1.5$ x V_R typical		0.50	Α

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30CTQ... Series

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ELECTRICAL SPECIFICATIONS					
PARAMETER	SYMBOL	TEST CONDITIONS		VALUES	UNITS
Maximum forward voltage drop per leg See fig. 1	V _{FM} ⁽¹⁾	15 A	T _J = 25 °C	0.86	V
		30 A		1.05	
		15 A	T _J = 125 °C	0.67	
		30 A		0.82	
Maximum reverse leakage current per leg	I _{RM} ⁽¹⁾	T _J = 25 °C	V _R = Rated V _R	0.55	mA
See fig. 2		T _J = 125 °C		7.0	IIIA
Maximum junction capacitance per leg	C _T	$V_R = 5 V_{DC}$ (test signal range 100 kHz to 1 MHz) 25 °C		500	pF
Typical series inductance per leg	L _S	Measured lead to lead 5 mm from package body		8.0	nΗ
Maximum voltage rate of change	dV/dt	Rated V _R 10		10 000	V/µs

Note

 $^{^{(1)}\,}$ Pulse width < 300 µs, duty cycle < 2 %

THERMAL - MECHANICAL SPECIFICATIONS					
PARAMETER		SYMBOL	TEST CONDITIONS	VALUES	UNITS
Maximum junction and storage temperature range	je	T _J , T _{Stg}		- 55 to 175	°C
Maximum thermal resistance junction to case per leg	,	D. DO anaration		3.25	
Maximum thermal resistance junction to case per package	•	- R _{thJC}	DC operation	1.63	°C/W
Typical thermal resistance, case to heatsink		R _{thCS}	Mounting surface, smooth and greased	0.50	
Approximate weight				2	g
				0.07	OZ.
Mounting torque -	minimum			6 (5)	kgf ⋅ cm
	maximum			12 (10)	(lbf ⋅ in)
Marking device			Coop at the TO COOAD	30CTQ080	
			Case style TO-220AB	30CTQ100	

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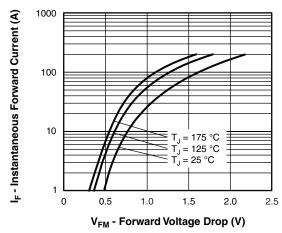


Fig. 1 - Maximum Forward Voltage Drop Characteristics (Per Leg)

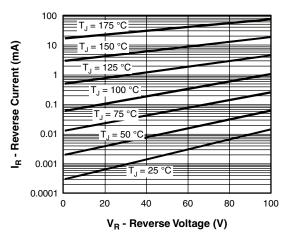


Fig. 2 - Typical Values of Reverse Current vs. Reverse Voltage (Per Leg)

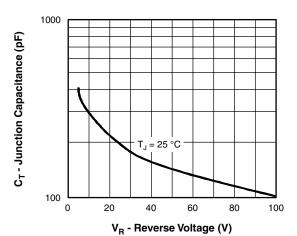


Fig. 3 - Typical Junction Capacitance vs. Reverse Voltage (Per Leg)

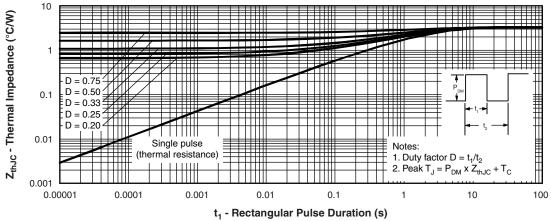


Fig. 4 - Maximum Thermal Impedance Z_{thJC} Characteristics (Per Leg)

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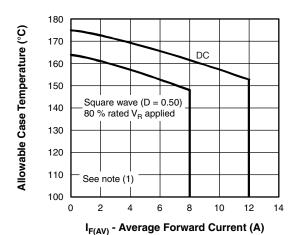


Fig. 5 - Maximum Allowable Case Temperature vs. Average Forward Current (Per Leg)

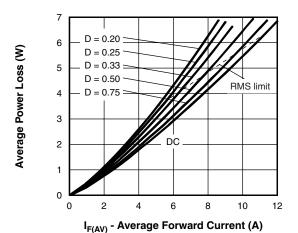


Fig. 6 - Forward Power Loss Characteristics (Per Leg)

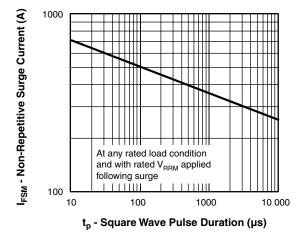


Fig. 7 - Maximum Non-Repetitive Surge Current (Per Leg)

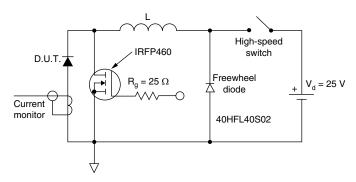


Fig. 8 - Unclamped Inductive Test Circuit

Note

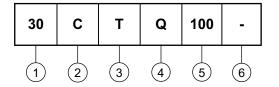
 $^{(1)}$ Formula used: T_C = T_J - (Pd + Pd_{REV}) x R_{thJC}; Pd = Forward power loss = I_{F(AV)} x V_{FM} at (I_{F(AV)}/D) (see fig. 6); Pd_{REV} = Inverse power loss = V_{R1} x I_R (1 - D); I_R at V_{R1} = 10 V



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ORDERING INFORMATION TABLE

Device code



- 1 Current rating (30 = 30 A)
- 2 Circuit configuration:

C = Common cathode

3 - Package

T = TO-220

4 - Schottky "Q" series

080 = 80 V 100 = 100 V

- 5 Voltage ratings
 - PbF = Lead (Pb)-free

• None = Standard production

Tube standard pack quantity: 50 pieces

LINKS TO RELATED DOCUMENTS				
Dimensions http://www.vishay.com/doc?95222				
Part marking information	http://www.vishay.com/doc?95225			



Vishay

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