

# HZU6.2Z

## Silicon Epitaxial Planar Zener Diode for Surge Absorb

REJ03G1218-0200  
(Previous: ADE-208-581A)  
Rev.2.00  
Jun 16, 2005

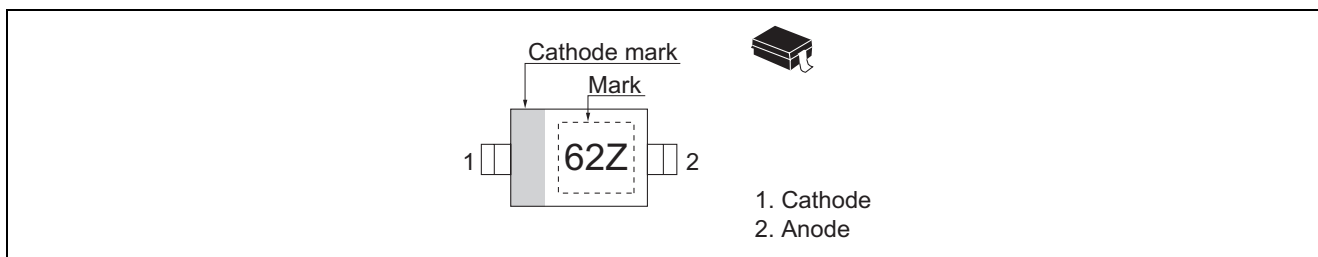
### Features

- Low capacitance ( $C = 8.5 \text{ pF max}$ ) and can protect signal line from ESD.
- Ultra small Resin Package (URP) is suitable for surface mount design.

### Ordering Information

Type No.	Laser Mark	Package Name	Package Code (Previous Code)
HZU6.2Z	62Z	URP	PTSP0002ZA-A (URP)

### Pin Arrangement



## Absolute Maximum Ratings

(Ta = 25°C)

Item	Symbol	Value	Unit
Power dissipation	Pd *	200	mW
Junction temperature	Tj	150	°C
Storage temperature	Tstg	-55 to +150	°C

Note: See Fig.2.

## Electrical Characteristics

(Ta = 25°C)

Item	Symbol	Min	Typ	Max	Unit	Test Condition
Zener voltage	V <sub>Z</sub>	5.90	—	6.50	V	I <sub>Z</sub> = 5 mA, 40 ms pulse
Reverse current	I <sub>R</sub>	—	—	3	μA	V <sub>R</sub> = 5.5 V
Capacitance	C	—	8.0	8.5	pF	V <sub>R</sub> = 0 V, f = 1 MHz
Dynamic resistance	r <sub>d</sub>	—	—	60	Ω	I <sub>Z</sub> = 5 mA

Main Characteristic

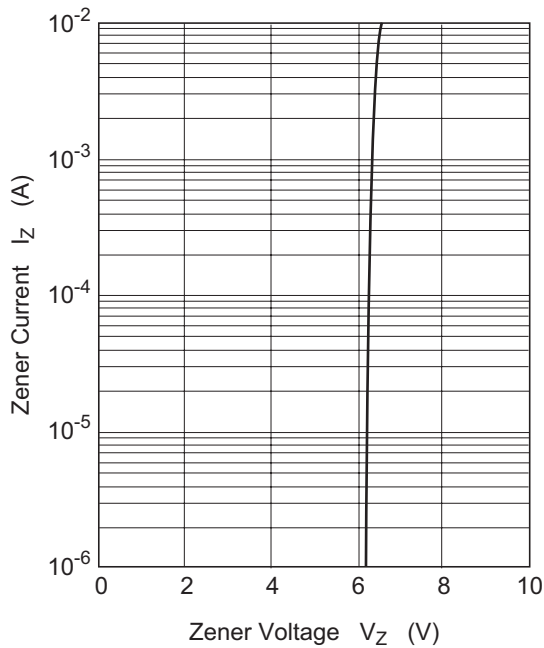


Fig.1 Zener current vs. Zener voltage

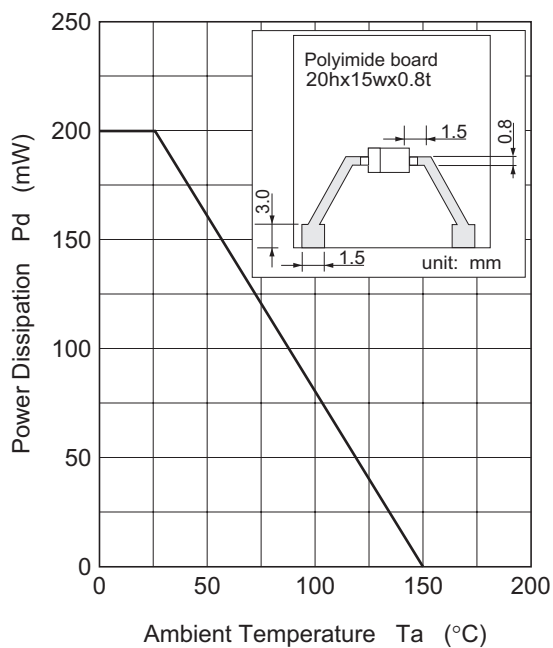


Fig.2 Power Dissipation vs. Ambient Temperature

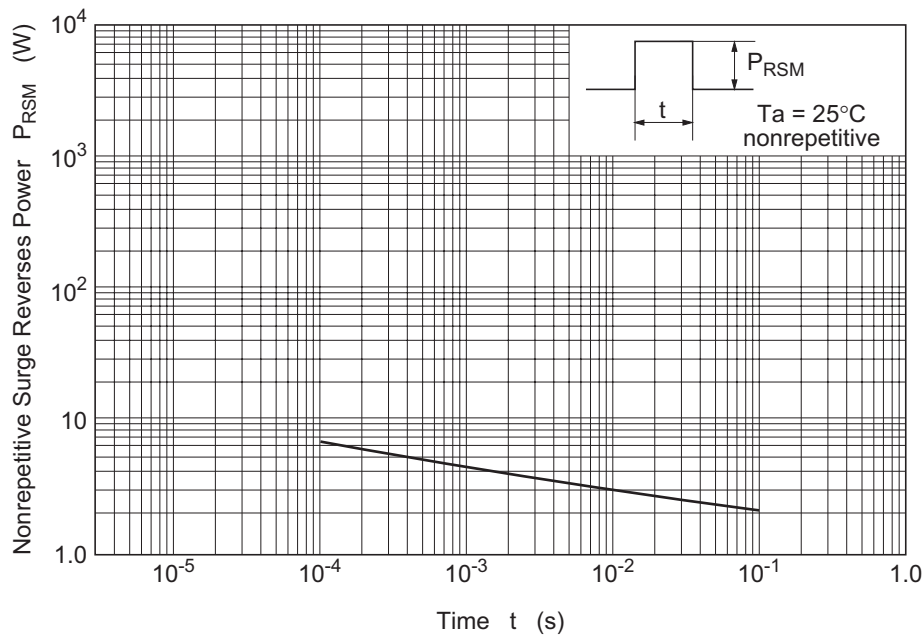


Fig.3 Surge Reverse Power Ratings

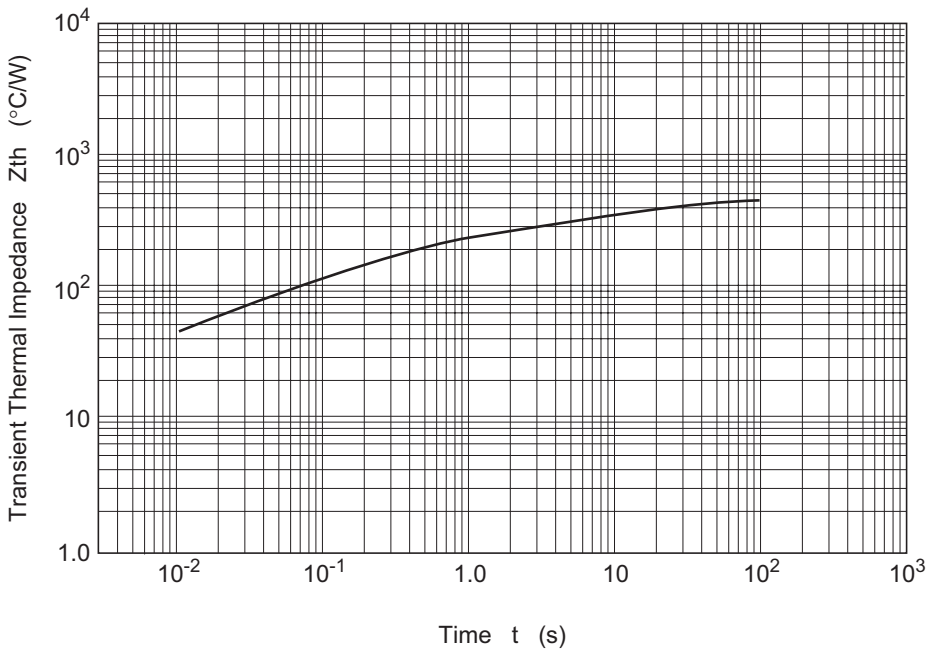
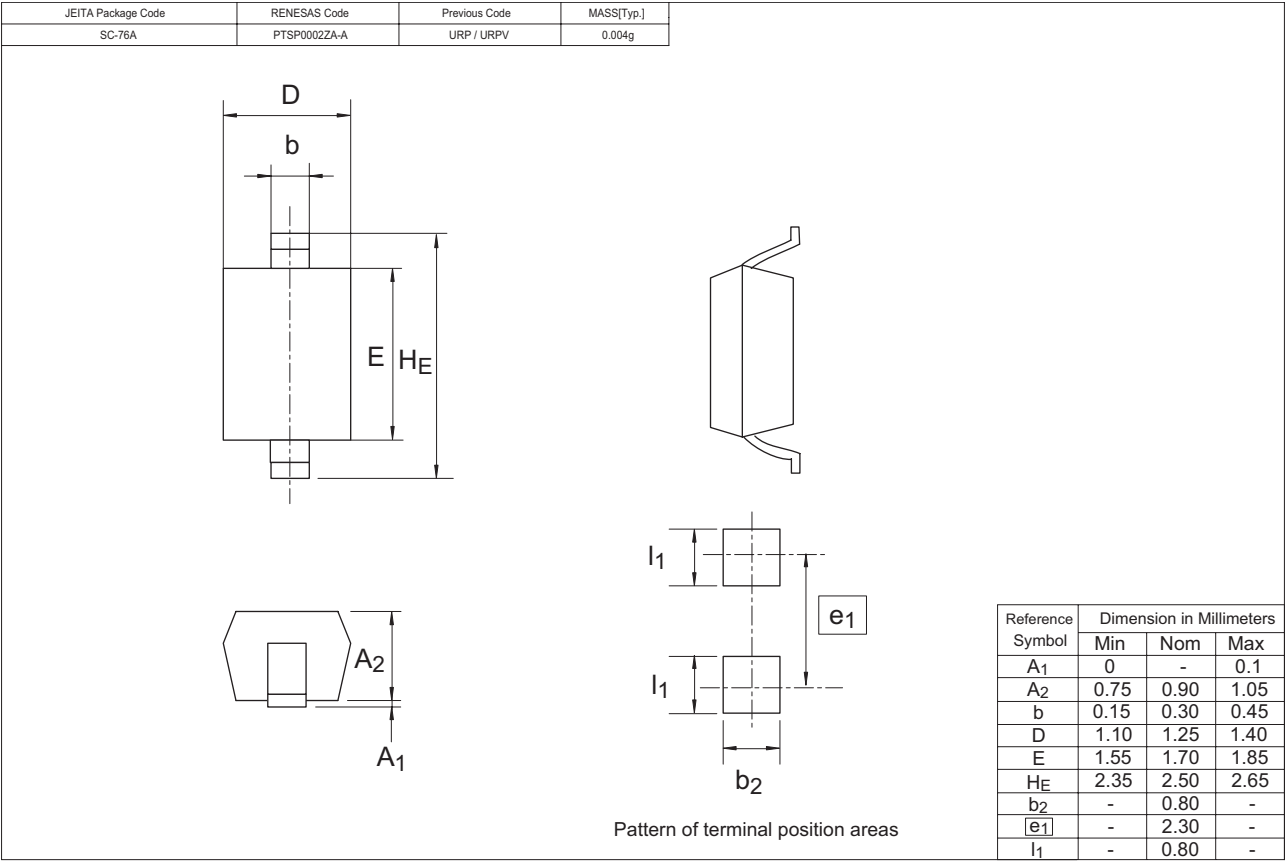


Fig.4 Transient Thermal Impedance

Package Dimensions



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Unit2607 Ruijing Building, No.205 Maoming Road (S), Shanghai 200020, China  
Tel: <86> (21) 6472-1001, Fax: <86> (21) 6415-2952

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1 Harbour Front Avenue, #06-10, Keppel Bay Tower, Singapore 098632  
Tel: <65> 6213-0200, Fax: <65> 6278-8001