

## Silicon NPN Power Transistors

2N6360

## DESCRIPTION

- With TO-3 package
- Low collector saturation voltage
- High DC current gain
- Excellent safe operating area

## APPLICATIONS

- Designed for high power applications and switching circuits such as relay or solenoid drivers, dc to dc converters or inverters.

## PINNING

| PIN | DESCRIPTION |
|-----|-------------|
| 1   | Base        |
| 2   | Emitter     |
| 3   | Collector   |



Fig.1 simplified outline (TO-3) and symbol

Absolute maximum ratings( $T_a = \square$ )

| SYMBOL    | PARAMETER                 | CONDITIONS        | VALUE   | UNIT      |
|-----------|---------------------------|-------------------|---------|-----------|
| $V_{CBO}$ | Collector-base voltage    | Open emitter      | 120     | V         |
| $V_{CEO}$ | Collector-emitter voltage | Open base         | 100     | V         |
| $V_{EBO}$ | Emitter-base voltage      | Open collector    | 7       | V         |
| $I_C$     | Collector current         |                   | 12      | A         |
| $I_{CM}$  | Collector current-peak    |                   | 24      | A         |
| $I_B$     | Base current              |                   | 4       | A         |
| $P_D$     | Total Power Dissipation   | $T_C = 25\square$ | 150     | W         |
| $T_j$     | Junction temperature      |                   | 150     | $\square$ |
| $T_{stg}$ | Storage temperature       |                   | -65~200 | $\square$ |

## THERMAL CHARACTERISTICS

| SYMBOL        | PARAMETER                           | VALUE | UNIT        |
|---------------|-------------------------------------|-------|-------------|
| $R_{th\ j-c}$ | Thermal resistance junction to case | 1.17  | $\square/W$ |

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## CHARACTERISTICS

T<sub>j</sub>=25°C unless otherwise specified

| SYMBOL               | PARAMETER                            | CONDITIONS                                                                  | MIN | TYP. | MAX         | UNIT |
|----------------------|--------------------------------------|-----------------------------------------------------------------------------|-----|------|-------------|------|
| V <sub>(BR)CEO</sub> | Collector-emitter breakdown voltage  | I <sub>C</sub> =0.2A ; I <sub>B</sub> =0                                    | 100 |      |             | V    |
| V <sub>CEsat-1</sub> | Collector-emitter saturation voltage | I <sub>C</sub> =6A ; I <sub>B</sub> =0.6A                                   |     |      | 1.4         | V    |
| V <sub>CEsat-2</sub> | Collector-emitter saturation voltage | I <sub>C</sub> =12A ; I <sub>B</sub> =2.4A                                  |     |      | 4.0         | V    |
| V <sub>BE</sub>      | Base-emitter on voltage              | I <sub>C</sub> =6A ; V <sub>CE</sub> =4V                                    |     |      | 2.2         | V    |
| I <sub>CEO</sub>     | Collector cut-off current            | V <sub>CE</sub> =100V ; I <sub>B</sub> =0                                   |     |      | 2.0         | mA   |
| I <sub>CEX</sub>     | Collector cut-off current            | V <sub>CE</sub> =120V ; V <sub>BE(off)</sub> =1.5V<br>T <sub>C</sub> =150°C |     |      | 2.0<br>10.0 | mA   |
| I <sub>EBO</sub>     | Emitter cut-off current              | V <sub>EB</sub> =7V ; I <sub>C</sub> =0                                     |     |      | 5.0         | mA   |
| h <sub>FE-1</sub>    | DC current gain                      | I <sub>C</sub> =6A ; V <sub>CE</sub> =4V                                    | 15  |      |             |      |
| h <sub>FE-2</sub>    | DC current gain                      | I <sub>C</sub> =12A ; V <sub>CE</sub> =4V                                   | 5   |      |             |      |
| f <sub>T</sub>       | Transition frequency                 | I <sub>C</sub> =1A ; V <sub>CE</sub> =4V                                    | 0.2 |      |             | MHz  |

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## PACKAGE OUTLINE

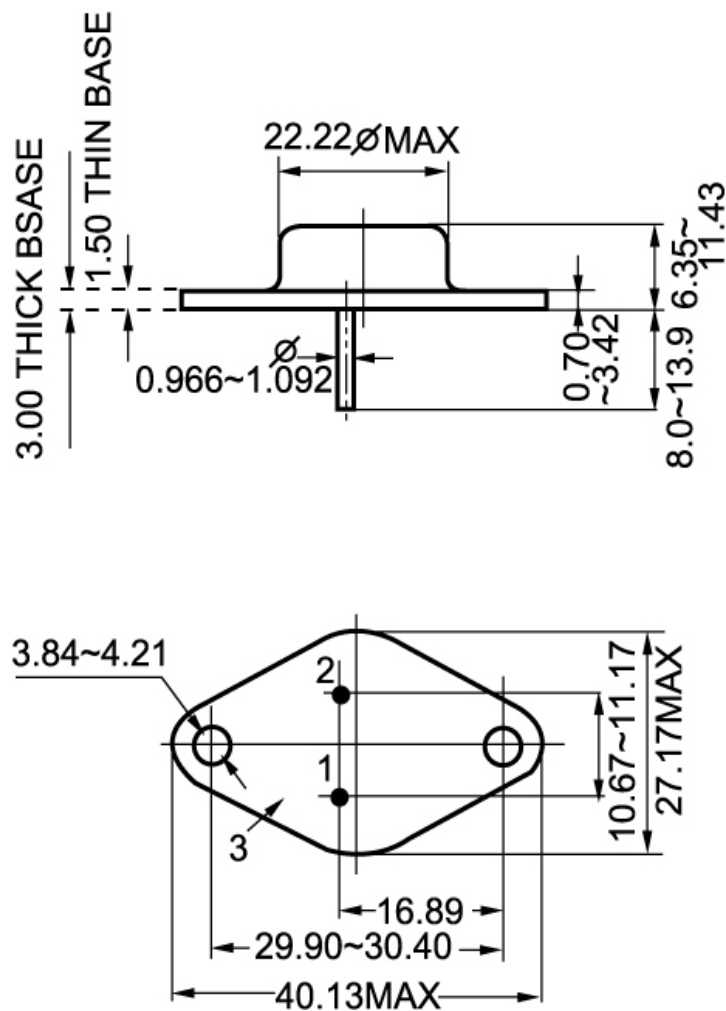


Fig.2 outline dimensions (unindicated tolerance:±0.10mm)