

**Silicon NPN Power Transistors****MJF18008****DESCRIPTION**

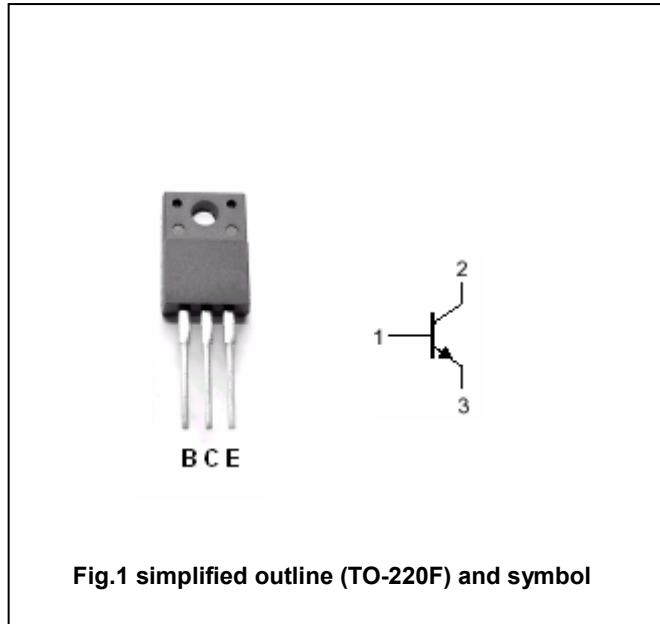
- With TO-220F package
- High voltage ,high speed

**APPLICATIONS**

- Designed for use in 220V line-operated switchmode power supplies and electronic light ballasts

**PINNING**

PIN	DESCRIPTION
1	Base
2	Collector
3	Emitter

**Fig.1 simplified outline (TO-220F) and symbol****ABSOLUTE MAXIMUM RATINGS( $T_c=25^\circ\text{C}$ )**

SYMBOL	PARAMETER	CONDITIONS	VALUE	UNIT
$V_{CBO}$	Collector-base voltage	Open emitter	1000	V
$V_{CEO}$	Collector-emitter voltage	Open base	450	V
$V_{EBO}$	Emitter-base voltage	Open collector	9	V
$I_C$	Collector current (DC)		10	A
$I_{CM}$	Collector current-Peak		16	A
$I_B$	Base current		4	A
$I_{BM}$	Base current-Peak		8	A
$P_D$	Total power dissipation	$T_c=25^\circ\text{C}$	45	W
$T_j$	Junction temperature		150	$^\circ\text{C}$
$T_{stg}$	Storage temperature		-65~150	$^\circ\text{C}$

**THERMAL CHARACTERISTICS**

SYMBOL	PARAMETER	VALUE	UNIT
$R_{th j-C}$	Thermal resistance junction to case	2.78	$^\circ\text{C}/\text{W}$
$R_{th j-A}$	Thermal resistance junction to ambient	62.5	$^\circ\text{C}/\text{W}$

**Silicon NPN Power Transistors****MJF18008****CHARACTERISTICS****T<sub>j</sub>=25°C unless otherwise specified**

SYMBOL	PARAMETER	CONDITIONS	MIN	TYP.	MAX	UNIT
V <sub>CEO(SUS)</sub>	Collector-emitter sustaining voltage	I <sub>C</sub> =0.1A; L=25mH	450			V
V <sub>CE(sat)-1</sub>	Collector-emitter saturation voltage	I <sub>C</sub> =2A; I <sub>B</sub> =0.2A T <sub>C</sub> =125°C			0.6 0.65	V
V <sub>CE(sat)-2</sub>	Collector-emitter saturation voltage	I <sub>C</sub> =4.5A; I <sub>B</sub> =0.9A T <sub>C</sub> =125°C			0.7 0.8	V
V <sub>BE(sat)-1</sub>	Base-emitter saturation voltage	I <sub>C</sub> =2A; I <sub>B</sub> =0.2A			1.10	V
V <sub>BE(sat)-2</sub>	Base-emitter saturation voltage	I <sub>C</sub> =4.5A; I <sub>B</sub> =0.9A			1.25	V
I <sub>CES</sub>	Collector cut-off current	V <sub>CES</sub> =Rated V <sub>CES</sub> ; V <sub>EB</sub> =0			0.1	mA
			T <sub>C</sub> =125°C		0.5	
		V <sub>CES</sub> =800V			0.1	
I <sub>CEO</sub>	Collector cut-off current	V <sub>CE</sub> =Rated V <sub>CEO</sub> ; I <sub>B</sub> =0			0.1	mA
I <sub>EBO</sub>	Emitter cut-off current	V <sub>EB</sub> =9V; I <sub>C</sub> =0			0.1	mA
h <sub>FE-1</sub>	DC current gain	I <sub>C</sub> =1A; V <sub>CE</sub> =5V	14		34	
h <sub>FE-2</sub>	DC current gain	I <sub>C</sub> =4.5A; V <sub>CE</sub> =1V	6			
h <sub>FE-3</sub>	DC current gain	I <sub>C</sub> =2A; V <sub>CE</sub> =1V	11			
h <sub>FE-4</sub>	DC current gain	I <sub>C</sub> =10mA; V <sub>CE</sub> =5V	10			
f <sub>T</sub>	Transition frequency	I <sub>C</sub> =0.5A; V <sub>CE</sub> =10V; f=1MHz		13		MHz
C <sub>OB</sub>	Collector output capacitance	I <sub>E</sub> =0; V <sub>CB</sub> =10V; f=1MHz		100		pF

Switching times resistive load, Duty Cycle≤10%, Pulse Width=20μs

t <sub>on</sub>	Turn-on time	V <sub>CC</sub> =300V, I <sub>C</sub> =2A I <sub>B1</sub> =0.2A; I <sub>B2</sub> =1.0A			0.3	μs
t <sub>off</sub>	Turn-off time				2.5	μs
t <sub>on</sub>	Turn-on time	V <sub>CC</sub> =300V, I <sub>C</sub> =4.5A I <sub>B1</sub> =0.9A; I <sub>B2</sub> =2.25A			0.18	μs
t <sub>off</sub>	Turn-off time				2.5	μs

**Silicon NPN Power Transistors****MJF18008****PACKAGE OUTLINE**