

M62253BGP

Charge Control IC for Li-Ion Batteries

REJ03F0243-0200 Rev.2.00 Sep 14, 2007

## Description

M62253BGP is a charge control IC dedicated to Lithium-ion batteries. Constant current/voltage charging suitable for Li-Ion batteries are available by utilizing on-chip current/voltage control circuits.

It also includes charge inhibit function for over discharged battery and charge control function detecting battery temperature, allowing for easy configuration of Li-Ion battery charging circuit.

## Features

- Available for 4.2 V battery
- High precision reference voltage  $4.2 \text{ V} \pm 30 \text{ mV}$
- Constant current and constant voltage charging
- Charge completion detection voltage settable by external resistors
- Charging inhibit and protection function for over discharged battery
- Charging inhibit and protection function for both high and low temperature battery
- Dual LED indications monitoring charging status
- Recharging function (LED2 remains turned on after recharging)
- Delay circuit to prevent erroneous detection

## Applications

Li-Ion battery charger for hand-held instruments such as cellular phones and notebook computers

## Block Diagram



## **Pin Arrangement**



# **Pin Description**

Pin No.	Pin Name	Function					
1	T <sub>IN</sub>	Temperature sensing input (also battery connecting detection input)					
2	C3	Delay tim <mark>e setting</mark> for temperature sensing (Tpd = 49 ms @ 0.1μF)					
3	ADJ	Charge completion detection voltage setting (One fifth of ADJ terminal voltage is equal to charge completion detection voltage.)					
4	V <sub>DD</sub>	Reference voltage output $V_{DD} = 4.53 V$					
5	V <sub>CC</sub>	Power supply					
7, 8	LED1, 2	LED driver outputs (opened collector)					
		LED1 for during charge, LED2 for completion of charge					
9	C1	Delay time setting for voltage sensing (Tpd = 1.06 s @ 2.2 μF)					
10	C2	Delay time setting for current sensing (Tpd = 1.15 s @ 2.2 μF)					
12	GND	Ground					
13	V <sub>SENSE</sub>	Battery output voltage sensing input					
14	SENSE-	Charging current sensing input (connect to lower voltage node)					
15	SENSE+	Charging current sensing input (connect to higher voltage node)					
16	OUT	Charger output (opened collector)					

## **Absolute Maximum Ratings**

			$(Ta = 25^{\circ}C, unless otherwise noted)$			
Item	Symbol	Ratings	Unit	Condition		
Supply voltage	Vcc	16	V			
Output current	Ι <sub>ΟυΤ</sub>	30	mA			
Applying voltage to SENSE pin	V <sub>SENSE</sub>	V <sub>CC</sub>	V			
Applying voltage to T <sub>IN</sub> pin	VT <sub>IN</sub>	V <sub>CC</sub>	V			
Power dissipation	Pd	300	mW	Ta = 25°C		
Thermal derating	Κθ	3.0	mW/°C	Ta > 25°C		
Operating temperature	Topr	-20 to +85	°C			
Storage temperature	Tstg	-40 to +125	°C			

## **Electrical Characteristics**

		(V <sub>C</sub>	$_{\rm C} = 12.0$ V	, V <sub>SENSE</sub>	= 3.6 V, T	a = 25°	C, unless otherwise noted)
Blocks	ltem	Symbol	Min	Тур	Max	Unit	Condition
All	Supply voltage	V <sub>CC</sub>	5.0		15.0	V	
device	Current consumption	Icc	_	7	—	mA	LED is off
$V_{DD}$	V <sub>DD</sub> output voltage	V <sub>DD</sub>	4.30	4.53	4.76	V	
Voltage	Charge start voltage	V <sub>THchgon</sub>	1.44	1.54	1.64	V	
detection	Quick charge start voltage	V <sub>THqchgon</sub>	2.97	3.07	3.17	V	
	Output voltage setting	Vo <sub>chg</sub>	4.17	4.20	4.23	V	During a charge
	Recharge start voltage	VTHrchgon	3.89	3.99	4.09	V	At charge completion
	V <sub>SENSE</sub> terminal input current	linVSENSE	-2.0	_	2.0	μA	
Current detection	Charging current setting voltage 1	I <sub>SET1</sub>	15	26	37	mV	1.56 V < battery V. < 3.07 V
	Charging current setting	I <sub>SET2</sub>	232	256	280	mV	$5.0 \text{ V} < \text{V}_{CC} < 8.0 \text{ V}$
	voltage 2						3.07 V < battery V. < 4.2 V
			237	256	275	mV	$8.0 \text{ V} < \text{V}_{\text{CC}} < 15.0 \text{ V}$
							3.07 V < battery V. < 4.2 V
	Charge completion detecting voltage	I <sub>THchgoff</sub>	15	26	37	mV	Vadj = 0.13 V
	Charge completion detecting voltage	I <sub>THchgoff</sub>	75	100	125	mV	Vadj = 0.5 V
	Input current into SENSE+ pin	I <sub>INSENSE+</sub>	—	60	85	μΑ	During a charge
	Input current into SENSE- pin	I <sub>INSENSE</sub> -		60	85	μΑ	During a charge
LED	LED1 pin saturation voltage	VOHLED1		_	0.4	V	ILED1 = 10 mA
	LED2 pin saturation voltage	V <sub>OHLED2</sub>		_	0.4	V	ILED2 = 10 mA
T <sub>IN</sub>	T <sub>IN</sub> pin upper threshold voltage 1	VTHINTH1	3.11	3.21	3.31	V	V <sub>DD</sub> = 4.53 V
	T <sub>IN</sub> pin lower threshold voltage 1	V <sub>THINTL1</sub>	1.39	1.49	1.59	V	
	T <sub>IN</sub> pin upper threshold voltage 2	V <sub>THINTH2</sub>	3.13	3.23	3.33	V	
	T <sub>IN</sub> pin lower threshold voltage 2	V <sub>THINTL2</sub>	1.49	1.59	1.69	V	
	Input current into T <sub>IN</sub> pin	lin <sub>tin</sub>	-1.0	—		μA	
OUT	Output pin saturation voltage	V <sub>OHOUT</sub>		1.0	2.0	V	I <sub>OUT</sub> = 20 mA
	Output pin leak current	I <sub>LOUT</sub>	_		1.0	μA	V <sub>OUT</sub> = 15 V
C1	Delay time for voltage detection	td <sub>C1</sub>	0.76	1.06	1.36	S	At recharge C1 = 2.2 $\mu$ F
C2	Connect/disconnect detection time1	td <sub>BDET1</sub>	0.82	1.15	1.48	S	4.2 V con. voltage output period C2 = 2.2 μF
	Delay time for current detection	td <sub>IDET</sub>	0.82	1.15	1.48	S	C2 = 2.2 μF
	Delay time for LED switch over	td <sub>ILED</sub>	1.56	2.21	2.86	S	C2 = 2.2 μF
	Discharging time	td <sub>chg</sub>	30	90	—	ms	C2 = 2.2 μF (after current detection)
C3	Delay time for temperature detection	td <sub>C3</sub>	34	49	64	ms	C3 = 0.1 μF

## **Functional Description**

(1) Detection of connection/disconnection of battery

Connection/disconnection of battery is detected via  $T_{IN}$  pin. The detection is performed by external resistor and thermistor using the regulated 4.53 V of  $V_{DD}$  terminal.

When  $T_{IN}$  terminal voltage is between 1.49 V and 3.21 V, battery is confirmed to be connected according to the internal setting.

When  $T_{IN}$  terminal voltage is 1.49 V or less, or 3.21 V or more, charge is inhibited owing to possibilities of battery din connection or unusual battery temperature.

— Charging start conditions (when battery is confirmed to be connected)

Charging voltage is set to 4.2 V when battery connection is confirmed.

1.15 second (C2 =  $2.2 \mu$ F) later, the voltage is changed to 1.46 V to detect battery voltage. (Voltage detection is not done until the voltage is changed to 1.46 V.)

During 4.2 V constant voltage output, the voltage drop by current detecting resistor is limited to 256 mV. When battery voltage is between 1.54 V and 4.2 V, constant current charge gets started.

In the case the voltage is 4.2 V or more, 4.2 V constant voltage charging starts to monitor charging current.

(2) Constant current control block and current detection

Charging current is measured between SENSE+ and SENSE- terminals so that constant current control is performed. Current sensing resistor voltage drop is set as follows:

When battery voltage is between 1.54 V and 3.07 V, voltage drop is set to 26 mV.

When battery voltage is 3.07 V or more, voltage drop is set to 256 mV.

When charge current is detected and battery voltage is 4.1 V or more, and the above voltage drop of less than one fifth of ADJ terminal voltage continues for 1.15 second ( $C2 = 2.2 \ \mu F$ ) during constant voltage charging, completion of charge is confirmed and then output changes to constant voltage of 1.46 V. (Preventing function of erroneous detection works.)

(3) Constant voltage control block

Battery voltage and output voltage is measured via V<sub>SENSE</sub> terminal.

When battery voltage rises up to 4.2 V or more by constant current charging, 4.2 V constant voltage control starts instead.

When the above voltage drop of less than one fifth of ADJ terminal voltage continues for 1.15 second ( $C2 = 2.2 \mu F$ ) during constant voltage charging, charge completion is confirmed and then output changes to constant voltage of 1.46 V. (The preventing function of erroneous detection works.)

(4) Voltage detection block

When battery voltage monitored via the  $V_{\text{SENSE}}$  terminal is 1.54 V or less, charging is inhibited by confirming that battery is over discharged or shorted battery is connected.

When battery voltage is 1.54 V or more, following two charging current values are to be set according to the battery outputs:

When battery voltage is between 1.54 V and 3.07 V, current measuring resistor voltage drop is set to 26 mV. When battery voltage is 3.07 V or more, above voltage drop is set to 256 mV.

When battery voltage rises up to 4.2 V during the constant current charging, constant voltage charging gets started. When battery voltage is 3.99 V or less for 1.06 second ( $C1 = 2.2 \mu F$ ) after charge completion, recharging will start. (Prevention function of erroneous detection works.)

(5) Temperature detecting block

Temperature detection is done through  $T_{IN}$  terminal. Detecting battery temperature is accomplished by external resistor and thermistor using the regulated 4.53 V of the  $V_{DD}$  terminal.

When  $T_{IN}$  voltage is between 1.49 V and 3.21 V, measured temperature is regarded as normal according to internal setting, then charging will start. When  $T_{IN}$  voltage is 1.49 V or less, or 3.21 V or more, charging is inhibited due to the decision of the unusual battery temperature.

When  $T_{IN}$  voltage goes down to 1.49 V or less after charging starts, charging stops until  $T_{IN}$  voltage rises up to 1.59 V or more. (It has hysteresis corresponding to battery temperature range of 2.5°C.)

Temperature detection is completed after 49 ms (C3 =  $0.1 \ \mu\text{F}$ ) halt of detection voltage. Preventing function of erroneous detection works.)

(6) Regulated power supply block

Regulated 4.53 V is provided at  $V_{DD}$  terminal. Detecting battery temperature is done by this regulated voltage together with external resistor and thermistor.

(7) Output block

External PNP transistor should be connected to OUT terminal because of an opened collector output circuit configuration.

Maximum output current is 30 mA, so charging can be done with charging current up to 30 mA multiplied by hFE of external transistor.

(8) LED driving block

Output configuration of this block is opened collector by NPN Tr.

Charging states are indicated by using two LEDs. LED1 is on during charge, LED2 is on at the completion of charge, where "on" refers to turning on of LED, "off" turning off of LED.

LED indication changes from "charge" to "completion of charge" 2.21 second ( $C2 = 2.2 \ \mu F$ ) after the completion is confirmed by monitoring the charging current.

Once charge completion is indicated (LED1 = OFF, LED2 = ON), LED1 continues to be OFF, and LED2 continues to be ON until non-connection of battery is confirmed at  $T_{IN}$  terminal. Such is also the case with the period for recharging.

## **Flowchart for Charging**



## Functional Timing Diagram (The component Values are the Same as those of the Application Circuits)

## (1) Battery Connecting/Disconnecting Detection Timing



## (2) Battery Temperature Detection Timing





## (3) Timing Diagram During a Charge

## **The Application Circuit**

Note:The following thermistor is recommended.Maker:ISHIZUKA DENSHIType:103HTCharacteristic:10 kΩ at 25°C

### 0 1 μF 222 π 2.2 μF OUT ulated su 4.53 V 4.2 V OUT Int curren SENSE+ ≸ 1Ω OUT SENSEtant voltage 3 07 V control ٢ ۲ VSEN **\$** 1.54 ∨ to V<sub>DD</sub> pin Current LED2 detection to V<sub>DD</sub> pin Battery ▲ 10 kΩ LED1 ₹ C C.2 0.1 μF # ┯ 2.2 µ Thermistor

## (1) When Thermistor is Located in Battery Pack

REJ03F0243-0200 Rev.2.00 Sep 14, 2007 Page 9 of 12

### (2) When Thermistor is Located in Charger or Thermistor is not Used (For Your Reference)



## How to Set Quick Charge Current and Charge Completion Detection Current

- (1) How to set quick charge current (Iqchg)
  - Quick charge current (Iqchg) depends upon RSENSE.

Set the value of RSENSE by putting Iqchg required into the formula below.

RSENSE [ $\Omega$ ] = charge current setting voltage 2 / lqchg [mA] =256 mA / lqchg [mA]

, where pre-charge current is given by:

Pre-charge current = charge current setting voltage 1 / RSENSE = 26 mV / RSENSE

(2) How to set charge completion detection current (Icomp)

Charge completion detection current (Icomp) depends upon RSENSE and ADJ terminal voltage (Vadj). Set ADJ terminal voltage by putting charge completion detection current required and RSENSE given by (1) into the formula below.

Vadj [V] =  $5 \times \text{RSENSE} [\Omega] \times \text{Icomp} [A]$ 

, where the constant 5 is internally fixed.

Note: Refer to the chart below for setting RSENSE and ADJ terminal voltage.

(Chart below shows the relationship between ADJ terminal voltage (Vadj) and charge completion detection current (Icomp).)

### ADJ Term. Volt. Setting

Quick charge	256 mA	450 mA		512 mA		700 mA	
Rsense	1Ω	0.56 Ω		0.50 Ω		0.36 Ω	
Charge comp. current lcomp	26 mA	100 mA	130 mA	100 mA	130 mA	100 mA	130 mA
ADJ term. voltage Vadj = 5 × Rs × lcomp	125 mV	280 mV	364 mV	250 mV	325 mV	180 mV	234 mV

Note: Several 10 k $\Omega$  or around is recommended for the total resistor value R1 and R2 for ADJ.

### How Icomp Relates itself to Vadj



## **Package Dimensions**



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