

HAT2029R

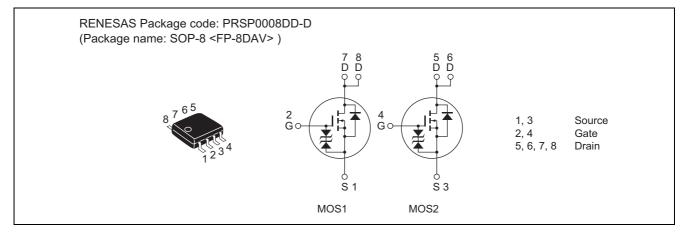
Silicon N Channel Power MOS FET High Speed Power Switching

> REJ03G1164-0600 (Previous: ADE-208-525D) Rev.6.00 Sep 07, 2005

# Features

- Low on-resistance
- Capable of 2.5 V gate drive
- Low drive current
- High density mounting

## Outline





# **Absolute Maximum Ratings**

			$(Ta = 25^{\circ}C)$
Item	Symbol	Value	Unit
Drain to source voltage	V <sub>DSS</sub>	28	V
Gate to source voltage	V <sub>GSS</sub>	±12	V
Drain current	ID	7.5	А
Drain peak current	I <sub>D (pulse)</sub> Note 1	60	А
Body-drain diode reverse drain current	I <sub>DR</sub>	7.5	А
Channel dissipation	Pch Note 2	2	W
Channel dissipation	Pch Note 3	3	W
Channel temperature	Tch	150	٥C
Storage temperature	Tstg	-55 to +150	٥C

Notes: 1. PW  $\leq$  10  $\mu s,\,duty\,cycle \leq$  1%

2. 1 Drive operation: When using the glass epoxy board (FR4 40  $\times$  40  $\times$  1.6 mm), PW  $\leq$  10 s

3. 2 Drive operation: When using the glass epoxy board (FR4 40  $\times$  40  $\times$  1.6 mm), PW  $\leq$  10 s

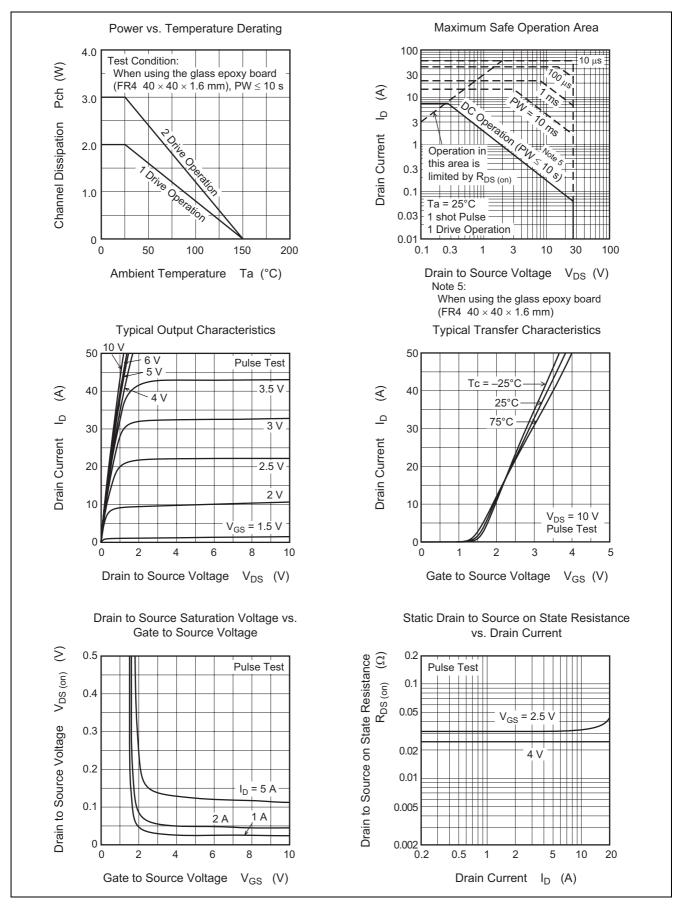
# **Electrical Characteristics**

						$(Ta = 25^{\circ}C)$
Item	Symbol	Min	Тур	Max	Unit	Test Conditions
Drain to source breakdown voltage	V (BR) DSS	28	—		V	$I_D = 10 \text{ mA}, V_{GS} = 0$
Gate to source breakdown voltage	V (BR) GSS	±12	—	_	V	$I_G = \pm 100 \ \mu A, \ V_{DS} = 0$
Gate to source leak current	I <sub>GSS</sub>	—	—	±10	μA	$V_{GS} = \pm 10 \text{ V}, \text{ V}_{DS} = 0$
Zero gate voltage drain current	I <sub>DSS</sub>	—	—	1	μA	$V_{DS} = 28 V, V_{GS} = 0$
Gate to source cutoff voltage	V <sub>GS (off)</sub>	0.4	—	1.4	V	$V_{DS} = 10 \text{ V}, \text{ I}_{D} = 1 \text{ mA}$
Static drain to source on state resistance	R <sub>DS (on)</sub>	—	0.025	0.033	Ω	$I_D = 4 \text{ A}, V_{GS} = 4 \text{ V}^{Note 4}$
	R <sub>DS (on)</sub>	—	0.031	0.043	Ω	$I_D = 4 \text{ A}, V_{GS} = 2.5 \text{ V}^{Note 4}$
Forward transfer admittance	y <sub>fs</sub>	9.5	15	_	S	$I_D = 4 \text{ A}, V_{DS} = 10 \text{ V}^{\text{Note 4}}$
Input capacitance	Ciss	—	780	_	pF	V <sub>DS</sub> = 10 V
Output capacitance	Coss	—	470	_	pF	$V_{GS} = 0$
Reverse transfer capacitance	Crss	—	190	_	pF	f = 1 MHz
Turn-on delay time	t <sub>d (on)</sub>	—	20	_	ns	$V_{GS} = 4 V, I_D = 4 A,$
Rise time	tr	—	170	_	ns	$V_{DD} \cong 10 \text{ V}$
Turn-off delay time	t <sub>d (off)</sub>	—	140	_	ns	
Fall time	t <sub>f</sub>	—	170	_	ns	
Body-drain diode forward voltage	V <sub>DF</sub>		0.88	1.15	V	$I_F = 7.5 \text{ A}, V_{GS} = 0^{\text{Note 4}}$
Body-drain diode reverse recovery time	t <sub>rr</sub>		65	_	ns	$I_F = 7.5 \text{ A}, V_{GS} = 0$
						di <sub>F</sub> /dt = 20 A/µs

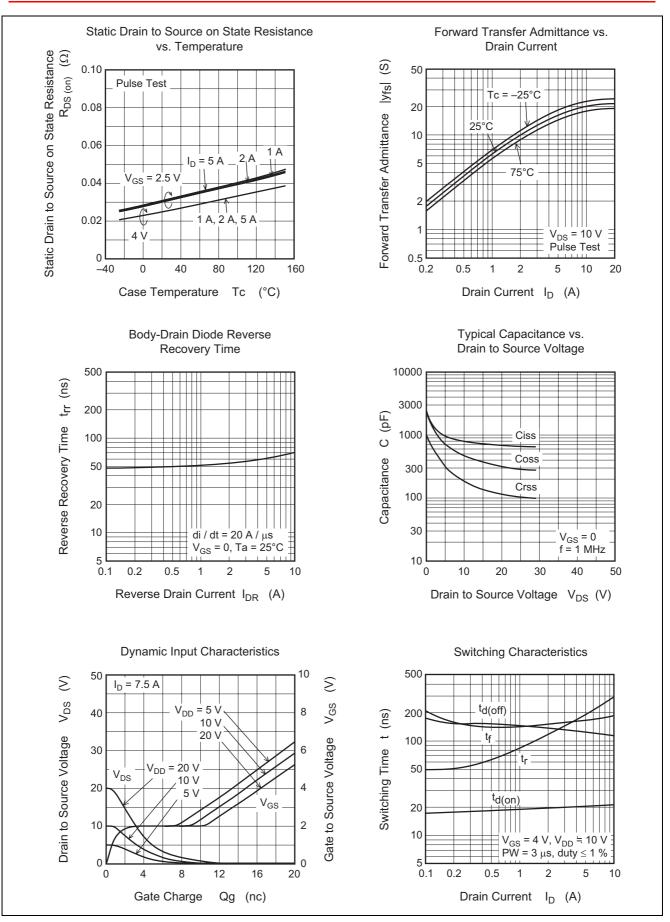
Note: 4. Pulse test



## **Main Characteristics**

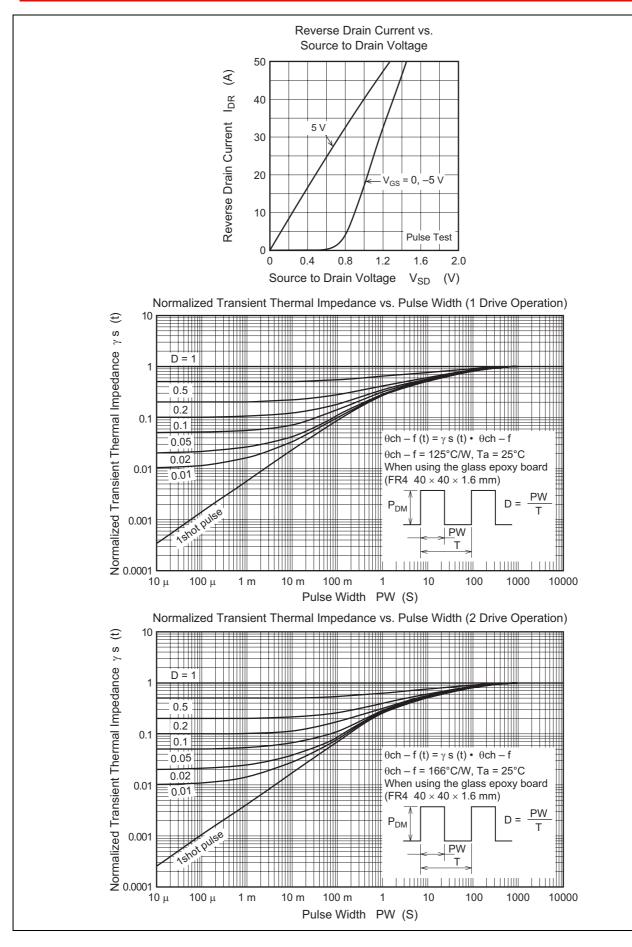




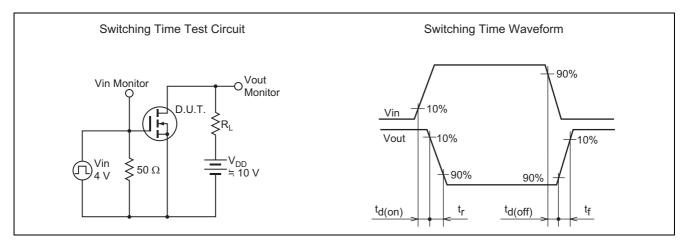


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Rev.6.00 Sep 07, 2005 page 4 of 7

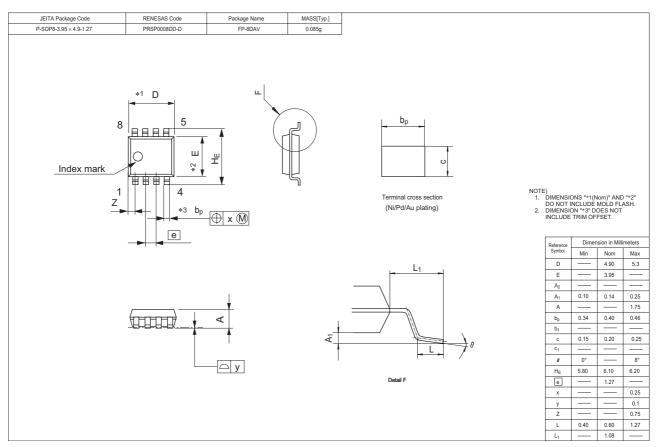


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# **Package Dimensions**



# **Ordering Information**

Part Name	Quantity	Shipping Container
HAT2029R-EL-E	2500 pcs	Taping

Note: For some grades, production may be terminated. Please contact the Renesas sales office to check the state of production before ordering the product.



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