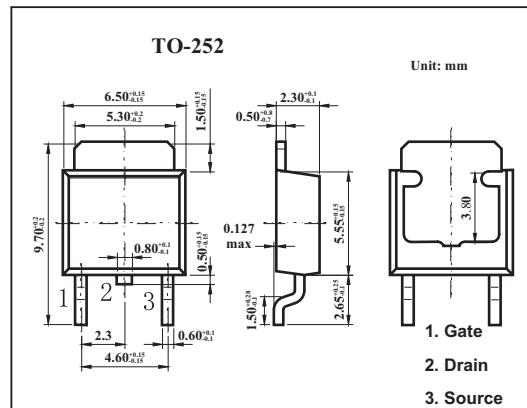
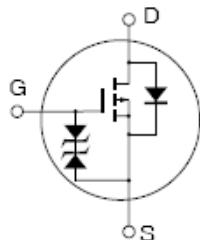


Silicon P-Channel MOS FET

2SJ130S

■ Features

- Low on-resistance
- High speed switching
- Low drive current
- No secondary breakdown
- Suitable for switching regulator, DC-DC converter and ultrasonic power oscillators



■ Absolute Maximum Ratings Ta = 25°C

Parameter	Symbol	Rating	Unit
Drain to source voltage	V _{DSS}	-300	V
Gate to source voltage	V _{GSS}	±20	V
Drain current	I _{D(DS)}	-1	A
Drain peak current	I _{D(pulse)}	-2	A
Body to drain diode reverse drain current	I _{DR}	-1	A
Channel dissipation (T _c =25°C)	P _{ch}	20	W
Channel temperature	T _{ch}	150	°C
Storage temperature	T _{stg}	-55 to +150	°C

2SJ130S

■ Electrical Characteristics Ta = 25°C

Parameter	Symbol	Testconditons	Min	Typ	Max	Unit
Drain to source breakdown voltage	V(BR)DSS	Id = -10 mA, Vgs = 0	-300			V
Gate to source breakdown voltage	V(BRGSS)	IG = ±100 µA, Vds = 0	±20			V
Gate to source leak current	IGSS	Vgs = ±16 V, Vds = 0			±10	µA
Zero gate voltage drain current	Idss	Vds = -240 V, Vgs = 0			-100	µA
Gate to source cutoff voltage	VGS(off)	Id = -1 mA, Vds = -10 V	-2		-4	V
Static Drain to source on stateresistance	RDS(on)	Id = -0.5 A, Vgs = -10 V		6.0	8.5	Ω
Forward transfer admittance	yfs	Id = -0.5 A, Vds = -20 V	0.25	0.4		S
Input capacitance	Ciss	Vds = -10 V, Vgs = 0, f = 1 MHz		235		pF
Output capacitance	Coss			65		pF
Reverse transfer capacitance	Crss			16		pF
Turn-on delay time	td(on)	Id = -0.5 A, Vgs = -10 V, RL = 60 Ω		10		ns
Rise time	tr			25		ns
Turn-off delay time	td(off)			35		ns
Fall time	tf			45		ns
Body to drain diode forward voltage	VDF	If = -1 A, Vgs = 0		-0.9		V
Body to drain diode reverse recovery time	trr	If = -1 A, Vgs = 0, dIf/dt = 50 A/µs		200		ns