

HD74HC688

8-bit Magnitude Comparator

REJ03D0643-0200 (Previous ADE-205-529) Rev.2.00 Mar 30, 2006

Description

The HD74HC688 compares bit for bit two 8-bit words and indicates whether or not they are equal. The $\overline{P=Q}$ output indicates equality when it is low.

A single active low enable is provided to facilitate cascading of several packages and enable comparison of words greater than 8-bits.

This device is useful in memory block decoding applications, where memory block enable signals must be generated from computer address information.

Features

• High Speed Operation: t_{pd} (P or Q to Output) = 17 ns typ ($C_L = 50 \text{ pF}$)

• High Output Current: Fanout of 10 LSTTL Loads

• Wide Operating Voltage: $V_{CC} = 2$ to 6 V

• Low Input Current: 1 µA max

• Low Quiescent Supply Current: I_{CC} (static) = 4 μ A max (Ta = 25°C)

• Ordering Information

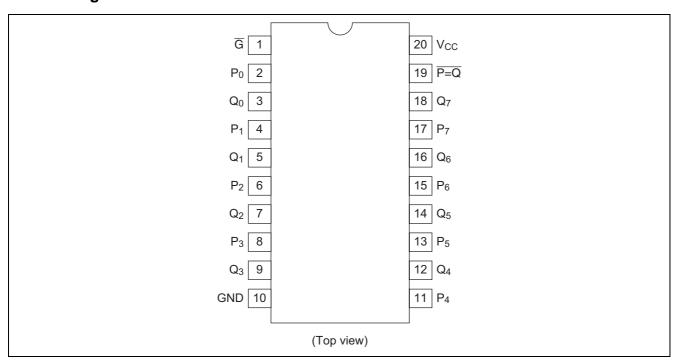
Part Name	Package Type Package Code Package (Previous Code) Abbreviation		Package Abbreviation	Taping Abbreviation (Quantity)	
HD74HC688P	DILP-20 pin (JEDEC)	PRDP0020AC-B (DP-20NEV)	Р	_	
HD74HC688FPEL	SOP-20 pin (JEITA)	PRSP0020DD-B (FP-20DAV)	FP	EL (2,000 pcs/reel)	
HD74HC688RPEL	SOP-20 pin (JEDEC)	PRSP0020DC-A (FP-20DBV)	RP	EL (1,000 pcs/reel)	

Function Table

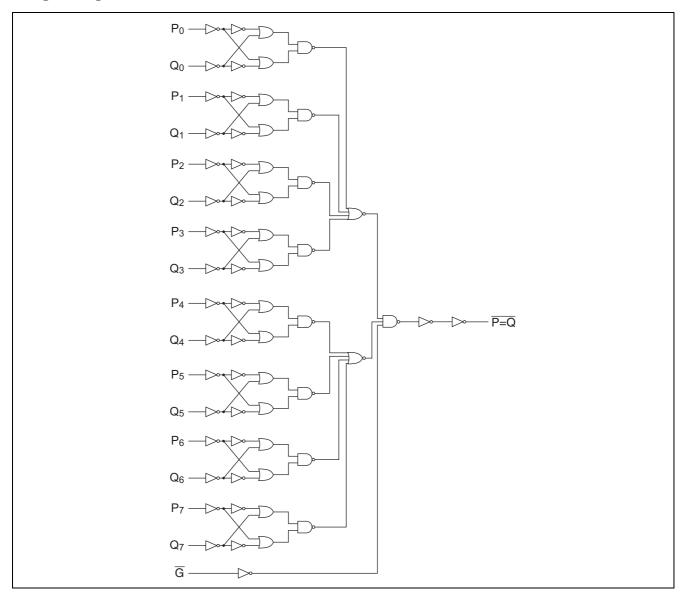
Inj		
Data P, Q	Output P=Q	
P=Q	L	L
P>Q	L	Н
P <q< td=""><td>L</td><td>Н</td></q<>	L	Н
X	Н	Н

H: high levelL: low levelX: irrelevant

Pin Arrangement



Logic Diagram



Absolute Maximum Ratings

Item	Symbol	Ratings	Unit
Supply voltage range	V _{CC}	-0.5 to 7.0	V
Input / Output voltage	V _{IN} , V _{OUT}	-0.5 to V _{CC} +0.5	V
Input / Output diode current	I _{IK} , I _{OK}	±20	mA
Output current	l _{out}	±25	mA
V _{CC} , GND current	I _{CC} or I _{GND}	±50	mA
Power dissipation	P _T	500	mW
Storage temperature	Tstg	-65 to +150	°C

Note: The absolute maximum ratings are values, which must not individually be exceeded, and furthermore, no two of which may be realized at the same time.

Recommended Operating Conditions

Item	Symbol	Ratings	Unit	Conditions
Supply voltage	V_{CC}	2 to 6	V	
Input / Output voltage	$V_{\text{IN}}, V_{\text{OUT}}$	0 to V _{CC}	V	
Operating temperature	Та	-40 to 85	°C	
Input rise / fall time*1	t _r , t _f	0 to 1000	ns	V _{CC} = 2.0 V
		0 to 500		V _{CC} = 4.5 V
		0 to 400		$V_{CC} = 6.0 \text{ V}$

Notes: 1. This item guarantees maximum limit when one input switches. Waveform: Refer to test circuit of switching characteristics.

Electrical Characteristics

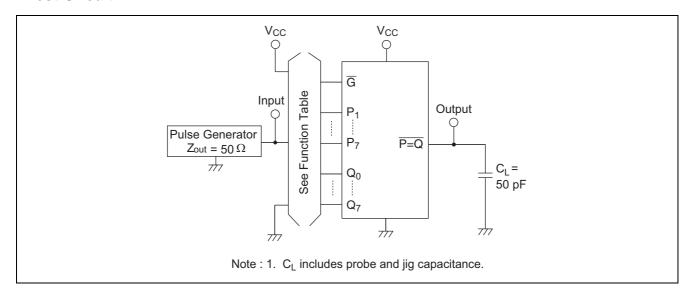
			Т	a = 25°	С	Ta = -40 to+85°C				
Item	Symbol	V _{cc} (V)	Min	Тур	Max	Min	Max	Unit	Test Co	nditions
Input voltage	V_{IH}	2.0	1.5	1	_	1.5	_	V		
		4.5	3.15	l	_	3.15	_			
		6.0	4.2	l	_	4.2	_			
	V_{IL}	2.0		l	0.5		0.5	V		
		4.5		l	1.35		1.35			
		6.0		l	1.8		1.8			
Output voltage	V _{OH}	2.0	1.9	2.0	_	1.9	_	V	$Vin = V_{IH} \text{ or } V_{IL}$	$I_{OH} = -20 \mu A$
		4.5	4.4	4.5	_	4.4	_			
		6.0	5.9	6.0	_	5.9	_			
		4.5	4.18	l	_	4.13	_			$I_{OH} = -4 \text{ mA}$
		6.0	5.68	l	_	5.63	_			$I_{OH} = -5.2 \text{ mA}$
	V_{OL}	2.0		0.0	0.1		0.1	V	$Vin = V_{IH} or V_{IL}$	$I_{OL} = 20 \mu A$
		4.5		0.0	0.1		0.1			
		6.0		0.0	0.1		0.1			
		4.5		l	0.26		0.33			$I_{OL} = 4 \text{ mA}$
		6.0		_	0.26	_	0.33			$I_{OL} = 5.2 \text{ mA}$
Input current	lin	6.0			±0.1	_	±1.0	μΑ	Vin = V _{CC} or GN	ID .
Quiescent supply	I _{CC}	6.0		_	4.0		40	μΑ	Vin = V _{CC} or GN	ID, lout = $0 \mu A$
current										

Switching Characteristics

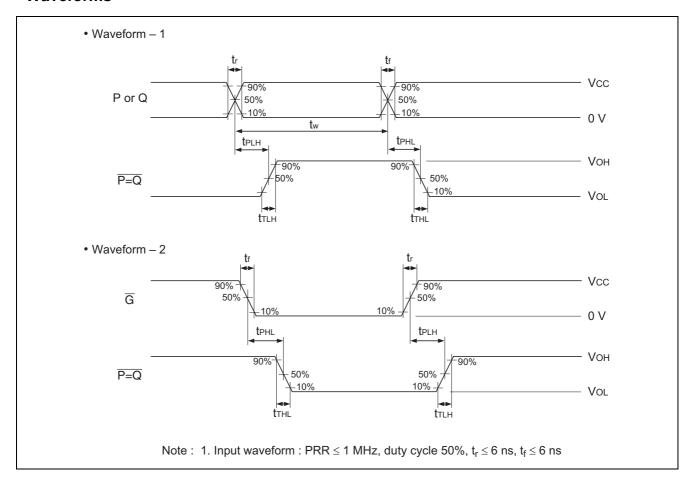
 $(C_L = 50 \text{ pF, Input } t_r = t_f = 6 \text{ ns})$

			Ta = 25°C		Ta = -40 to +85°C				
Item	Symbol	V _{cc} (V)	Min	Тур	Max	Min	Max	Unit	Test Conditions
Propagation delay time	t _{PLH}	2.0	_	_	210	_	265	ns	P or Q to output
	t _{PHL}	4.5	_	17	42	_	53		
		6.0	_	_	36	_	45		
	t _{PLH}	2.0	_	_	120	_	150	ns	Enable to P=Q
	t _{PHL}	4.5	_	9	24	_	30		
		6.0	_	_	20	_	26		
Output rise/fall time	t _{TLH}	2.0	_	_	75	_	95	ns	
	t _{THL}	4.5	_	5	15	_	19		
		6.0	_	_	13	_	16		
Input capacitance	Cin	_	_	5	10	_	10	pF	

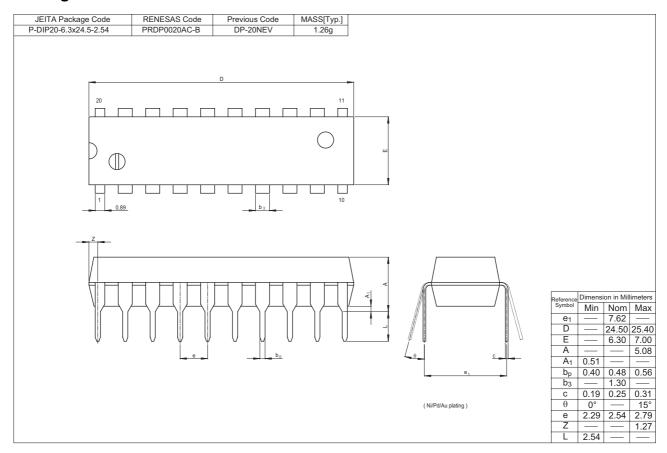
Test Circuit

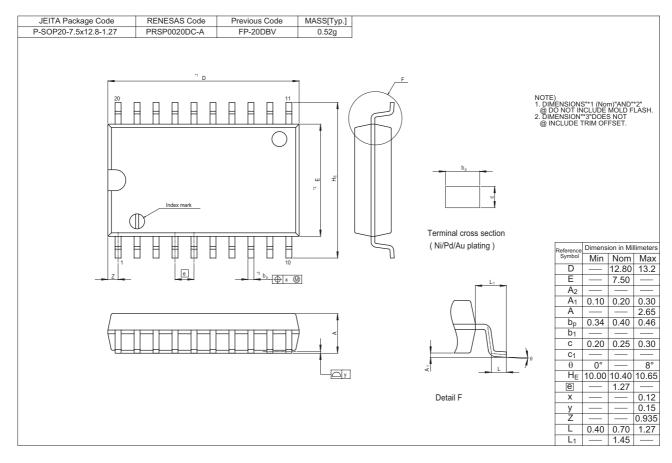


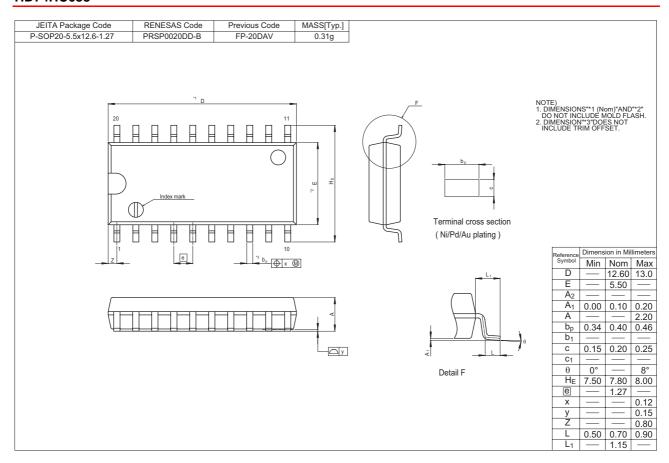
Waveforms



Package Dimensions







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