

MOS Field Effect Transistor

2SK1586

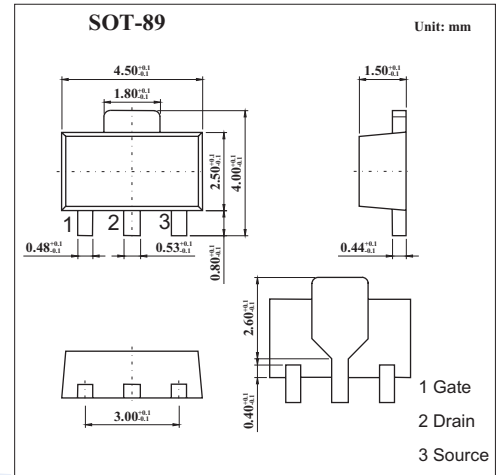
■ Features

- Directly driven by lcs having a 3V power supply.

- Has low on-state resistance

$$R_{DS(on)}=1.0\ \Omega\ \text{MAX.}@V_{GS}=4.0V, I_D=0.5A$$

$$R_{DS(on)}=0.6\ \Omega\ \text{MAX.}@V_{GS}=10V, I_D=0.5A$$



■ Absolute Maximum Ratings Ta = 25°C

Parameter	Symbol	Rating	Unit
Drain to source voltage	V_{DS}	30	V
Gate to source voltage	V_{GS}	± 20	V
Drain current (DC)	I_D	± 1.0	A
Drain current(pulse) *	I_D	± 2.0	A
Power dissipation	P_D	2.0	W
Channel temperature	T_{ch}	150	°C
Storage temperature	T_{stg}	-55 to +150	°C

* $PW \leq 10ms$, duty cycle $\leq 5\%$

■ Electrical Characteristics Ta = 25°C

Parameter	Symbol	Testconditons	Min	Typ	Max	Unit
Drain cut-off current	I_{DSS}	$V_{DS}=30V, V_{GS}=0$			10	μA
Gate leakage current	I_{GSS}	$V_{GS}=\pm 20V, V_{DS}=0$			± 10	μA
Gate to source cutoff voltage	$V_{GS(off)}$	$V_{DS}=10V, I_D=1mA$	1.3	1.9	2.5	V
Forward transfer admittance	$ Y_{fs} $	$V_{DS}=5.0V, I_D=0.5A$	0.4			s
Drain to source on-state resistance	$R_{DS(on)}$	$V_{GS}=4.0V, I_D=0.5A$		0.3	1.0	Ω
		$V_{GS}=10V, I_D=0.5A$		0.2	0.6	Ω
Input capacitance	C_{iss}	$V_{DS}=5.0V, V_{GS}=0, f=1MHz$		170		pF
Output capacitance	C_{oss}			170		pF
Reverse transfer capacitance	C_{rss}			55		pF
Turn-on delay time	$t_{d(on)}$			50		ns
Rise time	t_r	$I_D=0.5A, V_{GS(on)}=5.0V, R_L=6\ \Omega, V_{DD}=20V, R_G=10\ \Omega$		220		ns
Turn-off delay time	$t_{d(off)}$			210		ns
Fall time	t_f			230		ns

■ Marking

Marking	NI