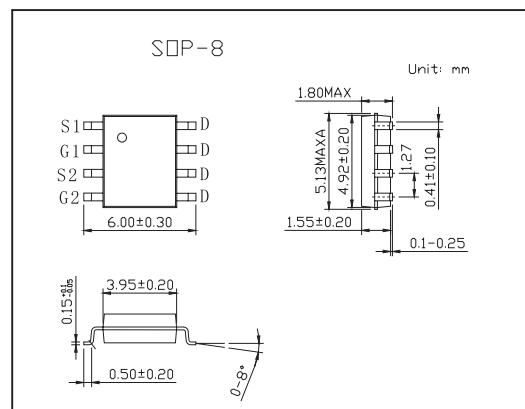
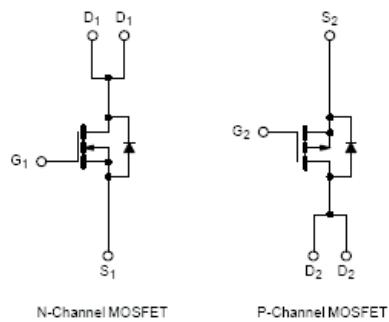


N- and P-Channel 2.5-V (G-S) MOSFET**KI4562DY****■ PIN Configuration****■ Absolute Maximum Ratings TA = 25°C**

Parameter	Symbol	N-Channel	P-Channel	Unit
Drain-Source Voltage	V _{DS}	20	-20	V
Gate-Source Voltage	V _{Gs}	±12	±12	V
Continuous Drain Current (T _J = 150°C)*	I _D	±7.1	±6.2	A
T _A = 70°C		±5.7	±4.9	A
Pulsed Drain Current	I _{DM}	±40	±40	A
Continuous Source Current (Diode Conduction)*	I _S	1.7	-1.7	A
Maximum Power Dissipation*	P _D	2		W
T _A = 70°C		1.3		W
Operating Junction and Storage Temperature Range	T _J , T _{stg}	-55 to 150		°C
Maximum Junction-to-Ambient *	R _{thJA}	62.5		°C/W

*Surface Mounted on FR4 Board, t ≤ 10 sec.

KI4562DY

■ Electrical Characteristics $T_J = 25^\circ\text{C}$

Parameter	Symbol	Testconditons		Min	Typ	Max	Unit
Gate Threshold Voltage	$V_{GS(\text{th})}$	$V_{DS} = V_{GS}, I_D = 250 \mu\text{A}$	N-Ch	0.6			V
		$V_{DS} = V_{GS}, I_D = -250 \mu\text{A}$	P-Ch	-0.6			
Gate Body Leakage	I_{GSS}	$V_{DS} = 0 \text{ V } V_{GS} = \pm 12 \text{ V}$	N-Ch			± 100	nA
		$V_{DS} = 0 \text{ V } V_{GS} = \pm 12 \text{ V}$	P-Ch			± 100	
Zero Gate Voltage Drain Current	I_{DSS}	$V_{DS} = 20\text{V}, V_{GS} = 0\text{V}$	N-Ch			1	μA
		$V_{DS} = -20\text{V}, V_{GS} = 0\text{V}$	P-Ch			-1	
		$V_{DS} = 20 \text{ V }, V_{GS} = 0 \text{ V }, T_J = 55^\circ\text{C}$	N-Ch			5	
		$V_{DS} = -20 \text{ V }, V_{GS} = 0 \text{ V }, T_J = 55^\circ\text{C}$	P-Ch			-5	
On State Drain Currenta	$I_{D(on)}$	$V_{DS} \geq 5 \text{ V }, V_{GS} = 4.5 \text{ V }$	N-Ch	20			A
		$V_{DS} \leq -5 \text{ V }, V_{GS} = -4.5 \text{ V }$	P-Ch	-20			
Drain Source On State Resistance*	$r_{DS(on)}$	$V_{GS} = 4.5 \text{ V }, I_D = 7.1\text{A}$	N-Ch		0.019	0.025	Ω
		$V_{GS} = -4.5 \text{ V }, I_D = -6.2\text{A}$	P-Ch		0.027	0.033	
		$V_{GS} = 2.5 \text{ V }, I_D = 6.0\text{A}$	N-Ch		0.025	0.035	
		$V_{GS} = -2.5 \text{ V }, I_D = -5.0\text{A}$	P-Ch		0.040	0.050	
Forward Transconductance*	g_{fs}	$V_{DS} = 10 \text{ V }, I_D = 7.1\text{A}$	N-Ch		27		S
		$V_{DS} = -10 \text{ V }, I_D = -6.2\text{A}$	P-Ch		20		
Diode Forward Voltage*	V_{SD}	$I_S = 1.7\text{A}, V_{GS} = 0 \text{ V }$	N-Ch			1.2	V
		$I_S = -1.7\text{A}, V_{GS} = 0 \text{ V }$	P-Ch			-1.2	
Total Gate Charge	Q_g	N-Channel $V_{DS} = 10 \text{ V }, V_{GS} = 4.5\text{V}, I_D = 7.1\text{A}$	N-Ch		25	50	nC
Gate Source Charge	Q_{gs}		P-Ch		22	35	
Gate Drain Charge	Q_{gd}		N-Ch		6.5		
			P-Ch		7		
Turn On Time	$t_{d(on)}$	N Channel $V_{DD} = 10 \text{ V }, R_L = 10 \Omega$ $I_D = 1\text{A}, V_{GEN} = 4.5\text{V}, R_g = 6 \Omega$	N-Ch		40	60	ns
Rise Time	t_r		P-Ch		27	50	
Turn Off Delay Time	$t_{d(off)}$		N-Ch		40	60	
Fall Time	t_f		P-Ch		32	50	
Source-Drain Reverse Recovery Time	t_{rr}	$I_F = 1.7 \text{ A}, dI/dt = 100 \text{ A}/\mu\text{s}$	N-Ch		90	150	
		$I_F = -1.7 \text{ A}, dI/dt = 100 \text{ A}/\mu\text{s}$	P-Ch		95	150	
			N-Ch		40	60	
			P-Ch		45	70	
			N-Ch		40	80	
			P-Ch		40	80	

* Pulse test; pulse width $\leq 300 \mu\text{s}$, duty cycle $\leq 2\%$.