

Silicon PNP Power Transistors

2SB1019

DESCRIPTION

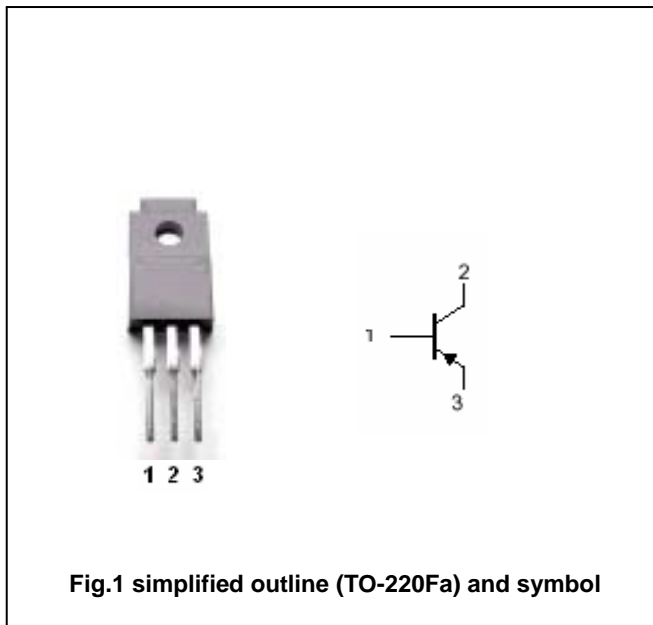
- With TO-220Fa package
- Low saturation voltage
- Complement to type 2SD1412

APPLICATIONS

- High current switching applications
- Power amplifier applications

PINNING

PIN	DESCRIPTION
1	Base
2	Collector
3	Emitter



Absolute maximum ratings(Ta=25)

SYMBOL	PARAMETER	CONDITIONS	VALUE	UNIT
V _{CB0}	Collector-base voltage	Open emitter	-70	V
V _{CEO}	Collector -emitter voltage	Open base	-50	V
V _{EBO}	Emitter-base voltage	Open collector	-5	V
I _C	Collector current		-7	A
I _B	Base current		-1	A
P _C	Collector power dissipation	T _a =25	2	W
		T _C =25	30	
T _j	Junction temperature		150	
T _{stg}	Storage temperature		-55~150	

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CHARACTERISTICS

T_j=25 unless otherwise specified

SYMBOL	PARAMETER	CONDITIONS	MIN	TYP.	MAX	UNIT
V _{(BR)CEO}	Collector-emitter breakdown voltage	I _C =-50mA; I _B =0	-50			V
V _{CEsat}	Collector-emitter saturation voltage	I _C =-4A ; I _B =-0.4A		-0.2	-0.4	V
V _{BEsat}	Base-emitter saturation voltage	I _C =-4A ; I _B =-0.4A		-0.9	-1.2	V
I _{CBO}	Collector cut-off current	V _{CB} =-70V; I _E =0			-30	μA
I _{EBO}	Emitter cut-off current	V _{EB} =-5V; I _C =0			-50	μA
h _{FE-1}	DC current gain	I _C =-1A ; V _{CE} =-1V	70		240	
h _{FE-2}	DC current gain	I _C =-4A ; V _{CE} =-1V	30			
f _T	Transition frequency	I _C =-1A ; V _{CE} =-4V		10		MHz
C _{OB}	Output capacitance	I _E =0 ; V _{CB} =-10V;f=1MHz		250		pF

Switching times

t _{on}	Turn-on time	R _L =10 I _{B1} =-I _{B2} =-0.3A V _{CC} =-30V		0.2		μs
t _s	Storage time			2.5		μs
t _f	Fall time			0.5		μs

◆ h_{FE-1} Classifications

O	Y
70-140	120-240

PACKAGE OUTLINE

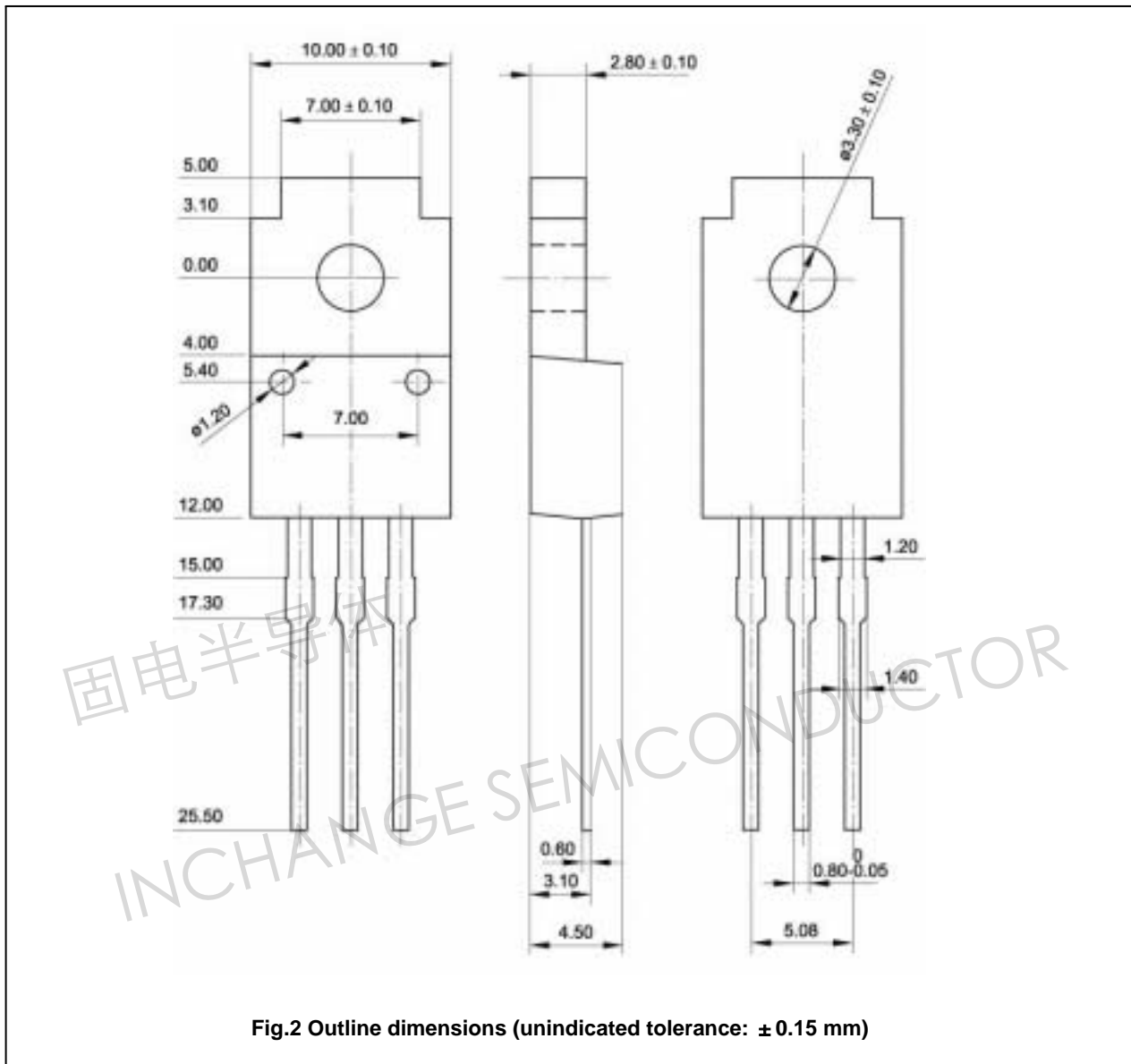


Fig.2 Outline dimensions (unindicated tolerance: ± 0.15 mm)