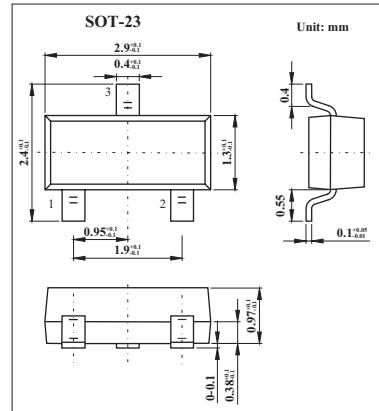


**Silicon Schottky Barrier Diode****HSM8WA****■ Features**

- Proof against high voltage.
- MPAK package is suitable for high density surface mounting and high speed assembly.

**■ Absolute Maximum Ratings Ta = 25°C**

Parameter	Symbol	Value	Unit
Reverse voltage	V <sub>R</sub>	10	V
Average rectified current	I <sub>o</sub>	15	mA
Junction temperature	T <sub>j</sub>	125	°C
Storage temperature	T <sub>stg</sub>	-55 to +125	°C

Note

10ms Sinewave 1pulse

**■ Electrical Characteristics Ta = 25°C**

Parameter	Symbol	Conditions	Min	Typ	Max	Unit
Forward voltage	V <sub>F</sub>	I <sub>F</sub> = 1 mA	350		420	mV
		I <sub>F</sub> = 10 mA	500		580	
Reverse current	I <sub>R</sub>	V <sub>R</sub> = 2 V			0.2	μ A
		V <sub>R</sub> = 10 V			10	
Capacitance	C	V <sub>R</sub> = 0 V, f = 1 MHz			0.85	pF
Capacitance deviation	ΔC	V <sub>R</sub> = 0 V, f = 1 MHz			0.10	pF
Forward voltage deviation	ΔV <sub>F</sub>	I <sub>F</sub> = 10 mA			10	mV
ESD-Capability (Note 1)		C=200pF, Both forward and reverse direction 1 pulse.	30			V

Note

1. Failure criterion ; I<sub>R</sub> ≥ 400nA at V<sub>R</sub> = 2 V**■ Marking**

Marking	C7
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