

Silicon PNP Power Transistors

2SA1051

DESCRIPTION

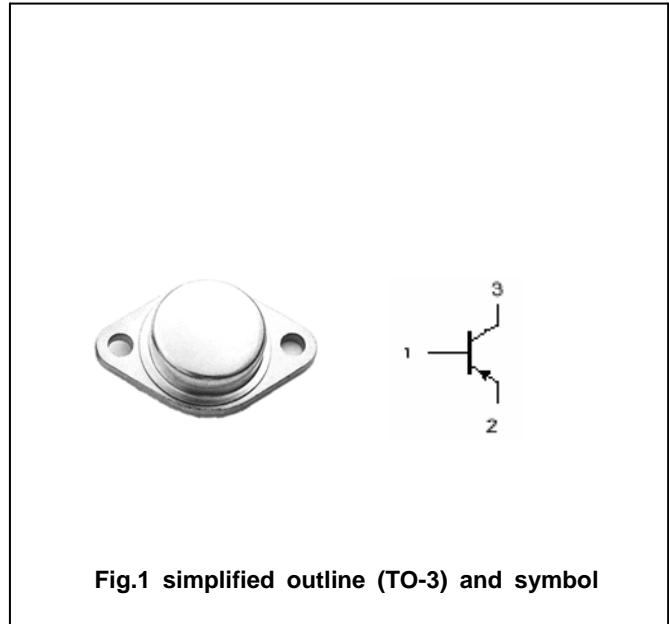
- With TO-3 package
- High transition frequency
- Excellent safe operating area

APPLICATIONS

- For audio and general purpose power amplifier applications

PINNING(see Fig.2)

PIN	DESCRIPTION
1	Base
2	Emitter
3	Collector



Absolute maximum ratings(Ta=°C)

SYMBOL	PARAMETER	CONDITIONS	VALUE	UNIT
V _{CBO}	Collector-base voltage	Open emitter	-150	V
V _{CEO}	Collector-emitter voltage	Open base	-150	V
V _{EBO}	Emitter-base voltage	Open collector	-5	V
I _C	Collector current		-15	A
I _B	Base current		-1.5	A
P _C	Collector power dissipation	T _C =25°C	150	W
T _j	Junction temperature		175	°C
T _{stg}	Storage temperature		-55~200	°C

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CHARACTERISTICS

T_j=25°C unless otherwise specified

SYMBOL	PARAMETER	CONDITIONS	MIN	TYP.	MAX	UNIT
V _{(BR)CEO}	Collector-emitter breakdown voltage	I _C =-25mA ; I _B =0	-150			V
V _{(BR)CBO}	Collector-base breakdown voltage	I _C =-1mA ; I _E =0	-150			V
V _{(BR)EBO}	Emitter-base breakdown voltage	I _E =-1mA ; I _C =0	-5			V
V _{CEsat}	Collector-emitter saturation voltage	I _C =-10A ; I _B =-1A			-3.0	V
V _{BE}	Base-emitter on voltage	I _C =-8A ; V _{CE} =-5V			-1.5	V
I _{CBO}	Collector cut-off current	V _{CB} =-140V ; I _E =0			-10	μ A
I _{EBO}	Emitter cut-off current	V _{EB} =-5V ; I _C =0			-10	μ A
h _{FE-1}	DC current gain	I _C =-1A ; V _{CE} =-5V	55		160	
h _{FE-2}	DC current gain	I _C =-8A ; V _{CE} =-5V	35			
f _T	Transition frequency	I _C =-1A ; V _{CE} =-10V		60		MHz

PACKAGE OUTLINE

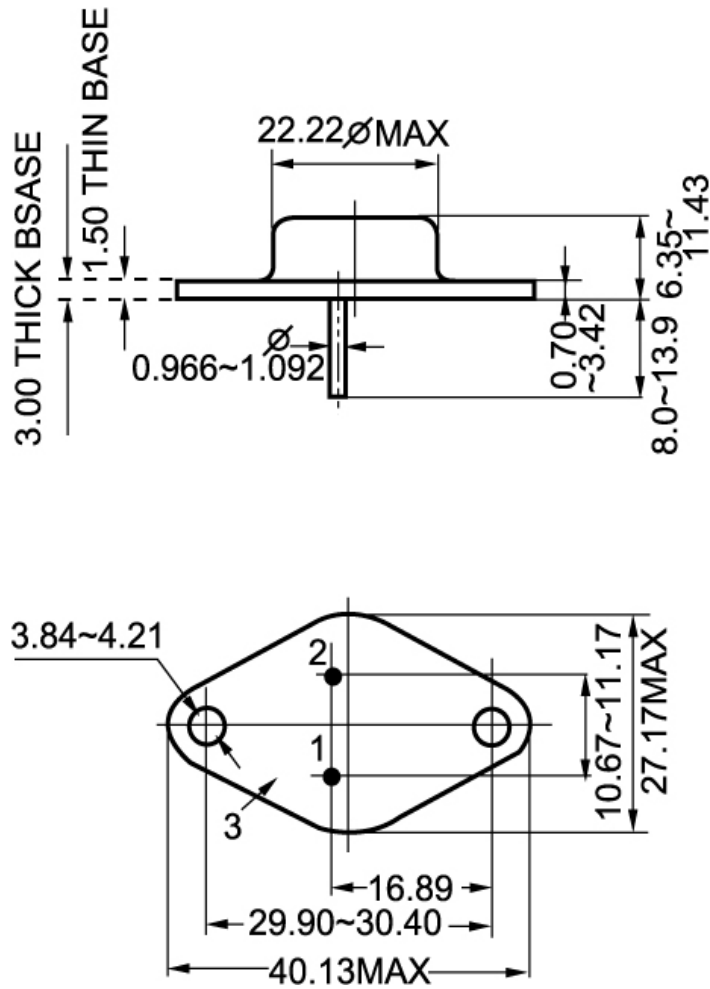


Fig.2 outline dimensions (unindicated tolerance: $\pm 0.1\text{mm}$)