

2SK2955

Silicon N Channel MOS FET
High Speed Power Switching

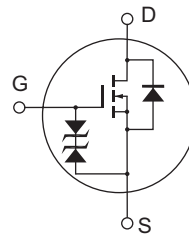
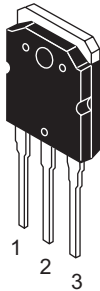
REJ03G1055-0400
(Previous: ADE-208-564B)
Rev.4.00
Sep 07, 2005

Features

- Low on-resistance
 $R_{DS} = 0.010 \Omega$ typ.
- High speed switching
- 4 V gate drive device can be driven from 5 V source

Outline

RENESAS Package code: PRSS0004ZE-A
(Package name: TO-3P)



1. Gate
2. Drain
(Flange)
3. Source

Absolute Maximum Ratings

(Ta = 25°C)

Item	Symbol	Ratings	Unit
Drain to source voltage	V_{DSS}	60	V
Gate to source voltage	V_{GSS}	±20	V
Drain current	I_D	45	A
Drain peak current	$I_{D(pulse)}$ ^{Note1}	180	A
Body-drain diode reverse drain current	I_{DR}	45	A
Avalanche current	I_{AP} ^{Note3}	45	A
Avalanche energy	E_{AR} ^{Note3}	173	mJ
Channel dissipation	P_{ch} ^{Note2}	100	W
Channel temperature	T_{ch}	150	°C
Storage temperature	T_{stg}	-55 to +150	°C

Notes: 1. $PW \leq 10 \mu s$, duty cycle $\leq 1 \%$
 2. Value at $T_c = 25^\circ C$
 3. Value at $T_{ch} = 25^\circ C$, $R_g \geq 50 \Omega$

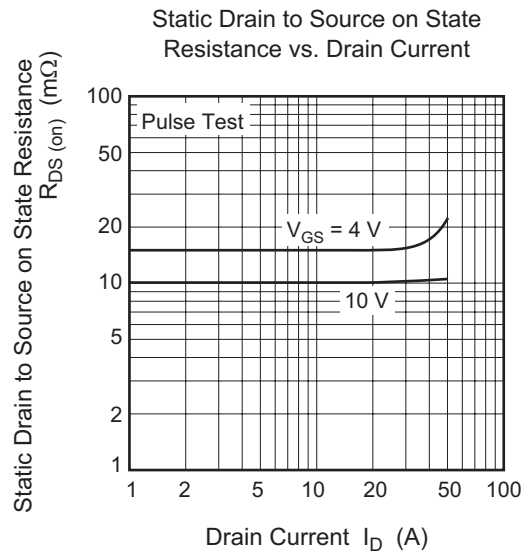
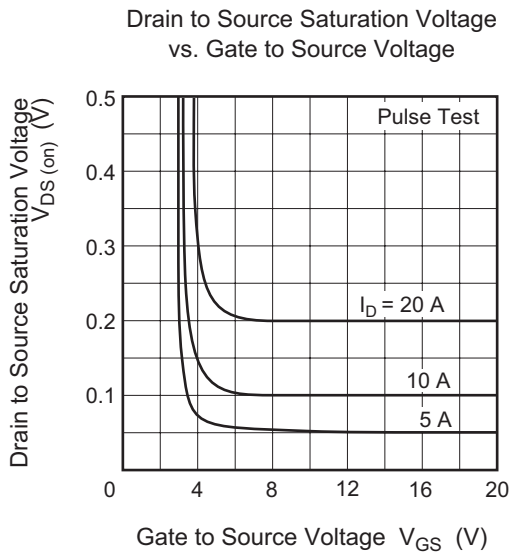
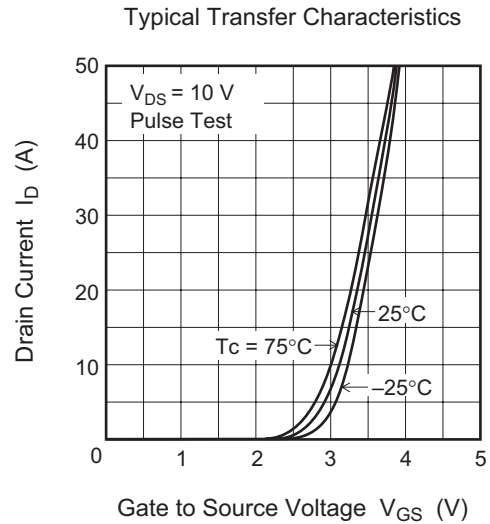
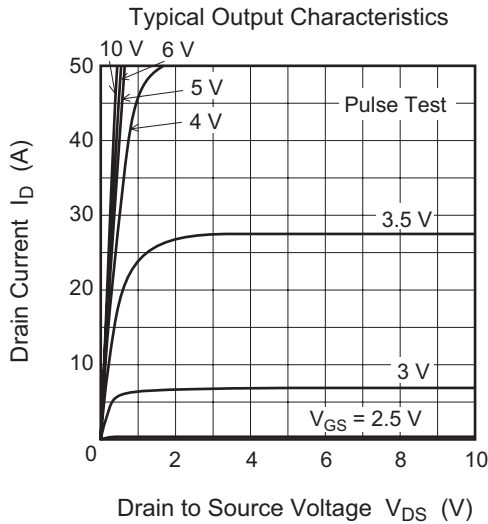
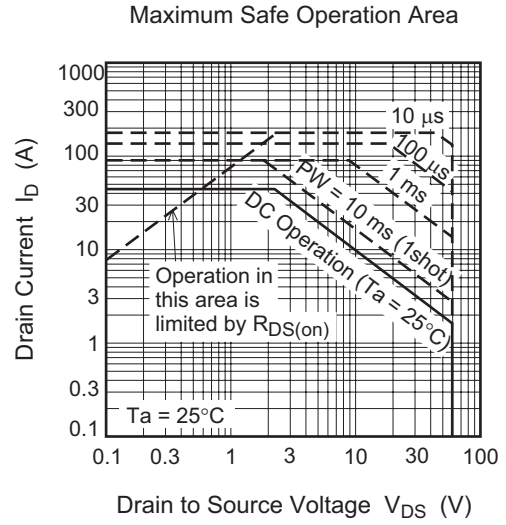
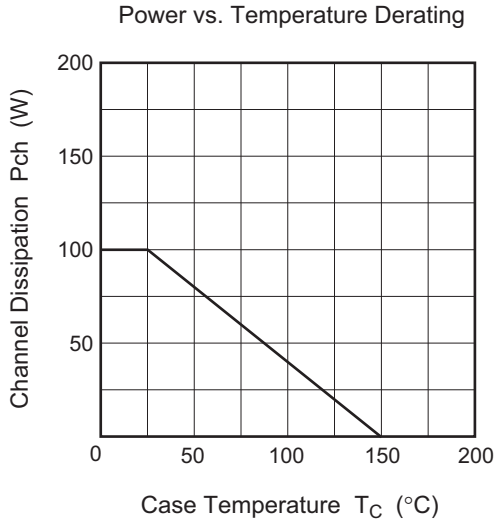
Electrical Characteristics

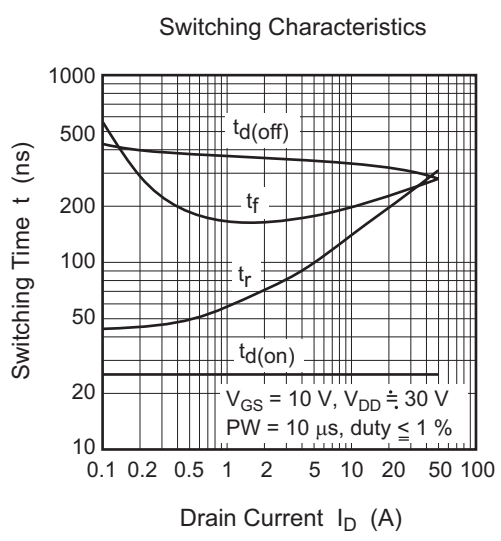
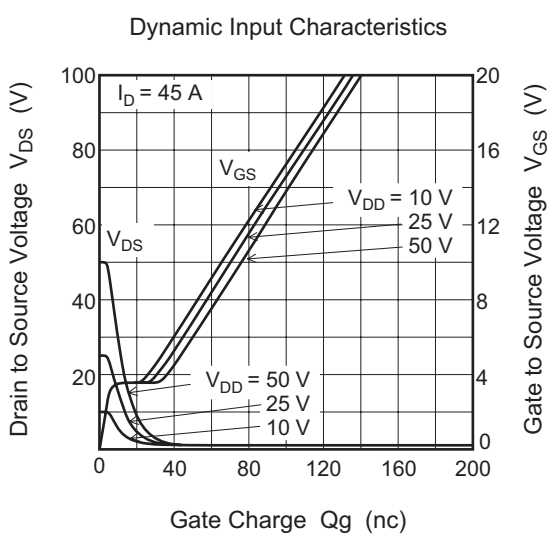
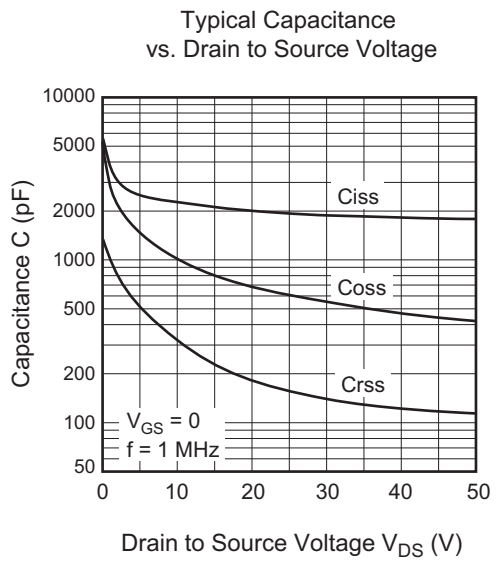
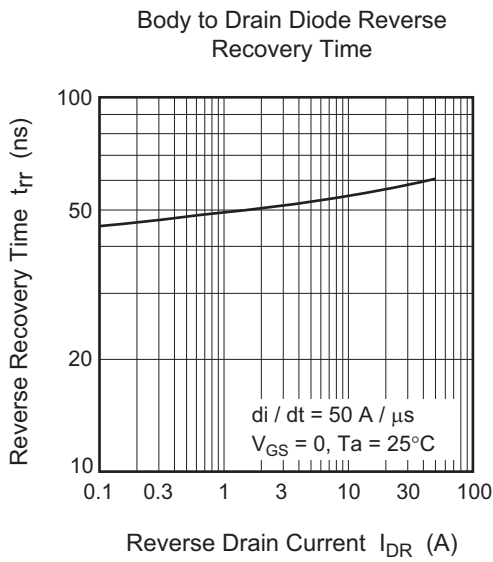
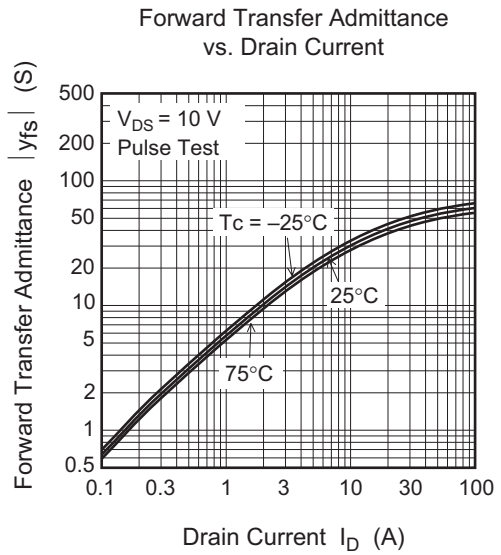
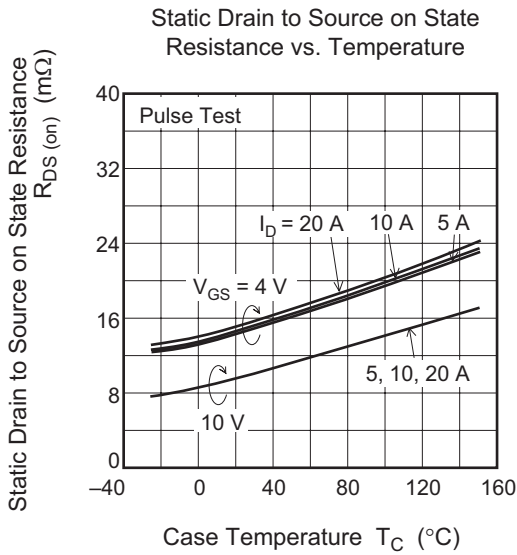
(Ta = 25°C)

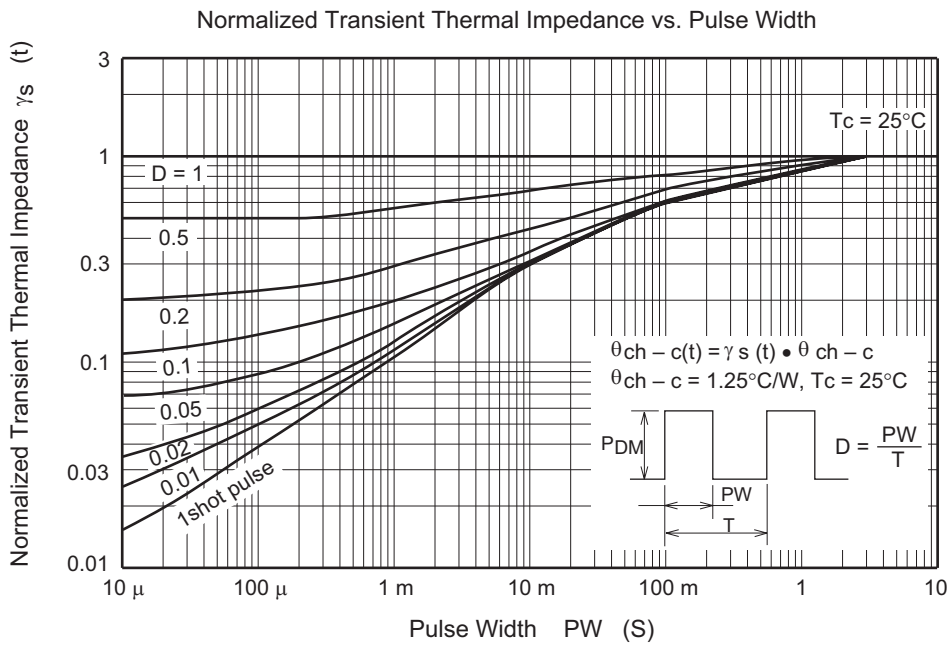
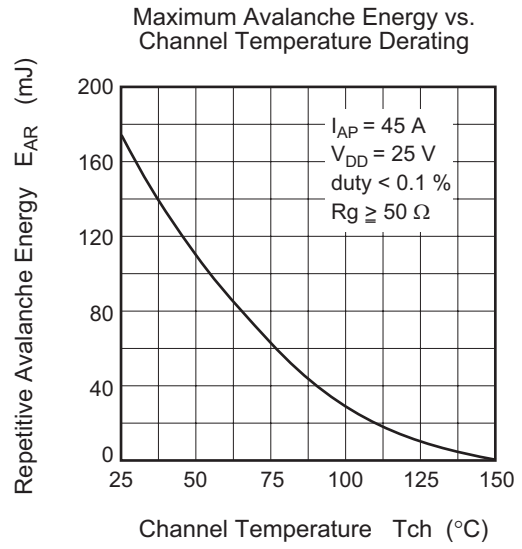
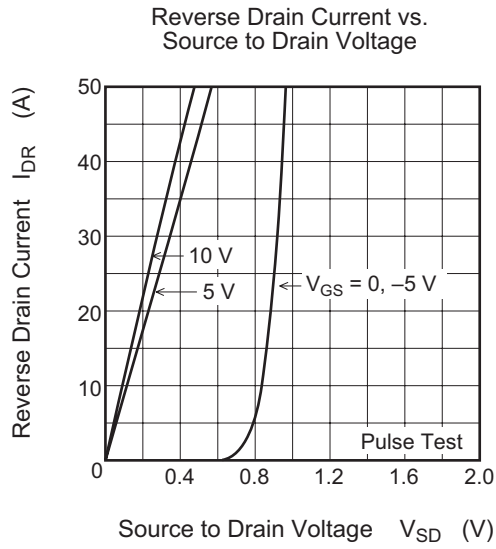
Item	Symbol	Min	Typ	Max	Unit	Test Conditions
Drain to source breakdown voltage	$V_{(BR)DSS}$	60	—	—	V	$I_D = 10 \text{ mA}$, $V_{GS} = 0$
Gate to source breakdown voltage	$V_{(BR)GSS}$	±20	—	—	V	$I_G = \pm 100 \mu A$, $V_{DS} = 0$
Gate to source leak current	I_{GSS}	—	—	±10	μA	$V_{GS} = \pm 16 \text{ V}$, $V_{DS} = 0$
Zero gate voltage drain current	I_{DSS}	—	—	10	μA	$V_{DS} = 60 \text{ V}$, $V_{GS} = 0$
Gate to source cutoff voltage	$V_{GS(off)}$	1.5	—	2.5	V	$I_D = 1 \text{ mA}$, $V_{DS} = 10 \text{ V}$
Static drain to source on state resistance	$R_{DS(on)}$	—	0.010	0.013	Ω	$I_D = 20 \text{ A}$, $V_{GS} = 10 \text{ V}$ ^{Note4}
	$R_{DS(on)}$	—	0.015	0.025	Ω	$I_D = 20 \text{ A}$, $V_{GS} = 4 \text{ V}$ ^{Note4}
Forward transfer admittance	$ y_{fs} $	24	40	—	S	$I_D = 20 \text{ A}$, $V_{DS} = 10 \text{ V}$ ^{Note4}
Input capacitance	C_{iss}	—	2200	—	pF	$V_{DS} = 10 \text{ V}$, $V_{GS} = 0$, $f = 1 \text{ MHz}$
Output capacitance	C_{oss}	—	1050	—	pF	
Reverse transfer capacitance	C_{rss}	—	320	—	pF	
Turn-on delay time	$t_{d(on)}$	—	25	—	ns	$I_D = 20 \text{ A}$, $V_{GS} = 10 \text{ V}$, $R_L = 1.5 \Omega$
Rise time	t_r	—	200	—	ns	
Turn-off delay time	$t_{d(off)}$	—	320	—	ns	
Fall time	t_f	—	240	—	ns	
Body-drain diode forward voltage	V_{DF}	—	0.95	—	V	$I_F = 45 \text{ A}$, $V_{GS} = 0$
Body-drain diode reverse recovery time	t_{rr}	—	60	—	ns	$I_F = 45 \text{ A}$, $V_{GS} = 0$ $diF/dt = 50 \text{ A}/\mu s$

Note: 4. Pulse test

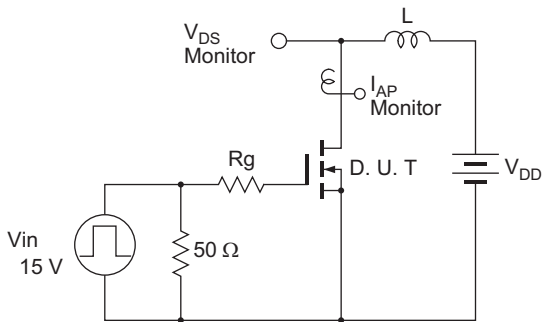
Main Characteristics



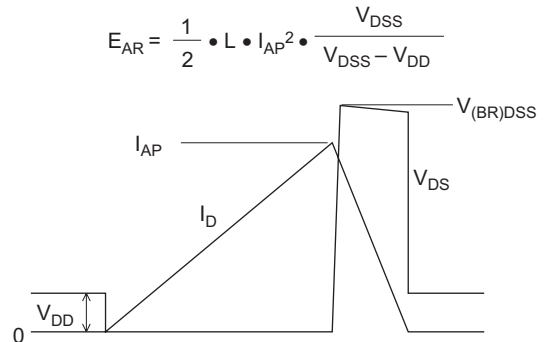




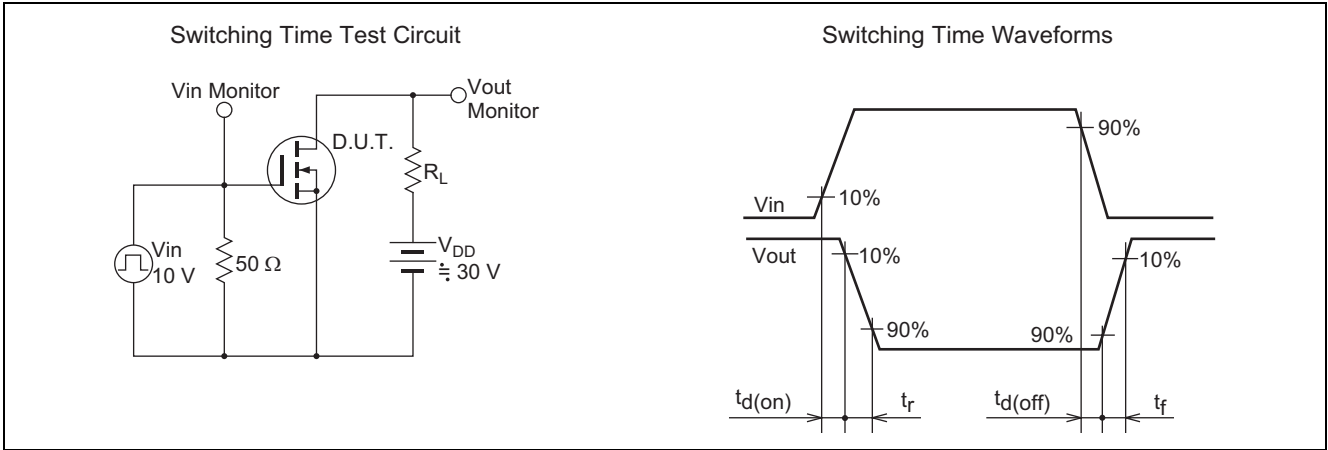
Avalanche Test Circuit



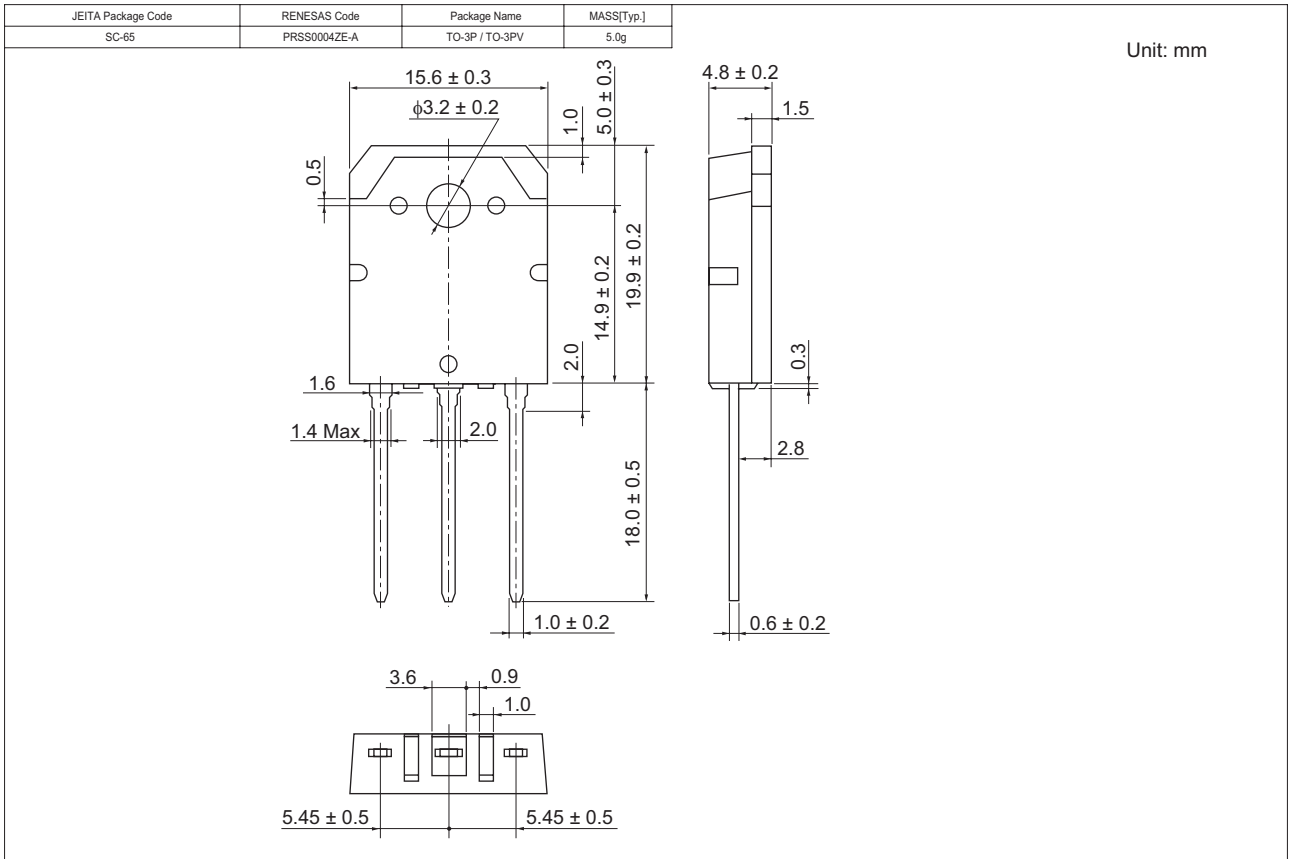
Avalanche Waveform



$$E_{AR} = \frac{1}{2} \cdot L \cdot I_{AP}^2 \cdot \frac{V_{DSS}}{V_{DSS} - V_{DD}}$$



Package Dimensions



Ordering Information

Part Name	Quantity	Shipping Container
2SK2955-E	360 pcs	Box (Tube)

Note: For some grades, production may be terminated. Please contact the Renesas sales office to check the state of production before ordering the product.

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