

# 2SK1151(L), 2SK1151(S) 2SK1152(L), 2SK1152(S)

Silicon N Channel MOS FET

REJ03G0907-0200 (Previous: ADE-208-1245)

Rev.2.00

Sep 07, 2005

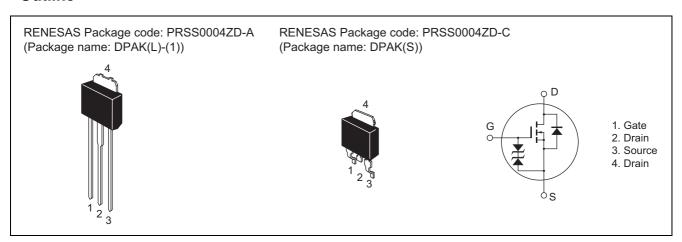
# **Application**

High speed power switching

### **Features**

- Low on-resistance
- High speed switching
- Low drive current
- No secondary breakdown
- Suitable for switching regulator and DC-DC converter

### **Outline**



# **Absolute Maximum Ratings**

 $(Ta = 25^{\circ}C)$ 

Item		Symbol	Ratings	Unit
Drain to source voltage 2SK1151		V <sub>DSS</sub>	450	V
	2SK1152	1	500	
Gate to source voltage		V <sub>GSS</sub>	±30	V
Drain current		I <sub>D</sub>	1.5	Α
Drain peak current		I <sub>D(pulse)</sub> *1	6	Α
Body to drain diode reverse drain current		I <sub>DR</sub>	1.5	А
Channel dissipation		Pch*2	20	W
Channel temperature		Tch	150	°C
Storage temperature		Tstg	-55 to +150	°C

Notes: 1. PW  $\leq$  10  $\mu$ s, duty cycle  $\leq$  1%

2. Value at  $T_C = 25^{\circ}C$ 

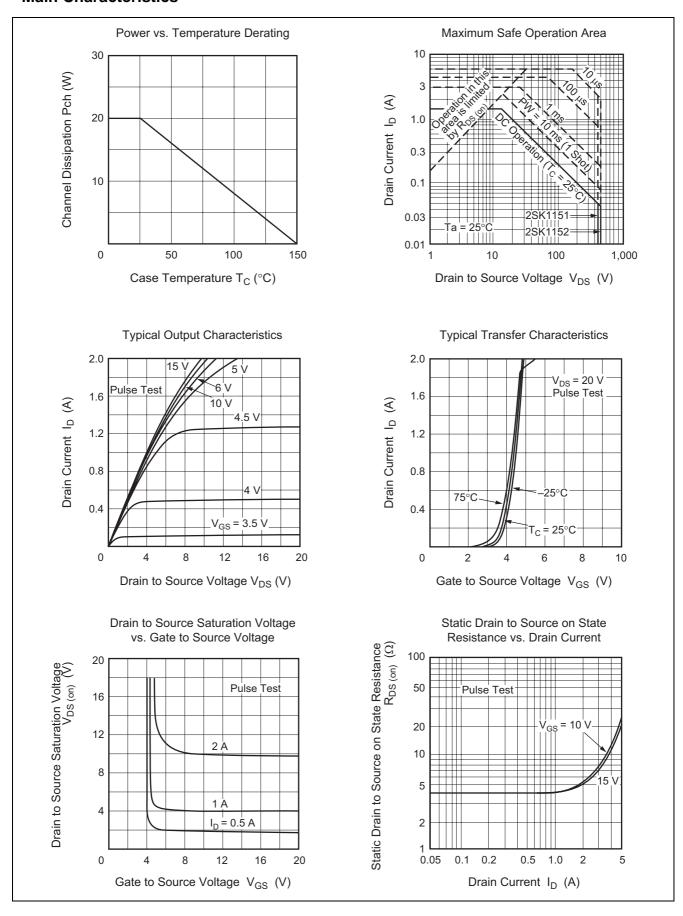
# **Electrical Characteristics**

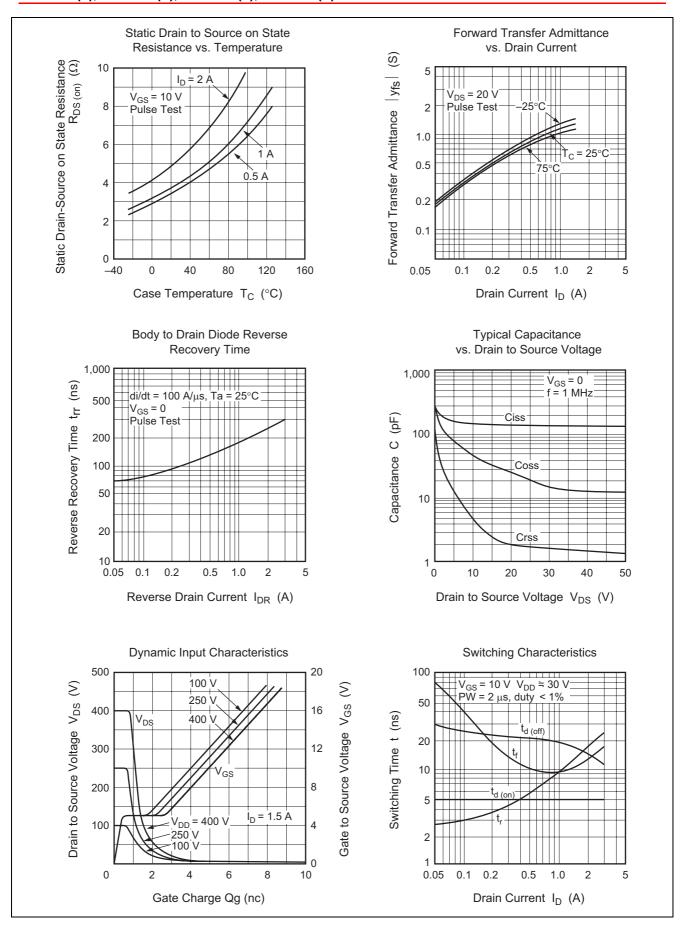
 $(Ta = 25^{\circ}C)$ 

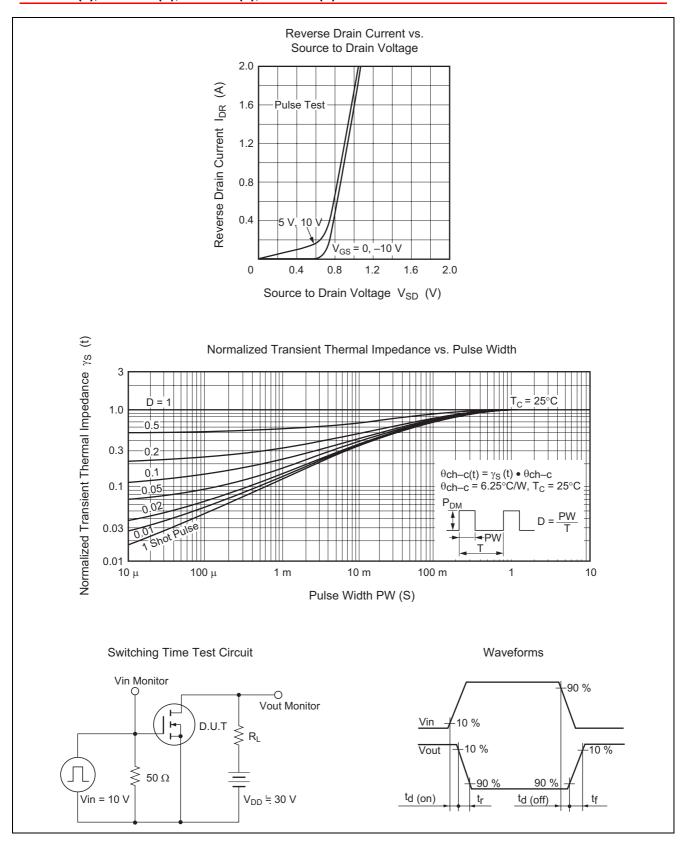
Item		Symbol	Min	Тур	Max	Unit	Test conditions
Drain to source breakdown	2SK1151	V <sub>(BR)DSS</sub>	450	_	_	V	$I_D = 10 \text{ mA}, V_{GS} = 0$
voltage	2SK1152		500				
Gate to source breakdown voltage		V <sub>(BR)GSS</sub>	±30	_	_	V	$I_G = \pm 100 \ \mu A, \ V_{DS} = 0$
Gate to source leak current		I <sub>GSS</sub>	_	_	±10	μΑ	$V_{GS} = \pm 25 \text{ V}, V_{DS} = 0$
Zero gate voltage drain 2SK1151		I <sub>DSS</sub>	_	_	100	μΑ	$V_{DS} = 360 \text{ V}, V_{GS} = 0$
current	2SK1152						$V_{DS} = 400 \text{ V}, V_{GS} = 0$
Gate to source cutoff voltage		$V_{GS(off)}$	2.0	_	3.0	V	$I_D = 1 \text{ mA}, V_{DS} = 10 \text{ V}$
Static drain to source on	2SK1151	R <sub>DS(on)</sub>	_	3.5	5.5	Ω	$I_D = 1 A, V_{GS} = 10 V^{*3}$
state resistance	2SK1152		_	4.0	6.0		
Forward transfer admittance		y <sub>fs</sub>	0.6	1.1	_	S	$I_D = 1 \text{ A}, V_{DS} = 20 \text{ V}^{*3}$
Input capacitance		Ciss	_	160	_	pF	$V_{DS} = 10 \text{ V}, V_{GS} = 0,$
Output capacitance		Coss	_	45	_	pF	f = 1 MHz
Reverse transfer capacitance		Crss	_	5	_	pF	
Turn-on delay time		t <sub>d(on)</sub>	_	5	_	ns	$I_D = 1 A, V_{GS} = 10 V,$
Rise time		t <sub>r</sub>	_	10	_	ns	$R_L = 30 \Omega$
Turn-off delay time		t <sub>d(off)</sub>	_	20	_	ns	
Fall time		t <sub>f</sub>	_	10	_	ns	
Body to drain diode forward voltage		$V_{DF}$	_	1.0	_	V	$I_F = 1.5 \text{ A}, V_{GS} = 0$
Body to drain diode reverse recovery		t <sub>rr</sub>	_	220	_	ns	$I_F = 1.5 \text{ A}, V_{GS} = 0,$
time							$di_F/dt = 100 A/\mu s$

Note: 3. Pulse test

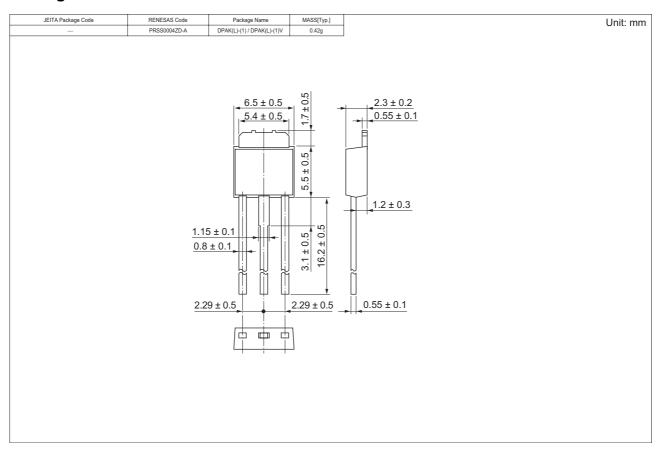
# **Main Characteristics**

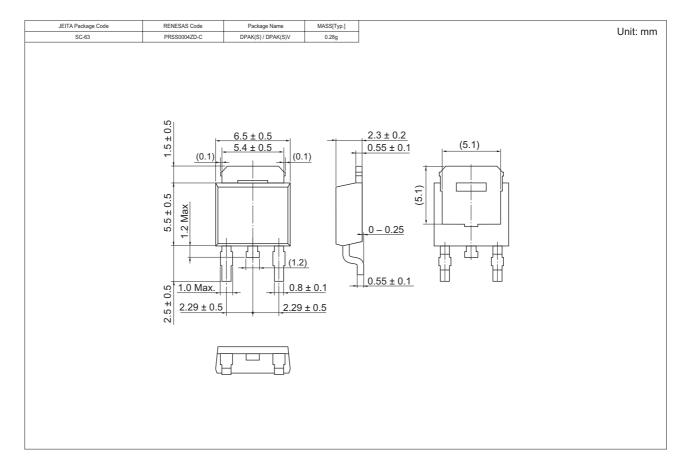






# **Package Dimensions**





# **Ordering Information**

Part Name	Quantity	Shipping Container
2SK1151L-E	3200 pcs	Box (Sack)
2SK1151STL-E	3000 pcs	Taping
2SK1152L-E	3200 pcs	Box (Sack)
2SK1152STL-E	3000 pcs	Taping

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