

# ACTOCX025

Compatible with Eu Directive  
2002/EC - RoHS



## FEATURES

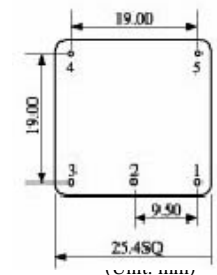
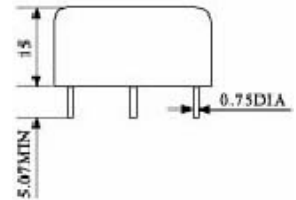
- Stabilities to  $\pm 0.01$ PPM ( AT Cut )
- Low Aging
- Voltage Controlled Frequency Adjustment
- Measurement Equipment

## APPLICATIONS

- PCS /Cellular Base Station
- Digital Switching
- Synthesizer
- SC Cut OCXO please enquire

## SPECIFICATIONS

Frequency Range	1~160MHz
Frequency Accuracy ( Adjustment 25°C)	$\pm 0.5$ PPM (Centre control voltage)
Frequency Stability vs Temperature	See Table 1
Aging (AT Cut)	$\pm 0.002$ PPM/day, first year $\pm 0.3$ PPM, 10 years $\pm 2$ PPM
(SC Cut)	$\pm 0.001$ PPM/day, first year $\pm 0.1$ PPM, 10 years $\pm 0.5$ PPM
Output Type and Load Characteristics	See Table 2
Frequency Stability vs Load	$\pm 0.02$ PPM vs $\pm 10\%$ load change
Supply Voltage	+3.3VDC, +5.0VDC, +12.0VDC
Frequency Stability vs Voltage	$\pm 0.02$ PPM vs $\pm 5\%$ voltage change
Supply Consumption	3.0W (max.) warm-up; 1.2W (max.) static
Warm-up Time (AT Cut)	$\pm 0.1$ PPM, <1 min.
(SC Cut)	$\pm 0.03$ PPM, <1 min.
Adjustable Frequency Range (AT Cut)	$\pm 7.0$ PPM
(SC Cut)	$\pm 1.0$ PPM
Control Voltage Range	0~5V
Slope	Positive
Linearity	$\pm 10\%$
Phase Noise (10MHz)	1Hz, -80dBc/Hz 10Hz, -120dBc/Hz 100Hz, -140dBc/Hz 1kHz, -145dBc/Hz 10kHz, -150dBc/Hz
Storage Temperature Range	-40~+100°C



### PIN FUNCTION:

- PIN1 – Output
- PIN2 – GND
- PIN3 – Control Voltage/NC
- PIN4 – Reference Voltage/NC
- PIN5 – Power Supply

### FREQUENCY STABILITY VS TEMPERATURE – TABLE 1

(Applies to frequencies < 20MHz & to 5v0 supply. For frequencies > 20MHz & 12v0 & 3v3 stabilities will be lower –Please enquire )

Frequency Stability vs Temperature	Temperature Range
$\pm 0.01$ PPM (AT Cut)	0 - +50°C
$\pm 0.03$ PPM (AT Cut)	-20 - +70°C
$\pm 0.05$ PPM (AT Cut)	40 - +75°C

### OUTPUT TYPE AND LOAD CHARACTERISTIC – TABLE 2

Output Waveform	Frequency Range	Oscillation State	Output Characteristics
Clipped Sine Wave	8MHz - 30MHz 10MHz - 100MHz	F: Fundamental O: Overtone	Load: 10k $\Omega$ /10pF Output level: >1Vp-p
TTL	1MHz - 30MHz 10MHz - 100MHz	F: Fundamental O: Overtone	Load: Max. 10 low power consumption TTL gates “1” level: >+2.4VDC; “0” level: <+0.2VDC Duty cycle: 45/55 Rise/fall time: <6ns
HCMOS	1MHz - 30MHz 10MHz - 100MHz	F: Fundamental O: Overtone	Load: Max. 10 low power consumption TTL/HCMOS “1” level: >+4.3VDC; “0” level: <+0.2VDC Duty cycle: 45/55 Rise/fall time: <6ns
ACMOS	1MHz - 30MHz 10MHz - 100MHz	F: Fundamental O: Overtone	Load: Max. 10 low power consumption TTL/ACMOS “1” level: >+4.3VDC; “0” level: <+0.5VDC Duty cycle: 45/55 Rise/fall time: <6ns

Please note that all parameters cannot necessarily be specified in one device.

Customer to specify : Frequency, Output, Voltage, Stability, and Operating Temperature

In line with our ongoing policy of product improvement and evolution the above specification may be subject to change without notice

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For quotations or further information, please contact us at :

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