

DATA SHEET

SMV1269-074, SMV1269-074LF: Hyperabrupt Junction Tuning Varactor

Features

- High capacitance ratio
- Ultrasmall SC-70 package
- Available lead (Pb)-free and RoHS-compliant MSL-1 @ 260 °C per JEDEC J-STD-020
- Designed for high-volume, low-cost battery applications
- · Available in tape and reel packaging

Description

The SMV1269-074 is a dual silicon hyperabrupt junction varactor diode in a common cathode configuration specifically designed for battery operation. The specified high capacitance ratio and low $R_{\rm S}$ of this varactor make it appropriate for low noise VCOs used at frequencies in wireless systems to beyond 2.5 GHz. Applications include low noise and wideband UHF and VHF VCO for GSM, PCS, CDMA and analog phones.



Skyworks offers lead (Pb)-free, RoHS (Restriction of Hazardous Substances)-compliant packaging.



Absolute Maximum Ratings

Characteristic	Value	
Forward current (I _F)	20 mA	
Power dissipation (P _D)	250 mW	
Storage temperature (T _{ST})	-55 °C to +150 °C	
Operating temperature (T _{OP})	-55 °C to +125 °C	
ESD human body model	Class 0	

Performance is guaranteed only under the conditions listed in the specifications table and is not guaranteed under the full range(s) described by the Absolute Maximum specifications. Exceeding any of the absolute maximum/minimum specifications may result in permanent damage to the device and will void the warranty.

CAUTION: Although this device is designed to be as robust as possible, ESD (Electrostatic Discharge) can damage this device. This device must be protected at all times from ESD. Static charges may easily produce potentials of several kilovolts on the human body or equipment, which can discharge without detection. Industry-standard ESD precautions must be employed at all times.

Electrical Specifications at 25°C

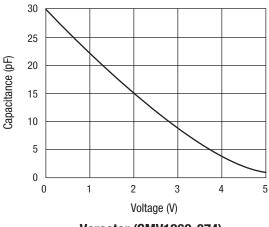
Parameter	Condition	Min.	Тур.	Max.	Unit
Reverse current (I _R)	$V_R = 8 V$			20	nA
Capacitance (C _T)	$C_T @ 0.5 \text{ V}, V_R = 0.5 \text{ V}, F = 1 \text{ MHz}$	19.2	20.5	21.8	pF
Capacitance (C _T)	C_T @ 2.5 V, V_R = 2.5 V, F = 1 MHz	6.5	7.3	8.1	pF
Capacitance ratio (C _{TR})	C _T (0.5 V)/C _T (2.5 V)	2.5	2.8		
Series resistance (R _S)	V _R = 1 V, F = 900 MHz		0.6	0.8	Ω
Breakdown voltage (V _{BR})	I _R = 10 μA	10			V

Common Cathode		
SC-70		
SMV1269-074 Marking: AE3		
SMV1269-074LF Marking: EE3		
L _S = 1.4 nH		

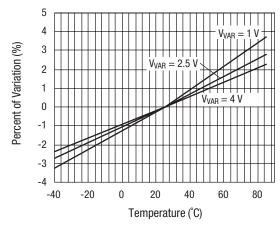


LF denotes lead (Pb)-free, RoHS-compliant packaging option as an alternative to our standard tin/lead (Sn/Pb) packaging.

Typical Performance Data

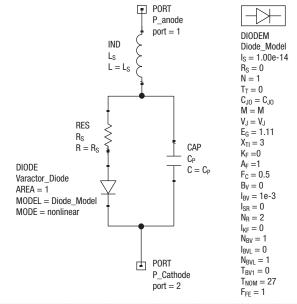






Relative Capacitance Change vs. Temperature

SPICE Model



Part Number	C _{JO} (pF)	V _J (V)	М	C _P (pF)	R _S (Ω)	L _S (nH)
SMV1269-074	28.5	6.3	4.2	0.5	0.6	1.4

Recommended Solder Reflow Profiles

Refer to the "<u>Recommended Solder Reflow Profile</u>" Application Note.

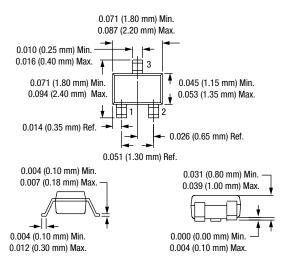
Tape and Reel Information

Refer to the "<u>Discrete Devices and IC Switch/Attenuators</u> Tape and Reel Package Orientation" Application Note.

Capacitance vs. Voltage

V _R (V)	C _T (pF)
0	29
0.2	25.3
0.4	22.4
0.6	20.1
0.8	18
1	16.2
1.2	14.6
1.4	13.2
1.6	11.9
1.8	10.7
2	9.6
2.2	8.7
2.4	7.8
2.6	7
2.8	6.3
3	5.7
3.2	5.2
3.4	4.8
3.6	4.4
3.8	4.1
4	3.9
4.2	3.7
4.4	3.5
4.6	3.3
4.8	3.2
5	3.1

SC-70



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