

IR3315(S)PbF

LOW EMI CURRENT SENSE HIGH SIDE SWITCH

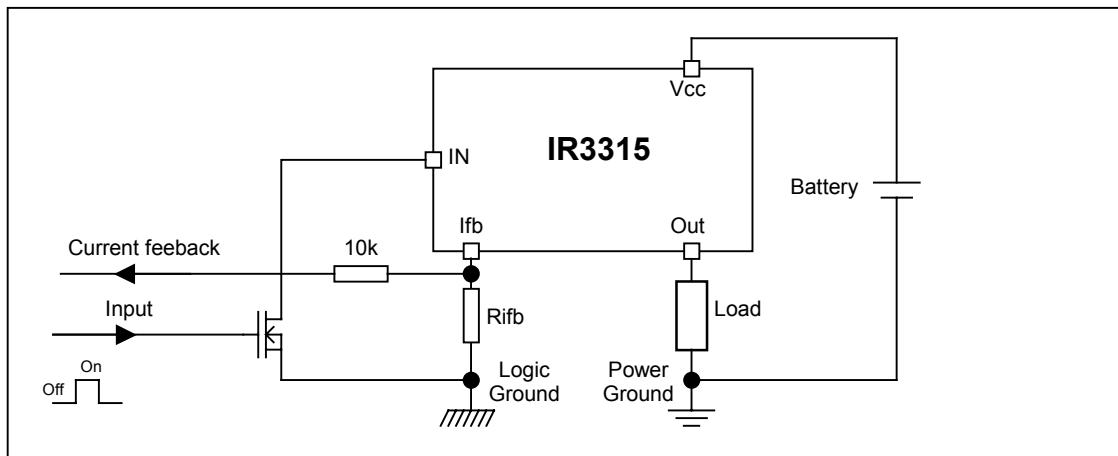
Features

- Load current feedback
- Programmable over current shutdown
- Active clamp
- ESD protection
- Input referenced to Vcc
- Over temperature shutdown
- Reverse battery protection
- Lead-Free

Description

The IR3315(S)PbF is a fully protected 4 terminals high side switch. The input signal is referenced to Vcc. When the input voltage Vcc - Vin is higher than the specified threshold, the output power Mosfet is turned on. When the Vcc - Vin is lower than the specified Vil threshold, the output Mosfet is turned off. A current proportional to the power Mosfet current is sourced to the Ifb pin. Over current shutdown occurs when Vst-Vin > 4.5V. The current shutdown threshold is adjusted by selecting the proper RIfb. Either over current and over temperature latches off the switch. The device is reset by pulling the input pin high. Other integrated protections (ESD, reverse battery, active clamp) make the switch very rugged in automotive environment.

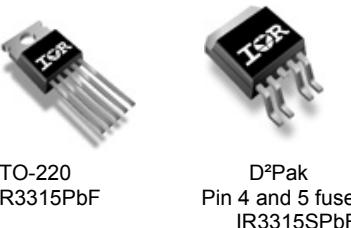
Typical Connection



Product Summary

Rds(on)	20 mΩ max.
Vcc op.	6 to 32V
Current Ratio	2800
Prog. Ishutdown	3 to 30A
Vclamp	40V

Packages



Absolute Maximum Ratings

Absolute maximum ratings indicate sustained limits beyond which damage to the device may occur. All voltage parameters are referenced to Vcc lead. (T_{ambient}=25°C unless otherwise specified).

Symbol	Parameter	Min.	Max.	Units
Vcc-Vin	Maximum Vcc voltage	-16	37	V
Vcc-Vin cont.	Maximum continuous Vcc voltage	-16	32	
Vcc-Vfb	Maximum Ifb voltage	-16	33	
Vcc-Vout	Maximum output voltage	-0.3	37	
Ids cont.	Maximum body diode continuous current R _{th} =60°C/W (1)	—	2.8	A
Ids pulsed	Maximum body diode pulsed current (1)	—	100	
Pd	Maximum power dissipation R _{th} =60°C/W	—	2	W
ESD1	Electrostatic discharge voltage (Human body) C=100pF, R=1500Ω	—	4	kV
ESD2	Electrostatic discharge voltage (Machine Model) C=200pF, R=0Ω	—	0.5	
T _j max.	Max. storage & operating temperature junction temperature	-40	150	°C
Min R _{fb}	Minimum on the resistor on Ifb pin	0.3	—	kΩ
Ifb max.	Max. Ifb current	-50	50	mA

(1) Limited by junction temperature. Pulsed is also limited by wiring

Thermal Characteristics

Symbol	Parameter	Typ.	Max.	Units
R _{th1}	Thermal resistance junction to ambient D ² -Pak Std footprint	60	—	°C/W
R _{th2}	Thermal resistance junction to case D ² -Pak	1.6	—	
R _{th3}	Thermal resistance junction to case TO-220	1.6	—	

Recommended Operating Conditions

These values are given for a quick design. For operation outside these conditions, please consult the application notes.

Symbol	Parameter	Min.	Max.	Units
I _{out}	Continuous output current	—	14	A
	T _{ambient} =85°C, R _{th} =5°C/W, T _j =125°C			
	T _{ambient} =85°C, R _{th} =60°C/W, T _j =125°C			
R _{ifb}	Recommended Ifb resistor (2)(3)	0.5	3.5	kΩ
Pulse min.	Minimum turn-on pulse width	1	—	ms
Fmax.	Maximum operating frequency	—	200	Hz

2) If R_{ifb} is too low, the device can be damaged.

3) If R_{ifb} is too high, the device may not switch on.

Protection Characteristics

T_j=25°C, R_{ifb}=500 to 5kΩ

Symbol	Parameter	Min.	Typ.	Max.	Units	Test Conditions
V _{ifb} -V _{in} @I _{sd}	Over-current shutdown threshold	4	4.7	5.6	V	
T _{sd}	Over temperature threshold	—	165	—	°C	See fig. 5
OV	Over voltage protection (not latched)	33	35	39	V	
I _{sd} f	Fixed over current shutdown	30	38	47	A	V _{ifb} <V _{ifb} -V _{in} @I _{sd}
I _{sd} _1k	Programmable over current shutdown 1k	9	12	16		R _{ifb} =1kΩ
T _{reset}	Time to reset protection	—	50	500	μs	See fig. 5
Min. pulse	Min. pulse width (no WAIT state)	200	400	1200		
WAIT	WAIT function timer	0.4	1	2	ms	See fig. 4 and 5
R _{ds(on)} rev.	Reverse battery On state resistance	10	16	28	mΩ	V _{cc} -V _{in} =-14V, I _{out} =10A

Static Electrical Characteristics

T_j=25°C, V_{cc}=14V (unless otherwise specified)

Symbol	Parameter	Min.	Typ.	Max.	Units	Test Conditions
V _{cc} op.	Operating Voltage range	6	—	32	V	
I _{cc} off	Supply leakage current	—	1.5	5	μA	V _{in} =V _{cc} , V _{cc} -V _{out} =14V, V _{cc} -V _{ifb} =14V
I _{in} , on	On state IN positive current	1.5	3	6	mA	V _{cc} -V _{in} =14V
V _{ih}	High level Input threshold voltage (4)	—	5.4	6.2	V	
V _{il}	Low level Input threshold voltage (4)	4	4.9	5.8		
V _{hyst}	Input hysteresis V _{ih} -V _{il}	0.2	0.4	1		
I _{out}	Drain to source leakage current	—	1.2	5	μA	V _{in} =V _{cc} , V _{cc} -V _{ifb} =0V, V _{cc} -V _{out} =14V
R _{ds(on)}	On state resistance (5) T _j =25°C	10	15	20	mΩ	I _{out} =10A, V _{cc} -V _{in} =14V
	On state resistance (5) T _j =25°C	10	16	28		I _{out} =7A, V _{cc} -V _{in} =6V
	On state resistance (5) T _j =150°C	20	28.5	38		I _{out} =10A, V _{cc} -V _{in} =14V
V clamp1	V _{cc} to V _{out} clamp voltage 1	36	39	—	V	I _{out} =50mA
V clamp2	V _{cc} to V _{out} clamp voltage 2	—	40	43		I _{out} =10A

(4) Input thresholds are measured directly between the input pin and the tab. Any parasitic resistance in common between the load current path and the input signal path can significantly affect the thresholds.

(5) R_{dson} is measured between the tab and the Out pin, 5mm away from the package.

Switching Electrical Characteristics

V_{cc}=14V, Resistive load=4Ω, T_j=25°C

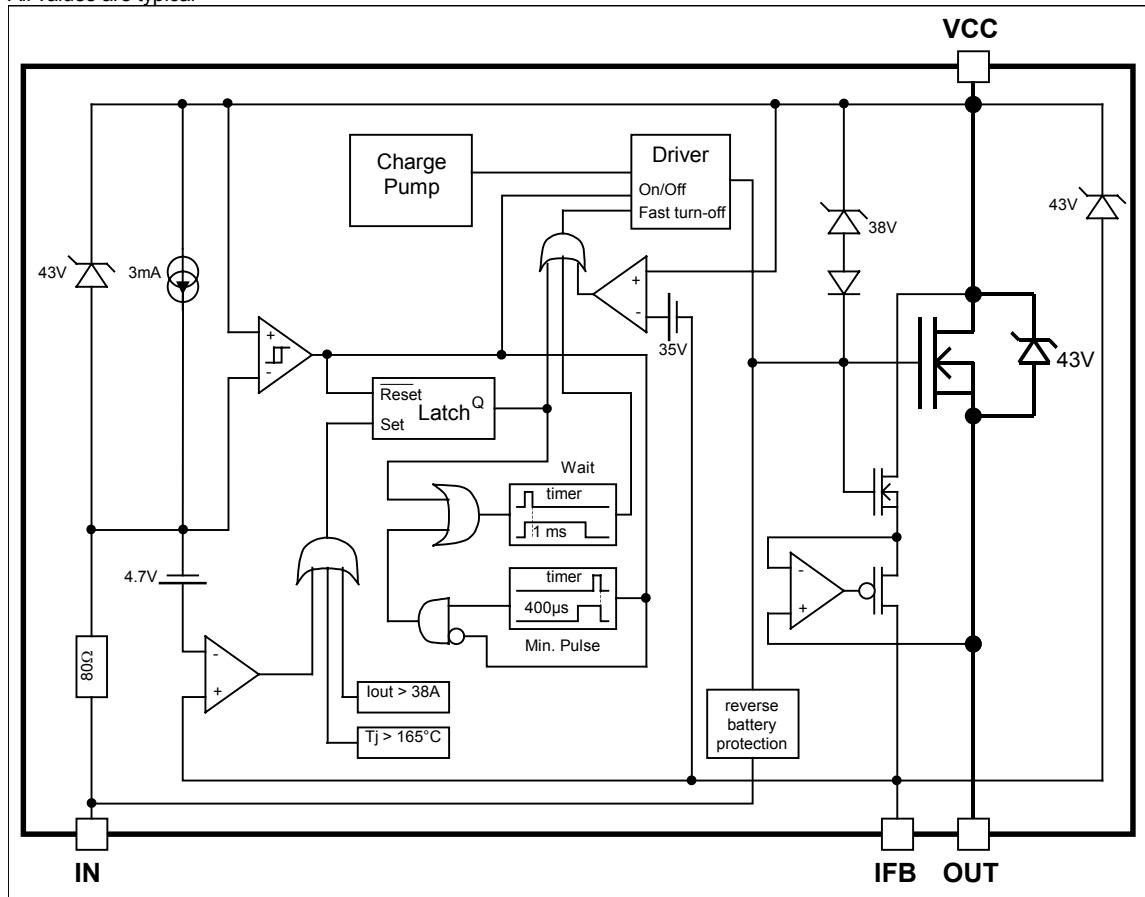
Symbol	Parameter	Min.	Typ.	Max.	Units	Test Conditions
T _{don}	Turn on delay time to 10% V _{cc}	3	11	27	μs	See figure 2
T _{r1}	Rise time to V _{cc} -V _{out} =5V	1	4	10		
T _{r2}	Rise time to V _{cc} -V _{out} =0.1V _{cc}	2	8	20		
E _{on}	Turn on energy	—	0.2	—	mJ	
T _{doff}	Turn off delay time	10	40	100	μs	
T _f	Fall time to V _{out} =10% of V _{cc}	2	8	20		
E _{off}	Turn off energy	—	0.1	—	mJ	

Current Sense Characteristics

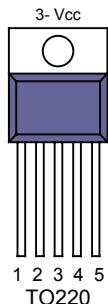
Symbol	Parameter	Min.	Typ.	Max.	Units	Test Conditions
Ratio	I Load/lifb current ratio	2,400	2,800	3,200	—	T _j =25°C, R _{fb} =500Ω, I _{out} =20A
Ratio_TC	I Load/lifb variation aver temperature	-5	—	+5	%	T _j =-40°C to 150°C
Offset	Load current diagnostic offset	-0.08	0	+0.07	A	I _{out} =1A
T _{rst}	lifb response time (low signal)	—	1	—	μs	90% of the I _{out} step

Functional Block Diagram

All values are typical



Lead Assignments



1- In
2- Ifb
3- Vcc (tab)
4- Out
5- Out

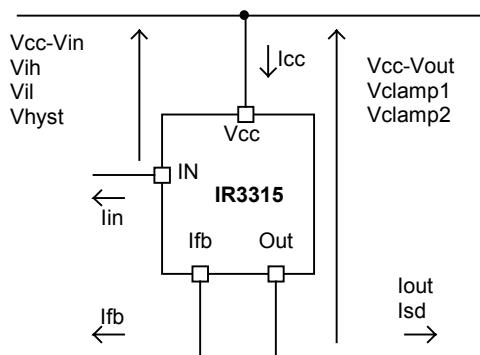
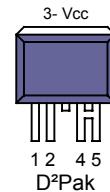


Figure 1 – Voltages and current definitions

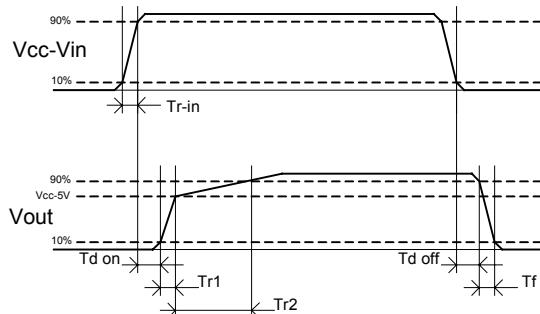
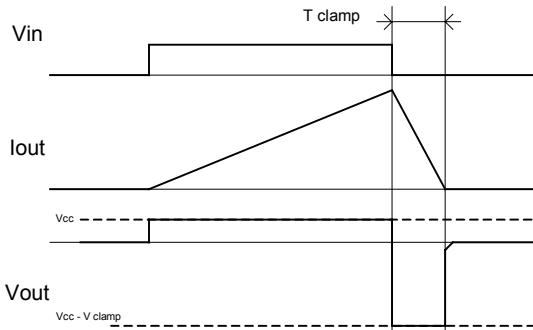


Figure 2 – Switching time definitions



See Application Notes to evaluate power dissipation

Figure 3 – Active clamp waveforms

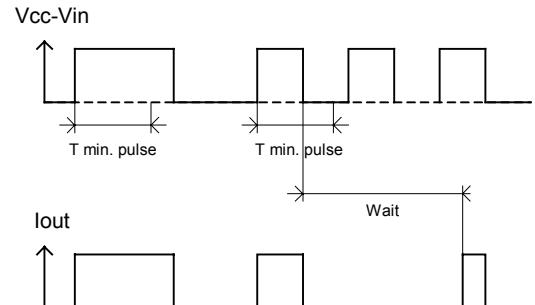


Figure 4 – Min. pulse and Wait function

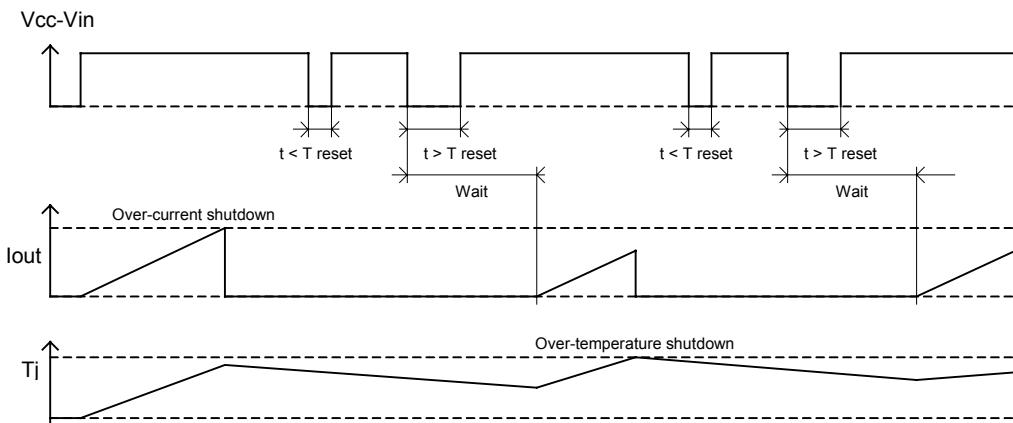


Figure 5 – Protection Timing Diagrams

All curves are typical characteristics. Operation in hatched areas is not recommended. $T_j=25^\circ\text{C}$, $R_{fb}=500\text{ohm}$, $V_{cc}=14\text{V}$ (unless otherwise specified).

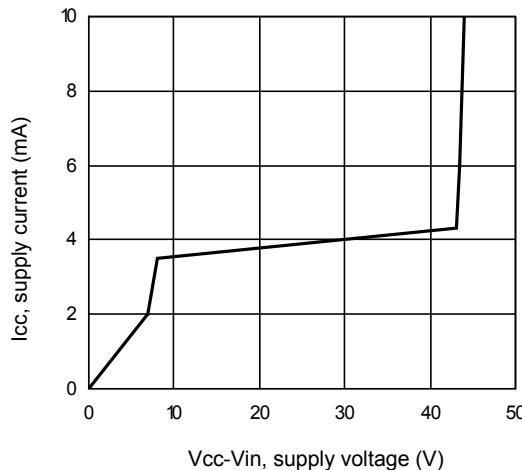


Figure 6 – I_{cc} (mA) Vs $V_{cc}-V_{in}$ (V)

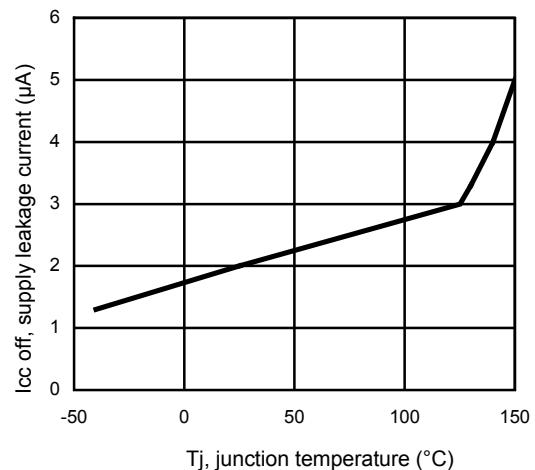


Figure 7 – $I_{cc\ off}$ (μA) Vs T_j ($^\circ\text{C}$)

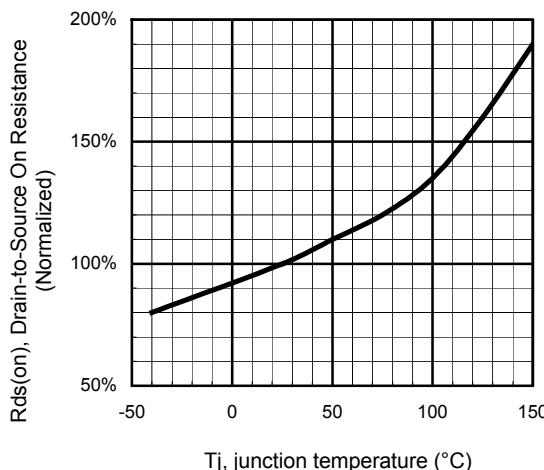


Figure 8 - Normalized $R_{ds(on)}$ (%) Vs T_j ($^\circ\text{C}$)

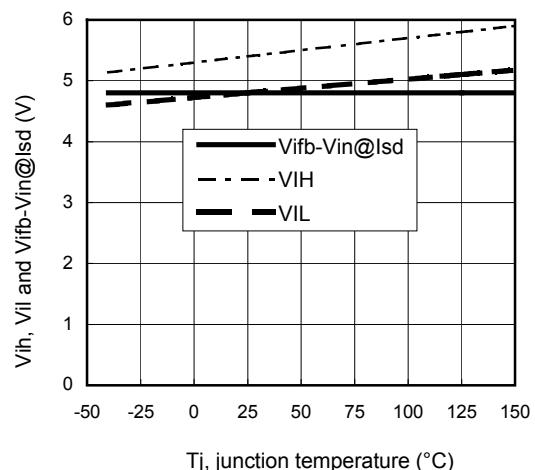


Figure 9 – V_{ih} , V_{il} and $V_{ifb}-V_{in}@Isd$ (V) Vs T_j ($^\circ\text{C}$)

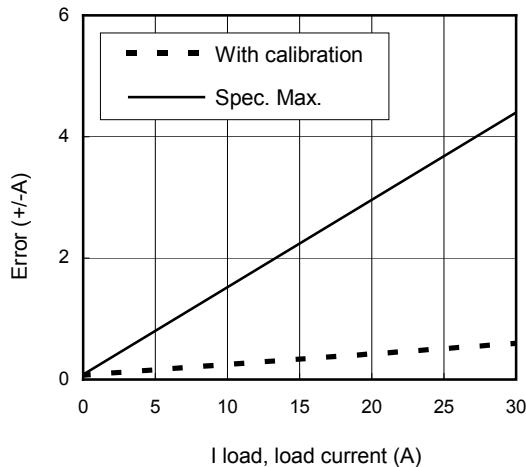


Figure 10 – Error (+/- A) Vs I load (A)

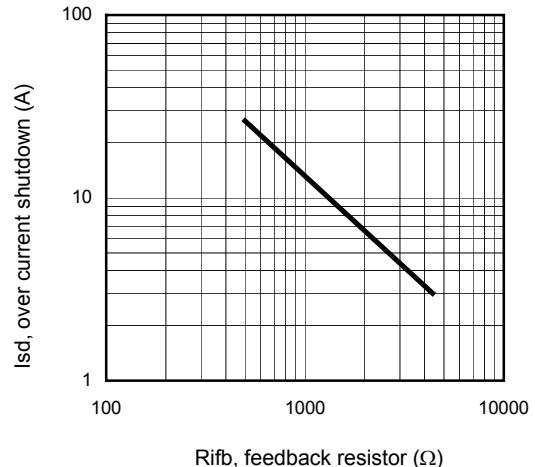


Figure 11 – Ids (A) Vs Rifb (Ω)

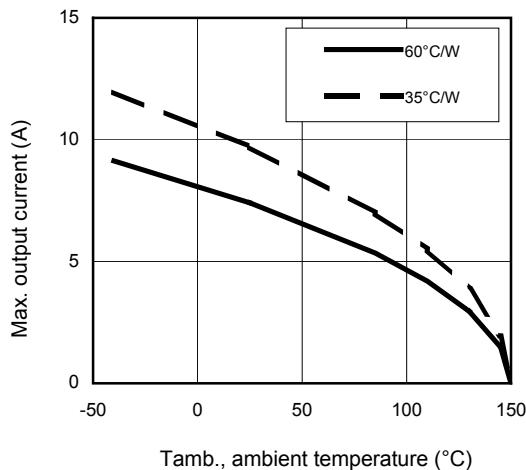


Figure 12 – Max. Iout (A) Vs Tamb. (°C)

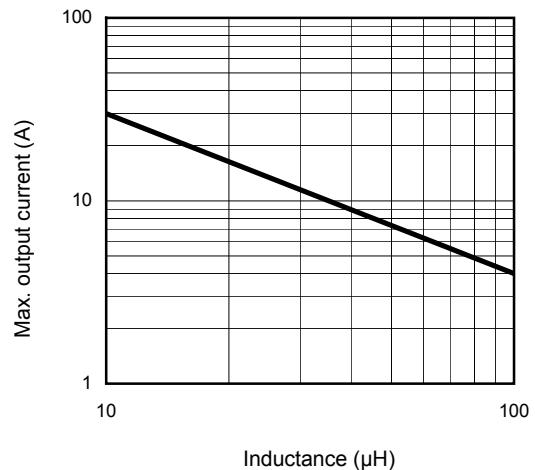


Figure 13 – Max. Iout (A) Vs inductance (μH)

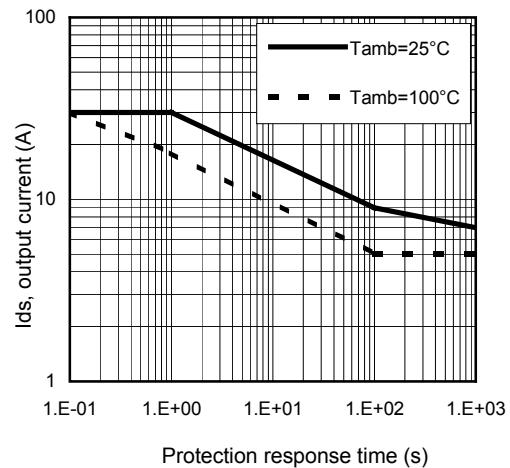


Figure 14 – Ids (A) Vs over temperature protection response time (s)

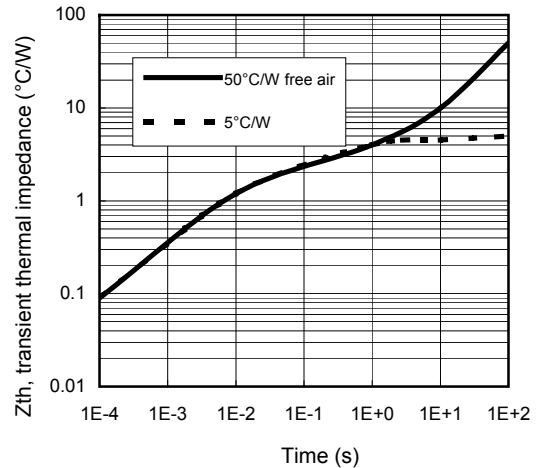
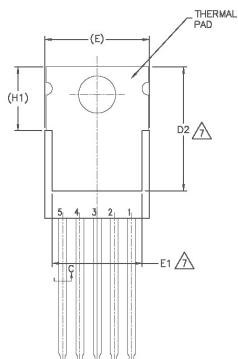
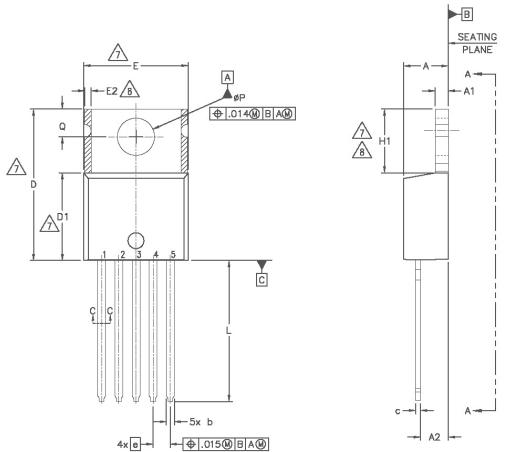


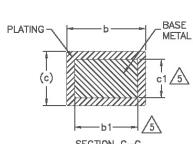
Figure 15 – Transient thermal impedance (°C/W) Vs time (s)

Case Outline – TO220 5 leads - Automotive Q100 PbF qualified



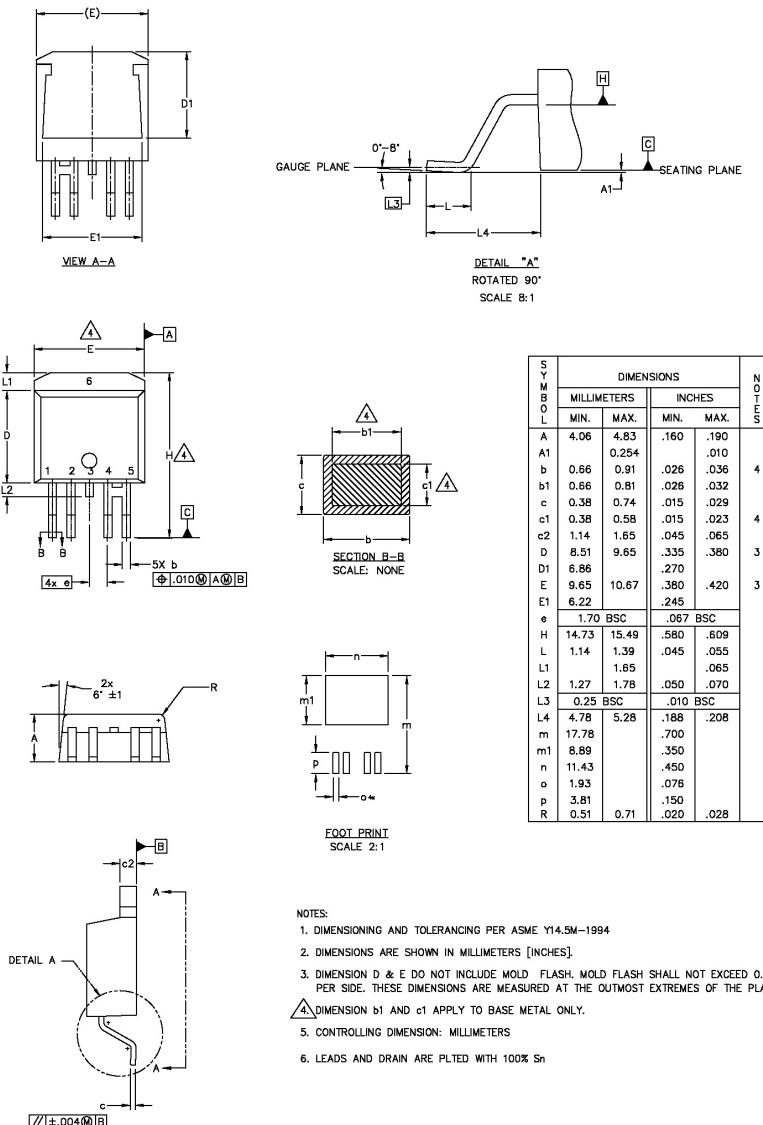
S Y M I C L	DIMENSIONS		N O R M S
	MILLIMETERS	INCHES	
	MIN.	MAX.	
A	3.56	.483	.140 .190
A1	.051	.140	.020 .055
A2	2.03	.292	.080 .115
b	0.64	.089	.025 .035
b1	0.64	.084	.025 .033
c	0.36	.061	.014 .024
c1	0.36	.056	.014 .022
D	14.22	16.51	.560 .650
D1	8.38	9.02	.330 .355
D2	11.68	12.88	.460 .507
E	9.65	10.67	.380 .420
E1	6.86	8.69	.270 .350
E2	—	.076	— .030
e	1.70 BSC	—	.067 BSC
H1	5.84	6.86	.230 .270
L	12.70	14.73	.500 .580
nP	3.53	3.73	.139 .147
Q	2.54	3.05	.100 .120

SECTION A-A

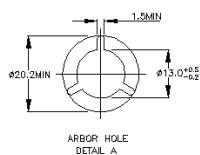
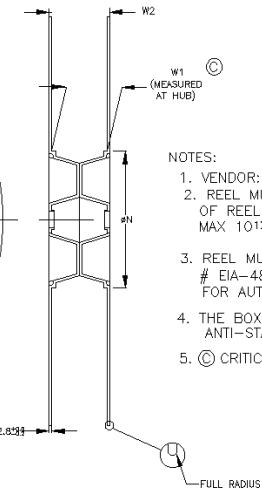
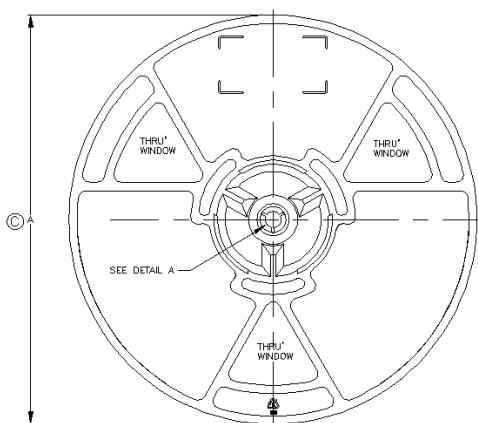


- NOTES:
- 1.– DIMENSIONING AND TOLERANCING AS PER ASME Y14.5 M - 1994.
 - 2.– DIMENSIONS ARE SHOWN IN INCHES (MILLIMETERS).
 - 3.– LEAD DIMENSION AND FINISH UNCONTROLLED IN 11.
 - 4.– DIMENSION D, D1 & E DO NOT INCLUDE MOLD FLASH. MOLD FLASH SHALL NOT EXCEED .005" (.0127) PER SIDE. THESE DIMENSIONS ARE MEASURED AT THE OUTERMOST EXTREMES OF THE PLASTIC BODY.
 - 5.– DIMENSION B1 & c1 APPLY TO BASE METAL ONLY.
 - 6.– CONTROLLING DIMENSION : INCHES.
 - 7.– THERMAL PAD CONTOUR OPTIONAL, WITHIN DIMENSIONS E,H1,D2 & E1
 - 8.– DRAINS ARE ALLOWED, BUT NO DRILLING, STRIPPING AND SINGULATION IRRREGULARITIES ARE ALLOWED.
 - 9.– OUTLINE CONFORMS TO JEDEC TO-220, EXCEPT A2 (max.) AND D2 (min.) WHERE DIMENSIONS ARE DERIVED FROM THE ACTUAL PACKAGE OUTLINE.
 - 10.– LEADS AND DRAIN ARE PLATED WITH 100% Sn

Case Outline 5 Lead - D2PAK - Automotive Q100 PbF MSL1 qualified



Tape & Reel 5 Lead - D2PAK



\varnothing A MAX = 330 \odot
 \varnothing N MAX = 100
 W1 = 24.4 $\frac{1}{2}$ \odot
 W2 MAX = 30.4

NOTES:

1. VENDOR: OPTIONAL
2. REEL MUST HAVE ANTI-STATIC COATING SURFACE RESISTIVITY OF REEL (AS PER EIA-541) min: 10^8 ohm/SQUARE; MAX 10^{12} ohm/SQUARE
3. REEL MUST ALSO MEET REQUIREMENTS OF EIA STANDARD # EIA-481A, TAPING OF SURFACE-MOUNT COMPONENTS FOR AUTOMATIC PLACEMENT.
4. THE BOX OF PACKING MUST CONTAIN THE REELS INSIDE AN ANTI-STATIC BAG.
5. \odot CRITICAL

International
IR Rectifier

D2Pak is MSL1 qualified.

This product has been designed and qualified for the Automotive [Q100] market.

Qualification standards can be found at www.irf.com

IR WORLD HEADQUARTERS: 233 Kansas St., El Segundo, California 90245 Tel: (310) 252-7105
Data and specifications subject to change without notice. 9/6/2006

Note: For the most current drawings please refer to the IR website at:
<http://www.irf.com/package/>