TOSHIBA CMOS Digital Integrated Circuit Silicon Monolithic

# TC7SG17FU

#### Schmitt Buffer

#### **Features**

• High-level output current:  $I_{OH}/I_{OL} = \pm 8$  mA (min) at  $V_{CC} = 3.0$  V

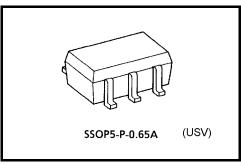
High-speed operation:  $t_{pd} = 2.4 \text{ ns (typ.)}$ 

at  $V_{CC} = 3.3 \text{ V},15\text{pF}$ 

Operating voltage range: V<sub>CC</sub> = 0.9~3.6 V

• 5.5-V tolerant inputs.

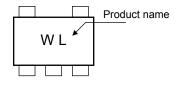
• 3.6-V power down protection output.

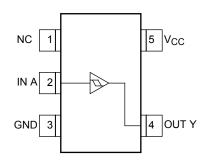


Weight: 0.006 g (typ.)

#### Marking

## Pin Assignment (top view)





## Absolute Maximum Ratings (Ta = 25°C)

Characteristics	Symbol	Value	Unit				
Power supply voltage	V <sub>CC</sub>	-0.5~4.6	V				
DC input voltage	V <sub>IN</sub>	-0.5~7.0	V				
DC output voltage	V	−0.5~ 4.6 (Note 1)	V				
DC output voltage	V <sub>OUT</sub>	-0.5~ V <sub>CC</sub> + 0.5 (Note 2)	V				
Input diode current	I <sub>IK</sub>	-20	mA				
Output diode current	lok	-20 (Note 3)	mA				
DC output current	lout	±25	mA				
DC V <sub>CC</sub> /ground current	Icc	±50	mA				
Power dissipation	PD	200	mW				
Storage temperature	T <sub>stg</sub>	-65~150	°C				

Note:

Using continuously under heavy loads (e.g. the application of high temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the reliability significantly even if the operating conditions (i.e. operating temperature/current/voltage, etc.) are within the absolute maximum ratings and the operating ranges.

Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook ("Handling Precautions"/Derating Concept and Methods) and individual reliability data (i.e. reliability test report and estimated failure rate, etc).

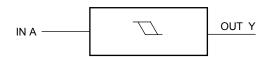
Note 1:  $V_{CC} = 0V$ 

Note 2: High or Low State. I<sub>OUT</sub> abusolute maximum rating must be observed.

Note 3: V<sub>OUT</sub> < GND

# **IEC Logic Symbol**

## **Truth Table**



Α	Y
L	L
Н	Н

## **Operating Range**

Characteristics	Symbol	Value	Unit	
Power supply voltage	V <sub>CC</sub>	. 0.9~3.6	V	
Input voltage	V <sub>IN</sub>	0~5.5	V	
Output voltage	V	0~3.6 (Note 4)	V	
	V <sub>OUT</sub>	0~V <sub>CC</sub> (Note 5)	V	
Output Current	I <sub>OH</sub> /I <sub>OL</sub>	±8.0 (Note 6)		
		±4.0 (Note 7)		
		±3.0 (Note 8)	m ^	
		±1.7 (Note 9)	mA	
		±0.3 (Note 10)		
		±0.02 (Note 11)		
Operating temperature	T <sub>opr</sub>	-40~85	°C	

Note 4:  $V_{CC} = 0.0 \text{ V}$ 

Note 5: High or Low state

Note 6:  $V_{CC} = 3.0 \sim 3.6 \text{ V}$ 

Note 7:  $V_{CC} = 2.3 \sim 2.7 \text{ V}$ 

Note 8:  $V_{CC} = 1.65 \sim 1.95 \text{ V}$ 

Note 9: V<sub>CC</sub> = 1.4~1.6 V

Note 10:  $V_{CC} = 1.1 \sim 1.3 \text{ V}$ 

Note 11:  $V_{CC} = 0.9 \text{ V}$ 

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## **Electrical Characteristics**

## **DC Characteristics**

Characteristics S		O. mak al	T4	Test Condition V <sub>CC</sub> (V)		Ta = 25°C			Ta = -40~85°C		Lloit
		Symbol	Test			Min	Тур.	Max	Min	Max	Unit
High level					_	_	0.73	_	0.80		
				1.1	_	_	0.86	_	0.93		
					_	_	1.07	_	1.12		
	V <sub>P</sub>	_		1.65	_	_	1.23	_	1.25		
				2.3	_	_	1.66	_	1.68		
				3.0	_	_	2.14	_	2.15		
Threshold voltage					0.9	0.18	_	_	0.07	_	V
					1.1	0.26	_	_	0.18	_	
		.,			1.4	0.36	_	_	0.31	_	
	Low level	V <sub>N</sub>		_		0.45	_	_	0.41	_	
					2.3	0.69	_	_	0.64	_	
					3.0	0.96	_	_	0.91	_	
					0.9	0.20	_	0.38	0.15	0.53	
						0.25		0.41	0.21	0.53	v
Hysteresis vo			_		1.4	0.35		0.48	0.34	0.57	
Hysteresis vo	niage	VH			1.65	0.42	_	0.56	0.40	0.60	
					2.3	0.60		0.74	0.61	0.76	
					3.0	0.79	_	0.93	0.80	0.94	
		vel V <sub>OH</sub>	V <sub>IN</sub> = V <sub>IH</sub>	I <sub>OH</sub> =-0.02 mA	0.9	0.75	_	_	0.75	_	
				$I_{OH} = -0.3 \text{ mA}$	1.1~1.3	V <sub>CC</sub> × 0.75	_	_	V <sub>CC</sub> × 0.75	_	
	High level			I <sub>OH</sub> = -1.7 mA	1.4~1.6	V <sub>CC</sub> × 0.75			V <sub>CC</sub> × 0.75	_	
				$I_{OH} = -3.0 \text{ mA}$	1.65~ 1.95	V <sub>CC</sub> -0.45	_	_	V <sub>CC</sub> -0.45	_	
				$I_{OH} = -4.0 \text{ mA}$	2.3~2.7	2.0	_	_	2.0	_	
Output voltage				$I_{OH} = -8.0 \text{ mA}$	3.0~3.6	2.48	_	_	2.48	_	
		vel V <sub>OL</sub>		$I_{OL} = 0.02 \text{ mA}$	0.9	—	_	0.1	_	0.1	-
Low level				I <sub>OL</sub> = 0.3 mA	1.1~1.3	_	_	V <sub>CC</sub> × 0.25	_	V <sub>CC</sub> × 0.25	
	Low level		$V_{IN} = V_{IL}$	I <sub>OL</sub> = 1.7 mA	1.4~1.6	_	_	V <sub>CC</sub> × 0.25	_	V <sub>CC</sub> × 0.25	
			ļ.	I <sub>OL</sub> = 3.0 mA	1.65~ 1.95	_	_	0.45	_	0.45	
				$I_{OL} = 4.0 \text{ mA}$	2.3~2.7	_	_	0.4	_	0.4	
				$I_{OL} = 8.0 \text{ mA}$	3.0~3.6	_	_	0.4	_	0.4	
Input leakage curre	Input leakage current $I_{IN}$ $V_{IN} = 0~5.5V$		0~3.6	_	_	±0.1		±1.0	μΑ		
Power off leakage current IOFF		V <sub>IN</sub> = 0~5 V <sub>OUT</sub> = 0	V <sub>IN</sub> = 0~5.5V V <sub>OUT</sub> = 0~3.6V		_	—	1.0	_	10.0	μΑ	
Quiescent supply current I <sub>CC</sub> V <sub>IN</sub> =		$V_{IN} = V_{CC}$	or GND	3.6	_	_	1.0	_	10.0	μΑ	

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# AC Characteristics (Input: $t_r = t_f = 3 \text{ ns}$ )

Characteristics	Symbol	Test Condition		Ta = 25°C		Ta = -40~85°C		Unit	
Characteristics			V <sub>CC</sub> (V)	Min	Тур.	Max	Min	Max	Offic
		$C_L = 10 \text{ pF},$ $R_L = 1 \text{ M}\Omega$	0.9	_	27.3	_	_	_	
			1.1~1.3		13.0	22.6	1.0	35.9	
			1.4~1.6	_	7.5	10.5	1.0	11.3	ns
			1.65~ 1.95		6.0	7.8	1.0	8.2	
			2.3~2.7		4.3	5.4	1.0	5.8	
			3.0~3.6	_	3.5	4.4	1.0	4.6	
			0.9	_	29.5	_	_	_	
	<sup>†</sup> pLH <sup>†</sup> pHL		1.1~1.3	_	14.3	25.1	1.0	41.8	
Propagation delay time		$C_L = 15 \text{ pF},$ $R_L = 1 \text{ M}\Omega$	1.4~1.6	_	8.0	11.5	1.0	12.6	
			1.65~ 1.95		6.3	8.4	1.0	8.7	
			2.3~2.7	_	4.6	5.7	1.0	6.1	
			3.0~3.6		3.7	4.6	1.0	5.0	
		$C_L = 30 \text{ pF},$ $R_L = 1 \text{ M}\Omega$	0.9	_	40.5	_	_	_	
			1.1~1.3	_	19.6	35.7	1.0	58.1	
			1.4~1.6	_	10.7	15.8	1.0	17.6	
			1.65~ 1.95		7.8	10.7	1.0	11.7	
			2.3~2.7	_	5.4	6.9	1.0	8.1	
			3.0~3.6	_	4.3	5.2	1.0	6.1	
Input capacitance	C <sub>IN</sub>	_	3.6	_	3	_	_	_	pF
Power dissipation capacitance	C <sub>PD</sub>	(Note 12)	0.9 ~ 3.6	_	7	_	_	_	pF

Note 12: C<sub>PD</sub> is defined as the value of the internal equivalent capacitance which is calculated from the operating current consumption without load.

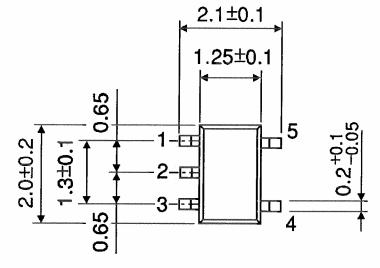
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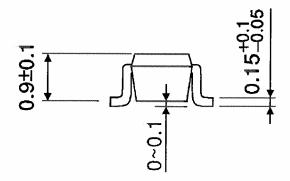
Average operating current can be obtained by the equation:

 $I_{CC \text{ (opr.)}} = C_{PD} \cdot V_{CC} \cdot f_{IN} + I_{CC}$ 

# **Package Dimensions**

SSOP5-P-0.65A Unit: mm





Weight: 0.006 g (typ.)

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20070701-EN GENERAL

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