TOSHIBA Field Effect Transistor Silicon N-Channel MOS Type (U-MOS III)

# 2SK4033

# Chopper Regulator, DC/DC Converter and Motor Drive Applications

4 V gate drive

• Low drain-source ON-resistance :  $R_{DS(ON)} = 0.07 \Omega$  (typ.)

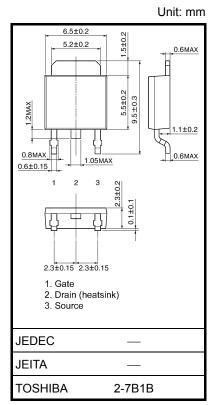
• High forward transfer admittance :  $|Y_{fs}| = 6.0 \text{ S (typ.)}$ 

Low leakage current : I<sub>DSS</sub> = 100 μA (max) (V<sub>DS</sub> = 60 V)

• Enhancement mode :  $V_{th} = 0.8 \sim 2.0 \text{ V} (V_{DS} = 10 \text{ V}, I_D = 1 \text{ mA})$ 

#### Absolute Maximum Ratings (Ta = 25°C)

Character	istic	Symbol	Rating	Unit
Drain-source voltage		$V_{DSS}$	60	V
Drain-gate voltage (R <sub>GS</sub> = 20 kΩ)		$V_{DGR}$	60	V
Gate-source voltage		$V_{GSS}$	±20	V
Drain current	DC (Note 1)	ID	5	Α
	Pulse (Note 1)	I <sub>DP</sub>	20	Α
Drain power dissipatio	n (Tc = 25°C)	PD	20	W
Single-pulse avalanch	e energy (Note 2)	E <sub>AS</sub>	40.5	mJ
Avalanche current		I <sub>AR</sub>	5	Α
Repetitive avalanche	energy (Note 3)	E <sub>AR</sub>	2	mJ
Channel temperature		T <sub>ch</sub>	150	°C
Storage temperature r	ange	T <sub>stg</sub>	-55~150	°C



Weight: 0.36 g (typ.)

Note: Using continuously under heavy loads (e.g. the application of high temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the reliability significantly even if the operating conditions (i.e. operating temperature/current/voltage, etc.) are within the absolute maximum ratings. Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook ("Handling Precautions"/Derating Concept and Methods) and individual reliability data (i.e. reliability test report and estimated failure rate, etc).

#### **Thermal Characteristics**

Characteristic	Symbol	Max	Unit	
Thermal resistance, channel to case	R <sub>th (ch-c)</sub>	6.25	°C/W	
Thermal resistance, channel to ambient	R <sub>th (ch-a)</sub>	125	°C/W	

Note 1: Ensure that the channel temperature does not exceed 150°C.

Note 2:  $V_{DD}$  = 25 V,  $T_{ch}$  = 25 °C (initial), L = 2.2 mH,  $R_{G}$  = 25  $\Omega$ ,  $I_{AR}$  = 5 A

Note 3: Repetitive rating: pulse width limited by maximum channel temperature

This transistor is an electrostatic-sensitive device. Handle with care.



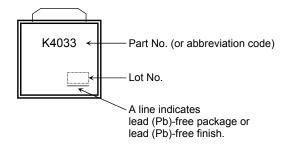
## **Electrical Characteristics (Ta = 25°C)**

Charac	cteristic	Symbol	Test Condition	Min	Тур.	Max	Unit
Gate leakage cu	ırrent	I <sub>GSS</sub>	V <sub>GS</sub> = ±16 V, V <sub>DS</sub> = 0 V	_	_	±10	μА
Drain cutoff curr	ent	I <sub>DSS</sub>	V <sub>DS</sub> = 60 V, V <sub>GS</sub> = 0 V	_	_	100	μА
Drain-source br voltage	eakdown	V (BR) DSS	I <sub>D</sub> = 10 mA, V <sub>GS</sub> = 0 V	60	_	_	٧
Gate threshold v	voltage	V <sub>th</sub>	V <sub>DS</sub> = 10 V, I <sub>D</sub> = 1 mA	1.3	_	2.5	V
Drain-source ON-resistance		Pro (OV)	V <sub>GS</sub> = 4 V, I <sub>D</sub> = 2.5 A	_	0.09	0.15	Ω
		R <sub>DS</sub> (ON)	V <sub>GS</sub> = 10 V, I <sub>D</sub> = 2.5 A	_	0.07	0.10	
Forward transfer	r admittance	Y <sub>fs</sub>	V <sub>DS</sub> = 10 V, I <sub>D</sub> = 2.5 A	3.0	6.0	_	S
Input capacitano	e	C <sub>iss</sub>			730	_	
Reverse transfer capacitance		C <sub>rss</sub>	V <sub>DS</sub> = 10 V, V <sub>GS</sub> = 0 V, f = 1 MHz		60	_	pF
Output capacitance		Coss			95	_	
Switching time	Rise time	t <sub>r</sub>	V <sub>cs</sub> <sub>OV</sub>	_	10	_	
	Turn-on time	t <sub>on</sub>		_	20	_	no
	Fall time	t <sub>f</sub>		_	4	_	ns
	Turn-off time	t <sub>off</sub>		_	35	_	
Total gate charge (gate-source plus gate-drain)		Qg	V <sub>DD</sub> ≈ 48 V, V <sub>GS</sub> = 10 V, I <sub>D</sub> = 5 A		15		
Gate-source charge		Q <sub>gs</sub>		_	11	_	nC
Gate-drain ("Miller") charge		Q <sub>gd</sub>		_	4	_	

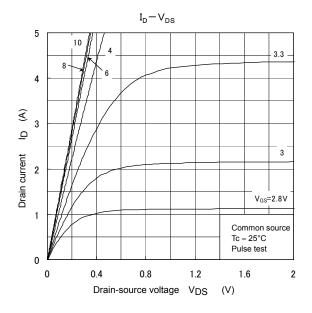
### Source-Drain Ratings and Characteristics (Ta = 25°C)

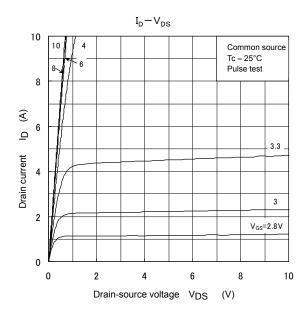
Characteristic	Symbol	Test Condition	Min	Тур.	Max	Unit
Continuous drain reverse current (Note 1)	I <sub>DR</sub>	_	_	_	5	А
Pulse drain reverse current (Note 1)	I <sub>DRP</sub>	_	_	_	20	Α
Forward voltage (diode)	$V_{DSF}$	I <sub>DR</sub> = 5 A, V <sub>GS</sub> = 0 V	_	_	-1.7	V
Reverse recovery time	t <sub>rr</sub>	$I_{DR}$ = 5 A, $V_{GS}$ = 0 V, $dI_{DR}$ / $dt$ = 50 A / $\mu$ s	_	34	_	ns
Reverse recovery charge	Q <sub>rr</sub>		_	28	_	μС

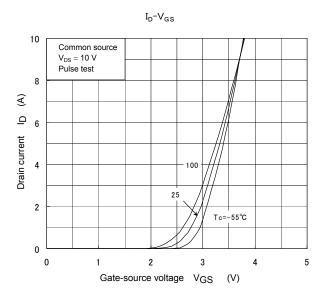
### Marking

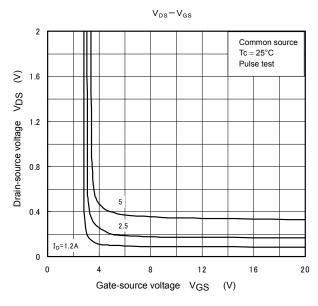


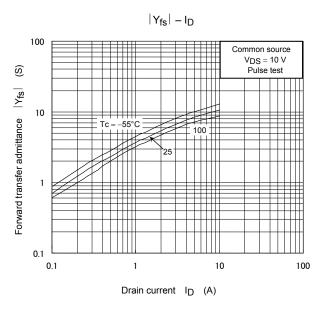
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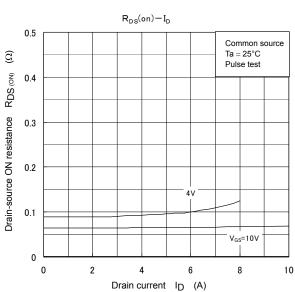




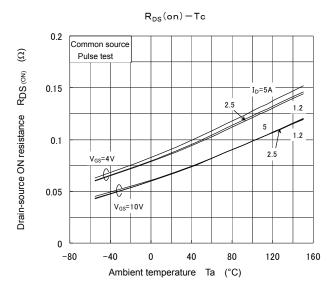


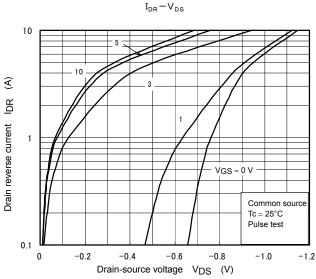


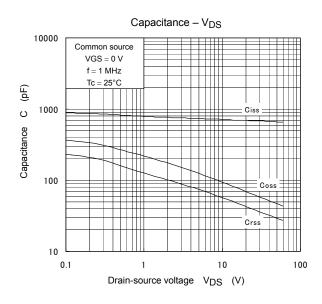


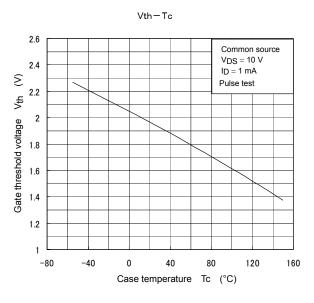


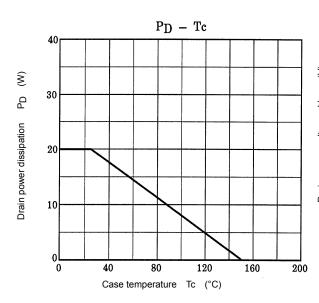
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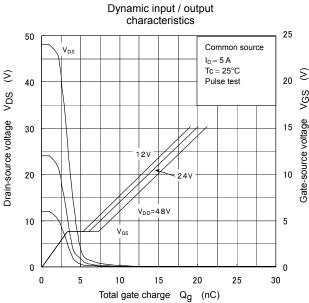


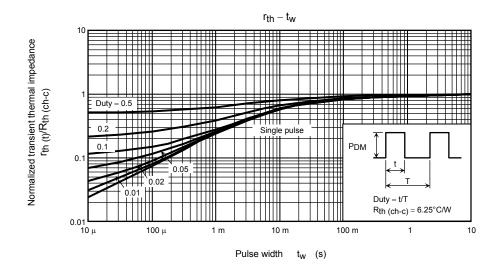




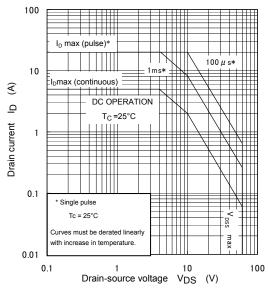


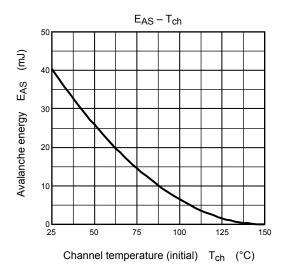












$$\begin{aligned} R_G &= 25~\Omega \\ V_{DD} &= 25~V,~L = 2.2~mH \end{aligned}$$

$$EAS = \frac{1}{2} \cdot L \cdot I^2 \cdot \left( \frac{BVDSS}{BVDSS - VDD} \right)$$

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