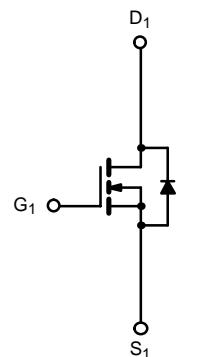
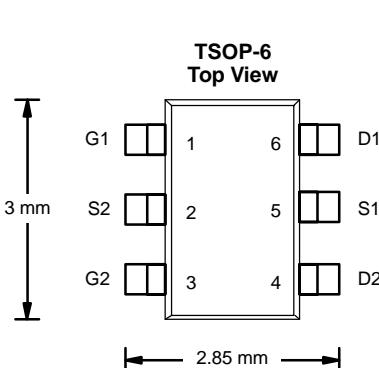


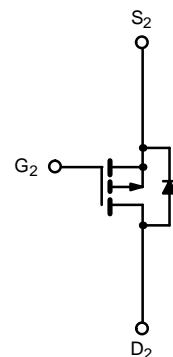
N- and P-Channel 20-V (D-S) MOSFET

PRODUCT SUMMARY			
	V_{DS} (V)	$r_{DS(on)}$ (Ω)	I_D (A)
N-Channel	20	0.125 @ $V_{GS} = 4.5$ V	2.4
		0.200 @ $V_{GS} = 2.5$ V	1.8
P-Channel	-20	0.200 @ $V_{GS} = -4.5$ V	-1.8
		0.340 @ $V_{GS} = -2.5$ V	-1.2

TrenchFET®
Power MOSFETs



N-Channel MOSFET



P-Channel MOSFET

ABSOLUTE MAXIMUM RATINGS ($T_A = 25^\circ\text{C}$ UNLESS OTHERWISE NOTED)

Parameter	Symbol	N-Channel		P-Channel		Unit
		10 secs	Steady State	10 secs	Steady State	
Drain-Source Voltage	V_{DS}		20		-20	
Gate-Source Voltage	V_{GS}		± 12		± 12	V
Continuous Drain Current ($T_J = 150^\circ\text{C}$) ^a	I_D	2.4	2.0	-1.8	-1.5	A
		1.7	1.4	-1.3	-1.2	
Pulsed Drain Current	I_{DM}	8		-7		
Continuous Source Current (Diode Conduction) ^a	I_S	1.05	0.75	-1.05	-0.75	
Maximum Power Dissipation ^a	P_D	1.15	0.83	1.15	0.83	W
		0.59	0.53	0.59	0.53	
Operating Junction and Storage Temperature Range	T_J, T_{stg}	-55 to 150				°C

THERMAL RESISTANCE RATINGS

Parameter	Symbol	N-Channel		P-Channel		Unit
		Typ	Max	Typ	Max	
Maximum Junction-to-Ambient ^a	R_{thJA}	93	110	93	110	°C/W
		130	150	130	150	
Maximum Junction-to-Foot (Drain)	Steady State	R_{thJF}	75	90	75	90

Notes

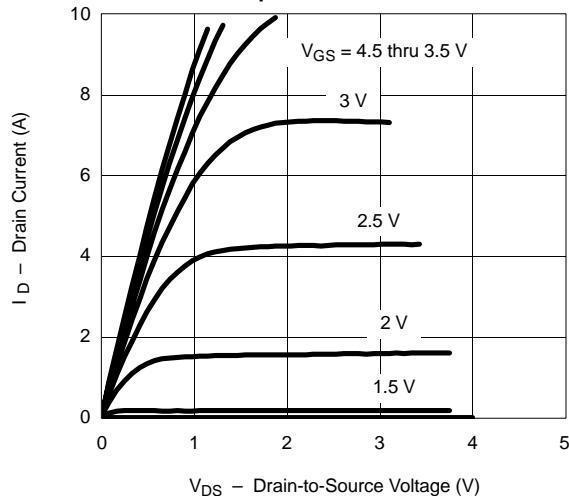
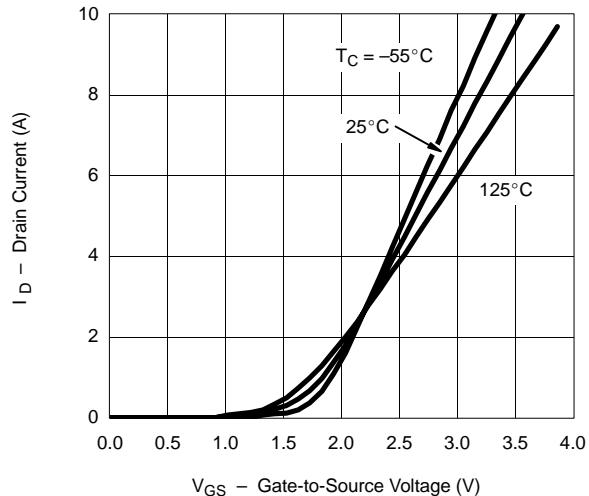
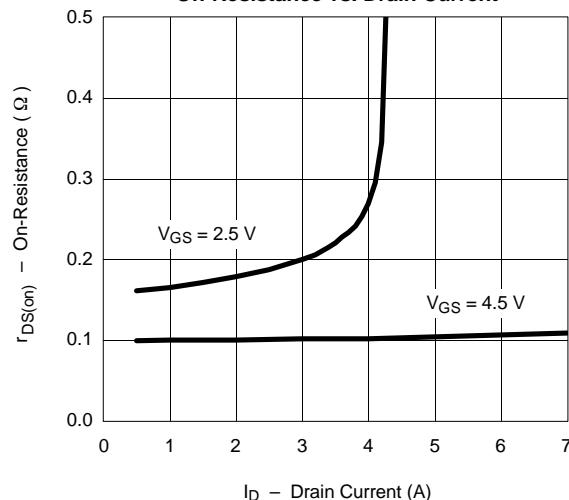
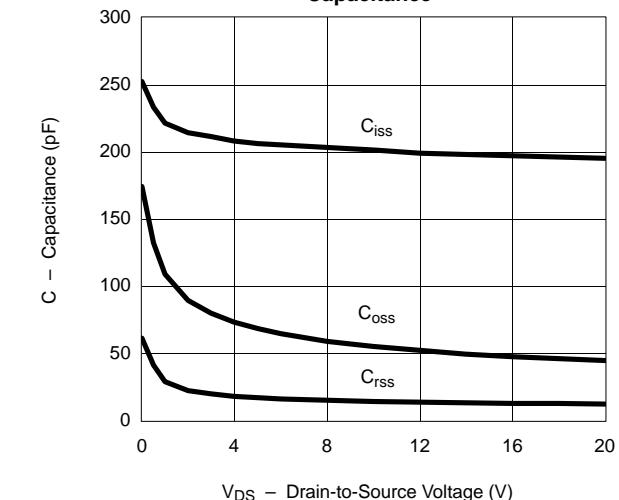
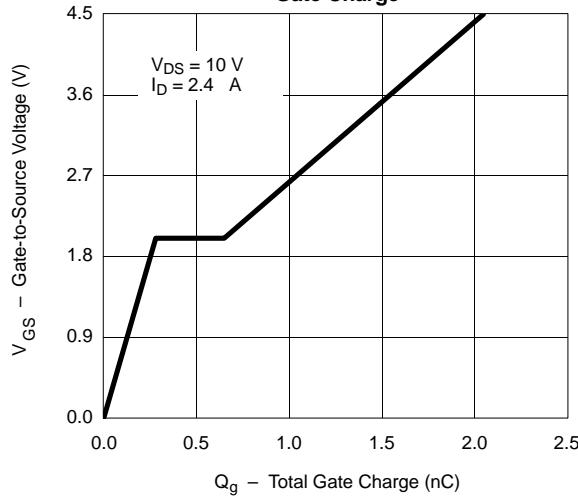
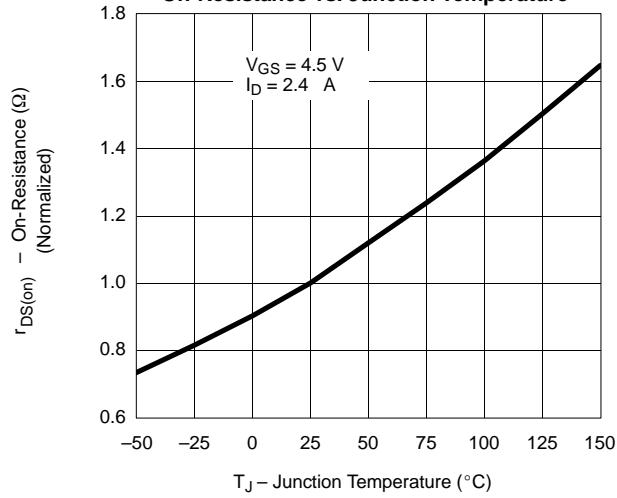
a. Surface Mounted on 1" x 1" FR4 Board.

SPECIFICATIONS ($T_J = 25^\circ\text{C}$ UNLESS OTHERWISE NOTED)

Parameter	Symbol	Test Condition	Min	Typ	Max	Unit	
Static							
Gate Threshold Voltage	$V_{GS(\text{th})}$	$V_{DS} = V_{GS}, I_D = 250 \mu\text{A}$	N-Ch	0.6			
		$V_{DS} = V_{GS}, I_D = -250 \mu\text{A}$	P-Ch	-0.5		V	
Gate-Body Leakage	I_{GSS}	$V_{DS} = 0 \text{ V}, V_{GS} = \pm 12 \text{ V}$	N-Ch P-Ch		± 100 ± 100	nA	
Zero Gate Voltage Drain Current	I_{DSS}	$V_{DS} = 16 \text{ V}, V_{GS} = 0 \text{ V}$	N-Ch		1		
		$V_{DS} = -16 \text{ V}, V_{GS} = 0 \text{ V}$	P-Ch		-1	μA	
		$V_{DS} = 16 \text{ V}, V_{GS} = 0 \text{ V}, T_J = 55^\circ\text{C}$	N-Ch		5		
		$V_{DS} = -16 \text{ V}, V_{GS} = 0 \text{ V}, T_J = 55^\circ\text{C}$	P-Ch		-5		
On-State Drain Current ^a	$I_{D(\text{on})}$	$V_{DS} \geq 5 \text{ V}, V_{GS} = 4.5 \text{ V}$	N-Ch	5		A	
		$V_{DS} \leq -5 \text{ V}, V_{GS} = -4.5 \text{ V}$	P-Ch	-5			
Drain-Source On-State Resistance ^a	$r_{DS(\text{on})}$	$V_{GS} = 4.5 \text{ V}, I_D = 2.4 \text{ A}$	N-Ch		0.100	0.125	
		$V_{GS} = -4.5 \text{ V}, I_D = -1.8 \text{ A}$	P-Ch		0.160	0.200	
		$V_{GS} = 2.5 \text{ V}, I_D = 1.8 \text{ A}$	N-Ch		0.160	0.200	
		$V_{GS} = -2.5 \text{ V}, I_D = -1.2 \text{ A}$	P-Ch		0.280	0.340	
Forward Transconductance ^a	g_{fs}	$V_{DS} = 5 \text{ V}, I_D = 2.4 \text{ A}$	N-Ch		5		
		$V_{DS} = -5 \text{ V}, I_D = -1.8 \text{ A}$	P-Ch		3.6	S	
Diode Forward Voltage ^a	V_{SD}	$I_S = 1.05 \text{ A}, V_{GS} = 0 \text{ V}$	N-Ch		0.80	1.10	
		$I_S = -1.05 \text{ A}, V_{GS} = 0 \text{ V}$	P-Ch		-0.83	-1.10	
Dynamic^b							
Total Gate Charge	Q_g	N-Channel $V_{DS} = 10 \text{ V}, V_{GS} = 4.5 \text{ V}, I_D = 2.4 \text{ A}$ P-Channel $V_{DS} = -10 \text{ V}, V_{GS} = -4.5 \text{ V}, I_D = -1.8 \text{ A}$	N-Ch P-Ch		2.1 2.7	3.2 4.0	nC
Gate-Source Charge	Q_{gs}		N-Ch P-Ch		0.3 0.4		
Gate-Drain Charge	Q_{gd}		N-Ch P-Ch		0.4 0.6		
Turn-On Delay Time	$t_{d(\text{on})}$		N-Ch P-Ch		10 11	17	
Rise Time	t_r	N-Channel $V_{DD} = 10 \text{ V}, R_L = 10 \Omega$ $I_D \approx 1 \text{ A}, V_{GEN} = 4.5 \text{ V}, R_G = 6 \Omega$ P-Channel $V_{DD} = -10 \text{ V}, R_L = 10 \Omega$ $I_D \approx -1 \text{ A}, V_{GEN} = -4.5 \text{ V}, R_G = 6 \Omega$	N-Ch P-Ch		30 34	50 50	ns
Turn-Off Delay Time	$t_{d(\text{off})}$		N-Ch P-Ch		14 19	25 30	
Fall Time	t_f		N-Ch P-Ch		6 24	12 36	
Source-Drain Reverse Recovery Time	t_{rr}		N-Ch P-Ch		30 20	50 40	

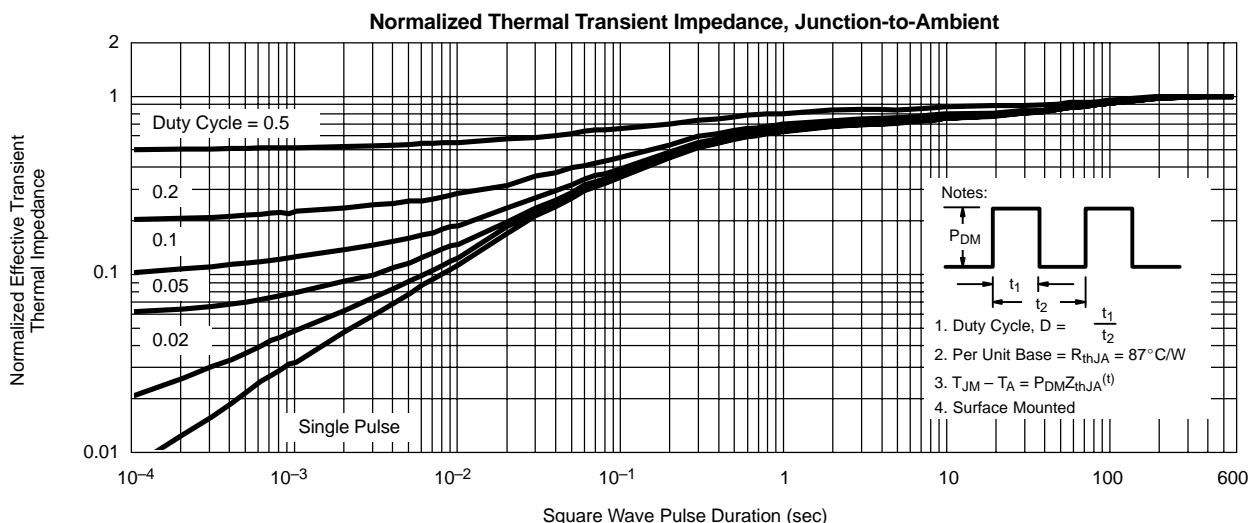
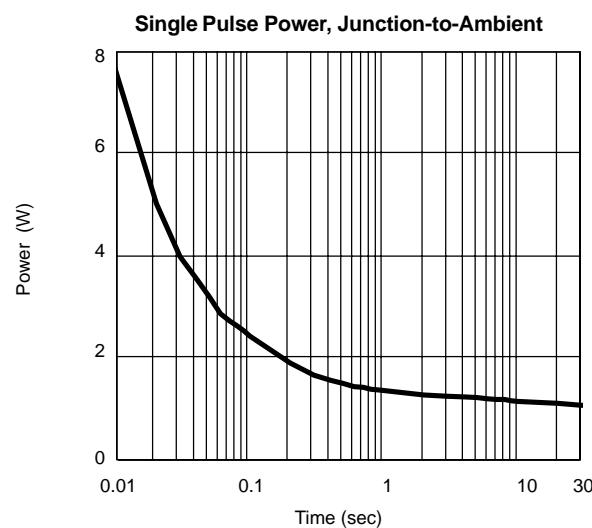
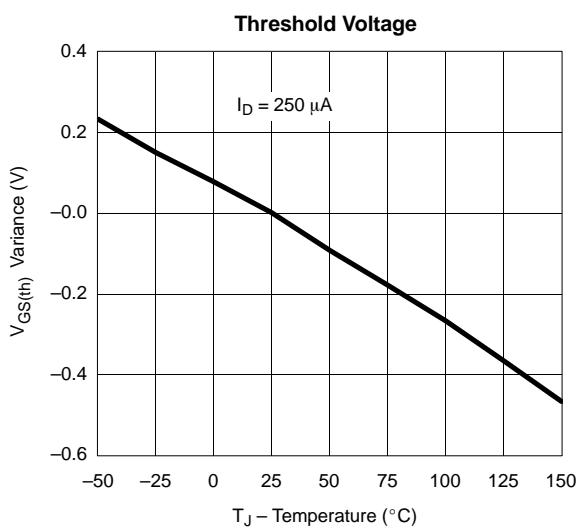
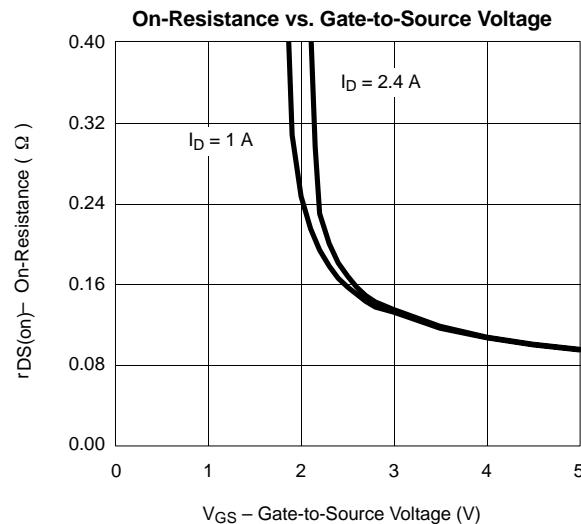
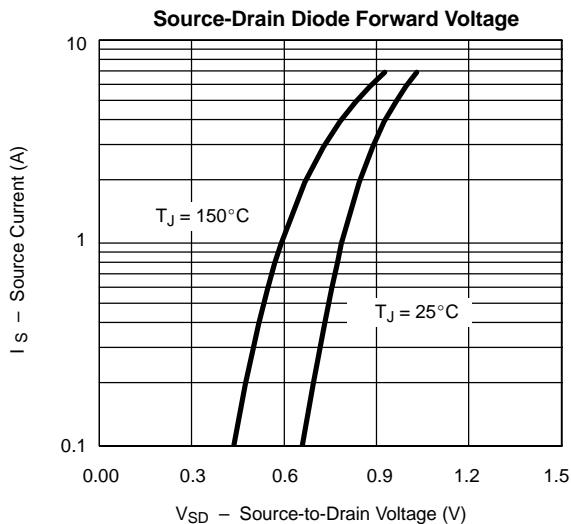
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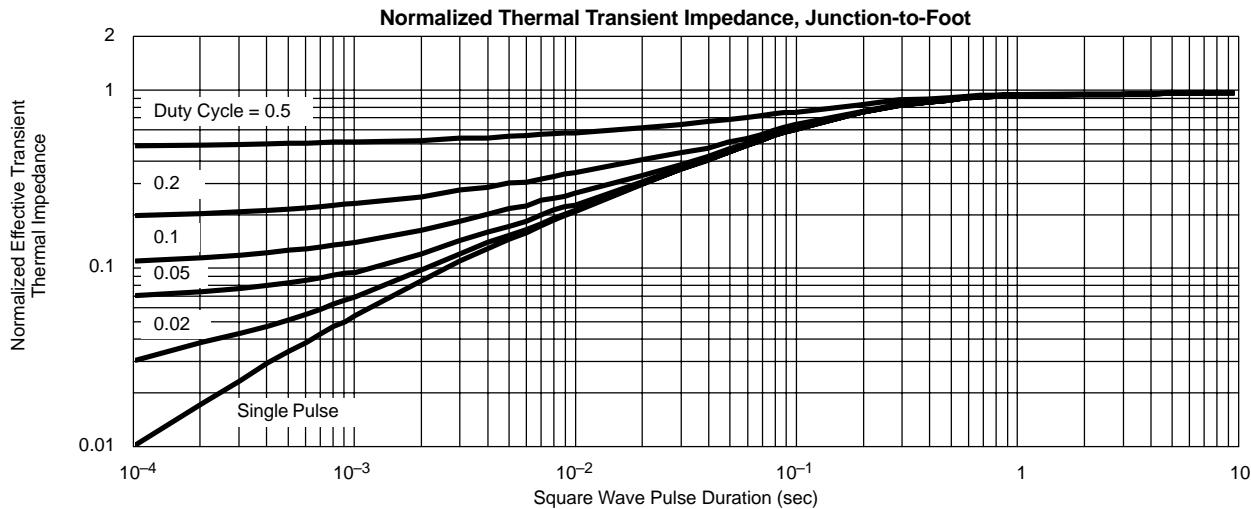
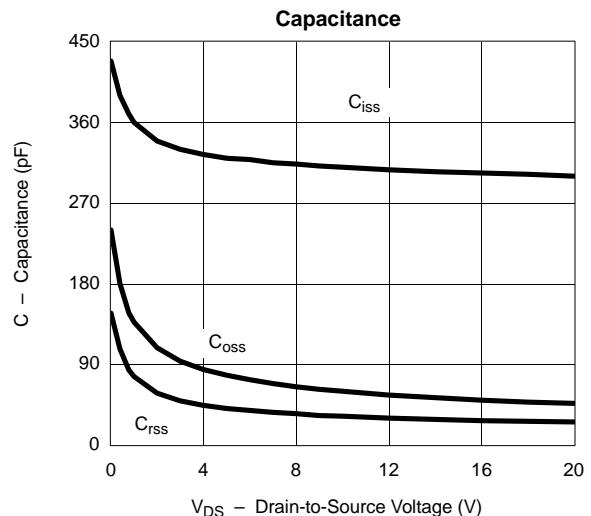
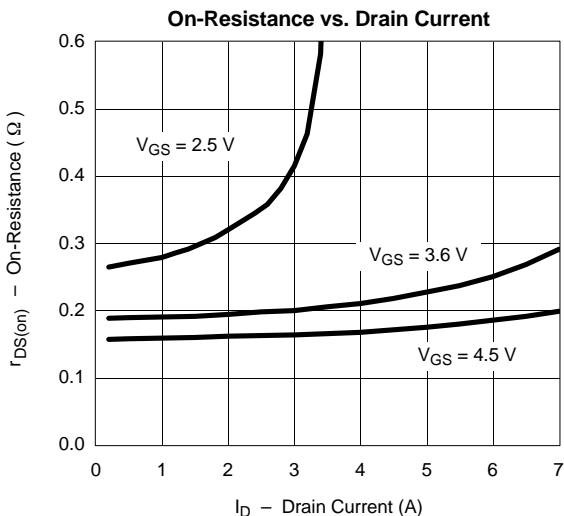
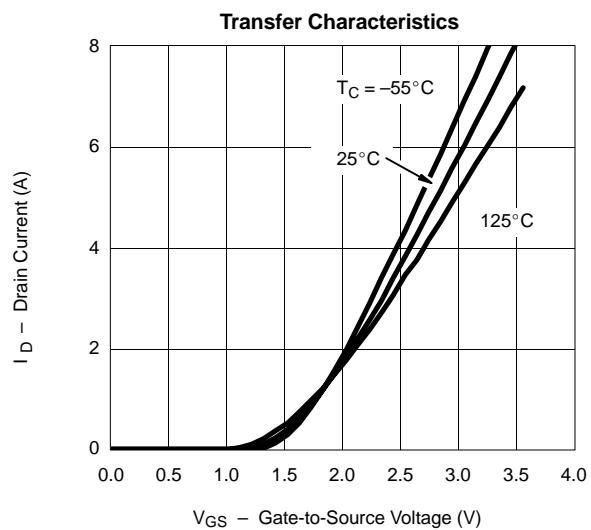
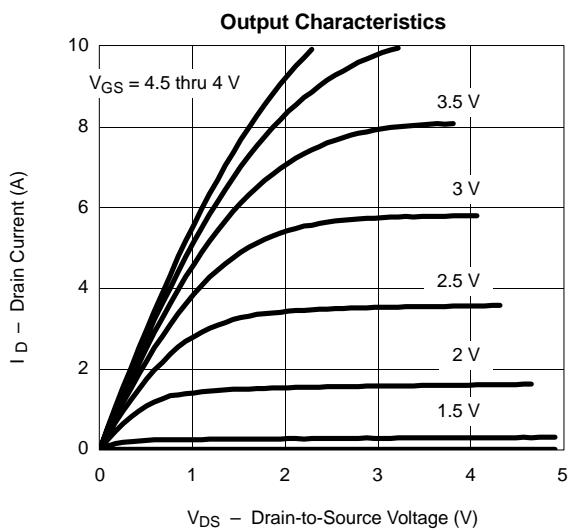
- a. Pulse test; pulse width $\leq 300 \mu\text{s}$, duty cycle $\leq 2\%$.
 b. Guaranteed by design, not subject to production testing.

TYPICAL CHARACTERISTICS (25°C UNLESS NOTED)
N-CHANNEL
Output Characteristics

Transfer Characteristics

On-Resistance vs. Drain Current

Capacitance

Gate Charge

On-Resistance vs. Junction Temperature


TYPICAL CHARACTERISTICS (25°C UNLESS NOTED)

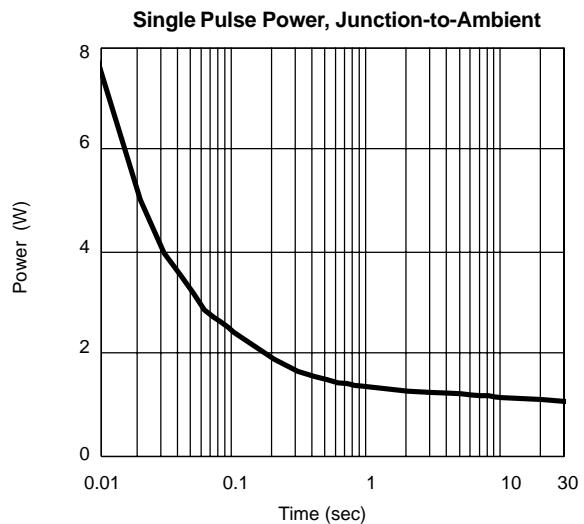
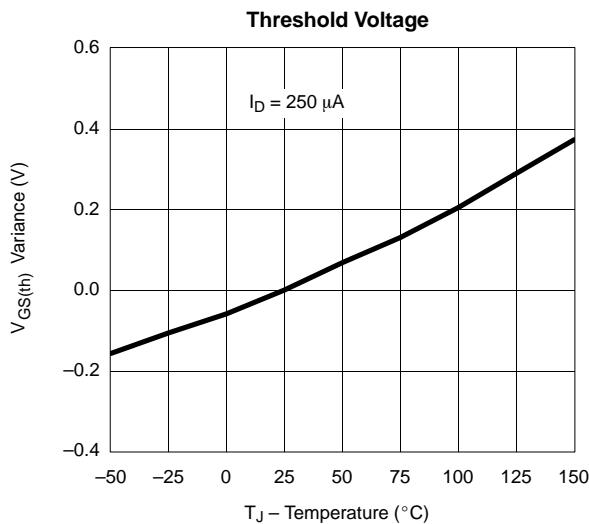
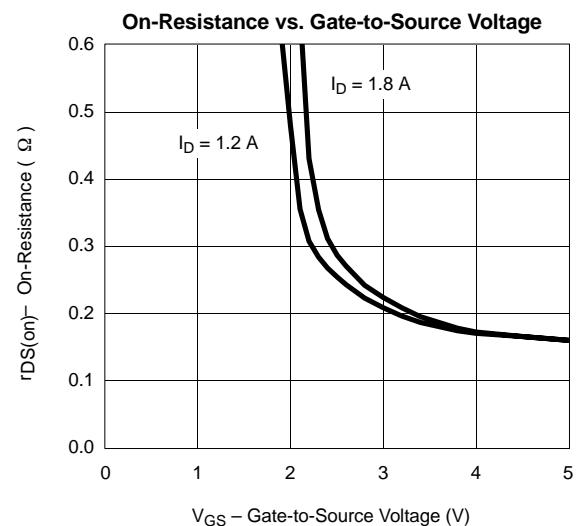
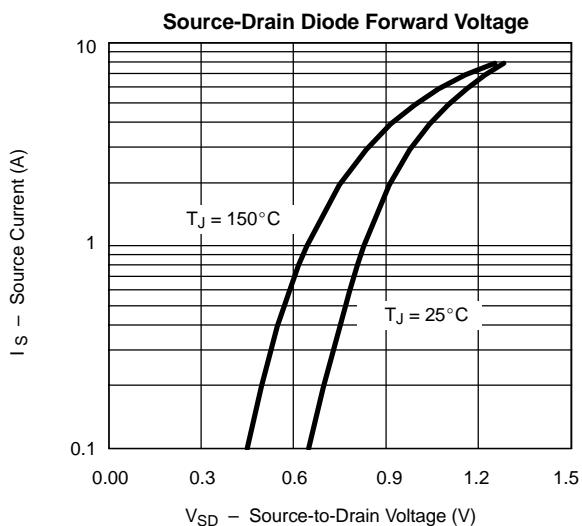
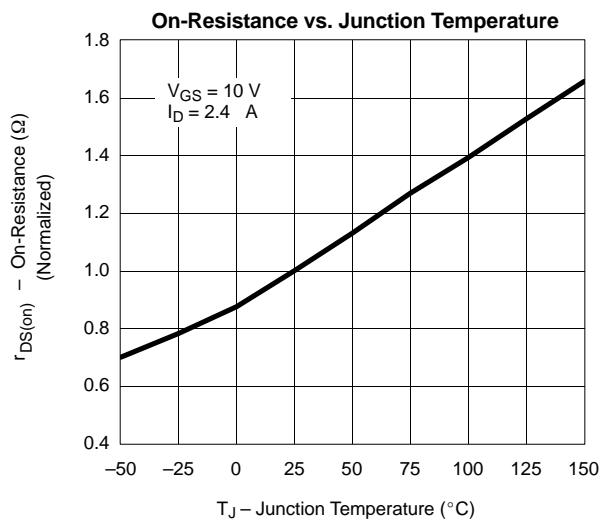
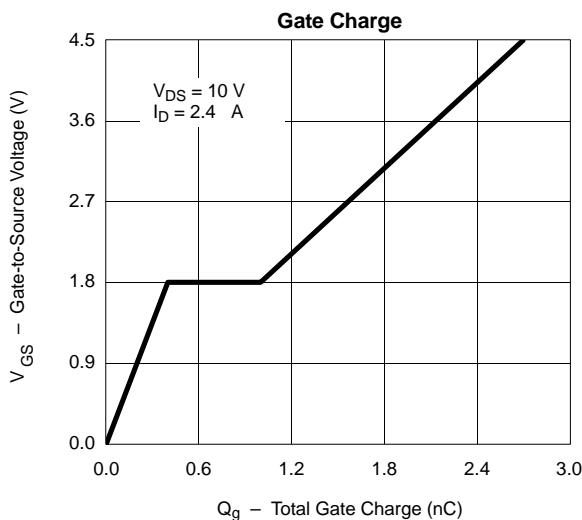
N-CHANNEL



TYPICAL CHARACTERISTICS (25°C UNLESS NOTED)
N-CHANNEL

TYPICAL CHARACTERISTICS (25°C UNLESS NOTED)
P-CHANNEL


TYPICAL CHARACTERISTICS (25°C UNLESS NOTED)

P-CHANNEL



TYPICAL CHARACTERISTICS (25°C UNLESS NOTED)
P-CHANNEL
