

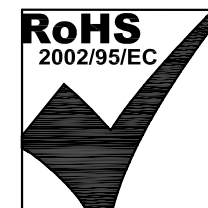
ELECTRICAL SPECIFICATIONS:

- | | | |
|-----|--|--|
| 1.0 | URNS RATIO: (P8-P6-P7) : (J6-J3) | : 1CT : 1 \pm 3% |
| | (P2-P3-P1) : (J2-J1) | : 1CT : 1 \pm 3% |
| 2.0 | INDUCTANCE: (P7-P8) | : 350uH MIN. @ 0.1V, 100KHz, 8mA DC Bias |
| | (P1-P2) | : 350uH MIN. @ 0.1V, 100KHz, 8mA DC Bias |
| 3.0 | LEAKAGE INDUCTANCE: P8-P7 (WITH J6 AND J3 SHORT) | : 0.3 MAX. @ 1MHz |
| | P2-P1 (WITH J2 AND J1 SHORT) | : 0.3 MAX. @ 1MHz |
| 4.0 | INTERWINDING CAPACITANCE: (P8,P6,P7) TO (J6,J3) | : 30pf MAX @ 1MHz |
| | (P3,P2,P1) TO (J2,J1) | : 30pf MAX. @ 1MHz |
| 5.0 | DC RESISTANCE: (J1-J2) | : 1.2 ohms Max. |
| | (P8-P7) | : 1.2 ohms Max. |

NOTES

1.0 PINS WITHOUT ELECTRICAL CONNECTION ARE OMITTED.

2.0 ALL RESISTORS ARE \pm 5% TOLERANCE.



Bel Stewart Connector
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Glen Rock, Pa 17327-9199
717.234.7512

MagJack®

<http://www.stewartconnector.com>

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SHEET 1 OF 4

DRAWING NO. SI-60120-F

REV. 13

RECEIVE

6.0 RETURN LOSS: 1MHz TO 30MHz	: -18dB MIN.
30MHz TO 80MHz	: $-(19-20 \text{ LOG } (f/30\text{MHz}))$
60MHz TO 80MHz	: -12dB MIN.
7.0 DIELECTRIC WITHSTAND: (J1, J2) TO (P1, P3)	: 1500 VAC
(J3, J6) TO (P8,P7)	: 1500 VAC
8.0 INSERTION LOSS: RS=RL=100 ohms	
1MHz TO 65MHz	: -1.0 dB MAX
65MHz TO 100MHz	: -1.1 dB MAX
9.0 RISE TIME: RS=100 OHMS AND RL = 100 OHMS	
OUTPUT VOLTAGE = 1 V peak	: 3.0 nS MAX
PULSE WIDTH= 112nS	: 3.0 nS MAX
10.0 CROSS TALK: 1MHz TO 100MHz	: -35 dB TYP
11.0 COMMON TO COMMON MODE ATTENUATION: 1MHz TO 100MHz	: -45dB TYP
100MHz TO 200MHz	: -20dB MIN

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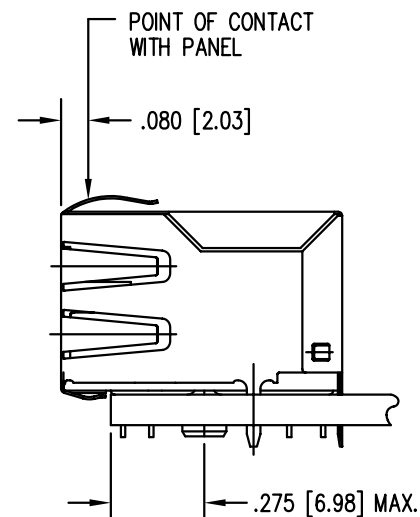
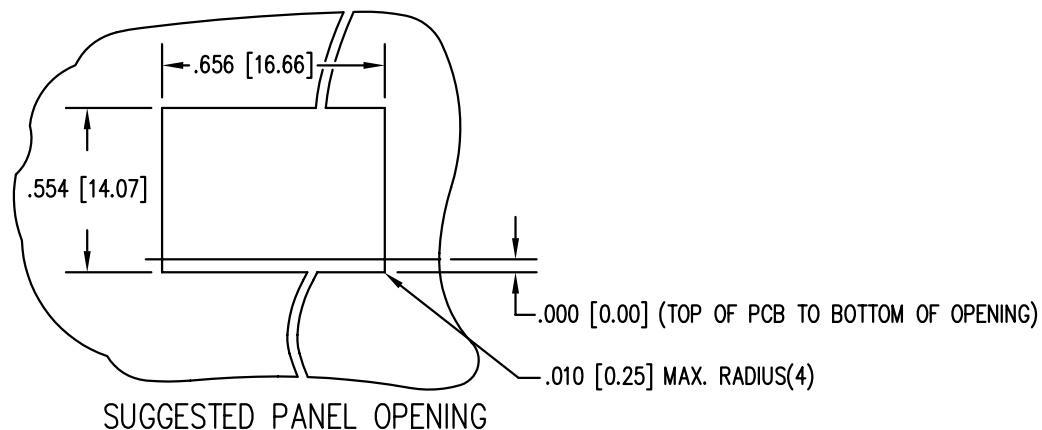
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SHEET 2 OF 4

DRAWING NO. SI-60120-F

REV. 13



1. THE SUGGESTED PANEL OPENING IS INTENDED TO GIVE THE USER THE ABILITY TO HAVE REASONABLE JACK / PANEL CLEARANCES YET MAINTAIN RELIABLE GROUNDING CAPABILITY.
2. ALL TOLERANCES NOT OTHERWISE SPECIFIED TO BE ± 0.005 [0.13]

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SHEET 4 OF 4

DRAWING NO.

SI-60120-F

REV. 02