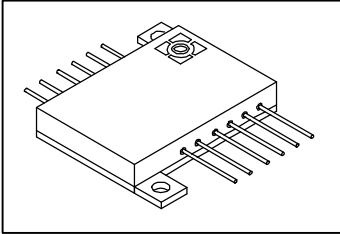




205 Crawford St. Leominster, MA 01453 (978)534-5776 Fax(978)537-4246, www.omnirel.com

OM9401SF



HERMETIC IGBT GATE DRIVER

For Driving IGBT Modules
600V and 1200V, 150A to 600A

FEATURES

- Rugged, Hermetic Package
- Built in Short Circuit Protection with Fault Output
- High CMRR Optocoupler
- CMOS/TTL Compatible Input Interface

DESCRIPTION

This series of hermetic gate drivers are ideally suited for driving the CERMOD™ family of IGBT Power Modules, where small size, high performance and high reliability are required. Designed specifically for high power motor controls, inverters, switching power supplies, choppers and high energy pulse circuits in the aerospace, defense, transportation, industrial and medical equipment industries.

ABSOLUTE MAXIMUM RATINGS (Tc=25°C unless otherwise noted)

Characteristic	Symbol	Min.	Typ.	Max.	UNIT
Operating Temperature	T _A	-55	-	125	°C
Peak Output Current	I _{O(PEAK)}	-	-	5	A
Logic Input Voltage	V _{CC}	-	5	5.25	V
Gate Positive Supply Voltage	+V _G		15	16	V
Gate Negative Supply Voltage	-V _G	-14	-	-0.5	V
Fault Output current	I _{FLT}	-	-	8	mA
Isolation Voltage	V _{is}			2000	Vdc



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ELECTRICAL CHARACTERISTICS: OM9401SF (Tc= 25°C unless otherwise specified)

Parameter	Test Condition	Min.	Typ.	Max	Unit
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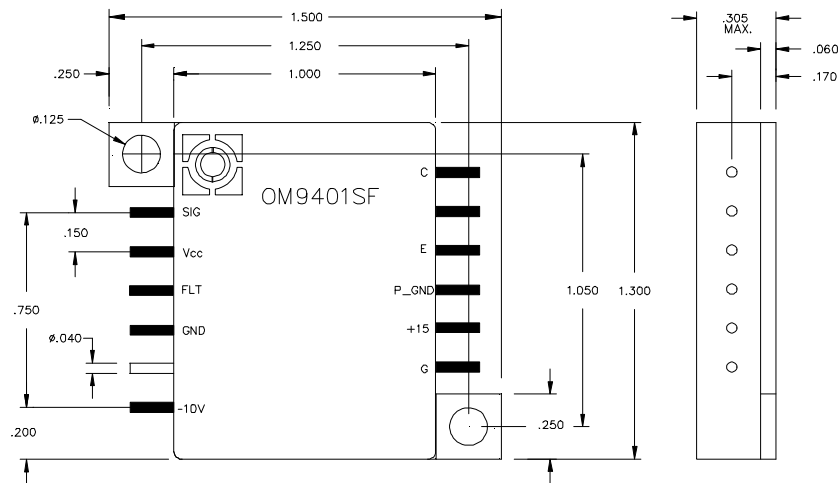
SWITCHING CHARACTERISTICS

t _{PLH} Signal to High Level Output Propagation Delay Time	RG=2.5Ω, CG=2200pF f _{PWM} =10kHz Duty=50%	0.15	0.30	0.55	μS
t _{PHL} Signal to Low Level Output Propagation Delay Time		0.15	0.32	0.55	μS
(t _{PHL} -t _{PLH}) Propagation Delay Difference Between Any Two Parts		-0.4	0.4		μS
t _r Rise Time			0.15		μS
t _f Fall Time			0.15		μS
I _{CM} Output Common Mode Transient Immunity	V _{CM} =1500V	15	25		kV/μS
V _C Desat. (Collector) Threshold		6.5	7.0	7.5	V
V _{SIG} Logic Low Signal Voltage				0.8	V
V _{SIG} Logic High Signal Voltage		2.0			V
I _{SIG} Logic Low Current	V _{IN} =0.4V	-0.5	-0.4		mA
V _{UVLO+} UVLO Threshold Positive	V _{OUT} >5V	11.5	13.0	13.6	V
V _{UVLO-} UVLO Threshold Negative	V _{OUT} <5V		11.8	12.4	V

SUPPLY VOLTAGES

V _{CC} Logic Input Voltage (Recommended)	4.75	5	5.25	V
+V _G Gate Positive Supply Voltage (Recommended)	14.5	15		V
-V _G Gate Negative Supply Voltage(Recommended)	-10		-8	V

MECHANICAL OUTLINE



APPLICATION CIRCUIT

